

## 6. Dynamic Programming II

- sequence alignment
- Hirschberg's algorithm
- Bellman-Ford
- distance vector protocols
- negative cycles in a digraph


## Shortest paths

Shortest path problem. Given a digraph $G=(V, E)$, with arbitrary edge weights or costs $c_{v w}$, find cheapest path from node $s$ to node $t$.


## Negative cycles

Def. A negative cycle is a directed cycle such that the sum of its edge weights is negative.

a negative cycle $\mathbf{W}: c(W)=\sum_{e \in W} c_{e}<0$

## Shortest paths and negative cycles

Lemma 1. If some path from $v$ to $t$ contains a negative cycle, then there does not exist a cheapest path from $v$ to $t$.

Pf. If there exists such a cycle $W$, then can build a $v \rightarrow t$ path of arbitrarily negative weight by detouring around cycle as many times as desired. .

$c(W)<0$

## Shortest paths and negative cycles

Lemma 2. If $G$ has no negative cycles, then there exists a cheapest path from $v$ to $t$ that is simple (and has $\leq n-1$ edges).

Pf.

- Consider a cheapest $v \rightarrow t$ path $P$ that uses the fewest number of edges.
- If $P$ contains a cycle $W$, can remove portion of $P$ corresponding to $W$ without increasing the cost. -

$c(W) \geq 0$


## Shortest paths: dynamic programming

Def. $O P T(i, v)=$ cost of shortest $v \rightarrow t$ path that uses $\leq i$ edges.

- Case 1: Cheapest $v \rightarrow t$ path uses $\leq i-1$ edges.
- OPT $(i, v)=\mathrm{OPT}(i-1, v)$
 (proof via exchange argument)
- Case 2: Cheapest $v \rightarrow t$ path uses exactly $i$ edges.
- if $(v, w)$ is first edge, then OPT uses $(v, w)$, and then selects best $w \rightarrow t$ path using $\leq i-1$ edges
$O P T(i, v)= \begin{cases}\infty & \text { if } \mathrm{i}=0 \\ \min \left\{O P T(i-1, v), \min _{(v, w) \in E}\left\{O P T(i-1, w)+c_{v w}\right\}\right\} & \text { otherwise }\end{cases}$

Observation. If no negative cycles, $O P T(n-1, v)=$ cost of cheapest $v \rightarrow t$ path.
Pf. By Lemma 2, cheapest $v \rightarrow t$ path is simple. -

## Shortest paths: implementation

```
Shortest-Paths (V,E,c,t)
FOREACH node v\inV
    M[0,v]}\leftarrow\infty
M[0,t]\leftarrow0.
FOR i = 1 TO n-1
    FOREACH node v\inV
        M[i,v]}\leftarrowM[i-1,v]
        FOREACH edge (v,w) \inE
            M[i,v]\leftarrow\operatorname{min}{M[i,v],M[i-1,w]+\mp@subsup{c}{vw}{}}.
```


## Shortest paths: implementation

Theorem 1. Given a digraph $G=(V, E)$ with no negative cycles, the dynamic programming algorithm computes the cost of the cheapest $v \rightarrow t$ path for each node $v$ in $\Theta(m n)$ time and $\Theta\left(n^{2}\right)$ space.

Pf.

- Table requires $\Theta\left(n^{2}\right)$ space.
- Each iteration $i$ takes $\Theta(m)$ time since we examine each edge once. -

Finding the shortest paths.

- Approach 1: Maintain a successor $(i, v)$ that points to next node on cheapest $v \rightarrow t$ path using at most $i$ edges.
- Approach 2: Compute optimal costs $M[i, v]$ and consider only edges with $M[i, v]=M[i-1, w]+c_{v w}$.


## Bellman-Ford: efficient implementation

## Shortest paths: practical improvements

Space optimization. Maintain two 1d arrays (instead of 2d array).

- $d(v)=$ cost of cheapest $v \rightarrow t$ path that we have found so far.
- $\operatorname{successor}(v)=$ next node on a $v \rightarrow t$ path.

Performance optimization. If $d(w)$ was not updated in iteration $i-1$, then no reason to consider edges entering $w$ in iteration $i$.

BELLMAN-FORD $(V, E, c, t)$
FOREACH node $v \in V$
$d(v) \leftarrow \infty$.
successor $(v) \leftarrow$ null.
$d(t) \leftarrow 0$.
FOR $\mathrm{i}=1$ TO $n-1$
Foreach node $w \in V$
IF ( $d(w)$ was updated in previous iteration) Foreach edge $(v, w) \in E$

IF $\left(d(v)>d(w)+c_{v w}\right)$

$$
d(v) \leftarrow d(w)+c_{v w} .
$$

$$
\operatorname{successor}(v) \leftarrow w .
$$

IF no $d(w)$ value changed in iteration i, STOP.

## Bellman-Ford: analysis

Claim. After the $i^{\text {th }}$ pass of Bellman Ford, $d(v)$ equals the cost of the cheapest $\psi t$ path using at most $i$ edges.

Counterexample. Claim is false!


If nodes $w$ considered before node $v$, then $\mathrm{d}(\mathrm{v})=3$ after 1 pass

## Bellman-Ford: analysis

Lemma 3. Throughout Bellman-Ford algorithm, $d(v)$ is the cost of some $v \rightarrow t$ path; after the $i^{\text {th }}$ pass, $d(v)$ is no larger than the cost of the cheapest $v \rightarrow t$ path using $\leq i$ edges.
Pf. [by induction on i]

- Assume true after $i^{\text {ih }}$ pass.
- Let $P$ be any $v \rightarrow t$ path with $i+1$ edges.
- Let $(v, w)$ be first edge on path and let $P^{\prime}$ be subpath from $w$ to $t$.
- By inductive hypothesis, $d(w) \leq c\left(P^{\prime}\right)$ since $P^{\prime}$ is a $w \rightarrow t$ path with $i$ edges.
- After considering $v$ in pass $i+1: d(v) \leq c_{v w}+d(w)$
$\leq c_{v w}+c\left(P^{\prime}\right)$
$=c(P)$ -

Theorem 2. Given a digraph with no negative cycles, Bellman-Ford computes the costs of the cheapest $v \rightarrow t$ paths in $O(m n)$ time and $\Theta(n)$ extra space.
Pf. Lemmas $2+3$.
can be substantiall
faster in practice

## Bellman-Ford: analysis

Claim. Throughout the Bellman Ford algorithm, following stecesson(i) pointers gives a directed path from vto of cost $d(v)$.

Counterexample. Claim is false!

- Cost of successor $v \rightarrow t$ path may have strictly lower cost than $d(v)$.
consider nodes in order: $\mathbf{t}, \mathbf{1 , 2 , 3}$



## Bellman-Ford: analysis

Claim. Throughout the Bellman Ford atgorithm, following suceessor(t) pointers gives a directed path from $v$ to 1 of cost $d(v)$.

Counterexample. Claim is false!

- Cost of successor $v \rightarrow t$ path may have strictly lower cost than $d(v)$.
consider nodes in order: t, 1, 2, 3



## Bellman-Ford: analysis

Claim. Throughout the Bellman Ford algorithm, following stecessor(i) pointers gives a directed path from $v$ to $t$ of cost $d(v)$.

Counterexample. Claim is false!

- Cost of successor $v \rightarrow t$ path may have strictly lower cost than $d(v)$.
- Successor graph may have cycles.
consider nodes in order: t, 1, 2, 3,4

$$
\mathrm{d}(2)=8
$$

## Bellman-Ford: analysis

Claim. Throughout the Bellman Ford algorithm, following strecessor(t) pointers gives a directed path from $v$ to 1 of cost $d(v)$.

Counterexample. Claim is false!

- Cost of successor $v \rightarrow t$ path may have strictly lower cost than $d(v)$.
- Successor graph may have cycles.
consider nodes in order: t, 1, 2, 3, 4



## Bellman-Ford: finding the shortest path

Lemma 4. If the successor graph contains a directed cycle $W$,
then $W$ is a negative cycle.
Pf.

- If $\operatorname{successor}(v)=w$, we must have $d(v) \geq d(w)+c_{v w}$.
(LHS and RHS are equal when successor(v) is set; $d(w)$ can only decrease;
$d(v)$ decreases only when successor $(v)$ is reset)
- Let $v_{l} \rightarrow v_{2} \rightarrow \ldots \rightarrow v_{k}$ be the nodes along the cycle $W$.
- Assume that $\left(v_{k}, v_{1}\right)$ is the last edge added to the successor graph.
- Just prior to that: $d\left(v_{1}\right) \geq d\left(v_{2}\right)+c\left(v_{1}, v_{2}\right)$

| $d\left(v_{2}\right)$ | $\geq$ | $d\left(v_{3}\right)$ | $+c\left(v_{2}, v_{3}\right)$ |
| :---: | :---: | :---: | :---: |
| $\vdots$ | $\vdots$ | $\vdots$ |  |

$d\left(v_{k-1}\right) \geq d\left(v_{k}\right) \quad+c\left(v_{k-1}, v_{k}\right)$
$d\left(v_{k}\right) \quad>d\left(v_{1}\right) \quad+c\left(v_{k}, v_{1}\right) \longleftarrow \begin{aligned} & \text { holds with strict inequality } \\ & \text { since we are updating } d v_{1}\end{aligned}$

- Adding inequalities yields $c\left(v_{1}, v_{2}\right)+c\left(v_{2}, v_{3}\right)+\ldots+c\left(v_{k-1}, v_{k}\right)+c\left(v_{k}, v_{1}\right)<0$. -
W is a negative cycle


## Bellman-Ford: finding the shortest path

Theorem 3. Given a digraph with no negative cycles, Bellman-Ford finds the cheapest $s \rightarrow t$ paths in $O(m n)$ time and $\Theta(n)$ extra space.

Pf.

- The successor graph cannot have a negative cycle. [Lemma 4]
- Thus, following the successor pointers from $s$ yields a directed path to $t$.
- Let $s=v_{l} \rightarrow v_{2} \rightarrow \ldots \rightarrow v_{k}=t$ be the nodes along this path $P$.
- Upon termination, if successor $(v)=w$, we must have $d(v)=d(w)+c_{v w}$.
(LHS and RHS are equal when $\operatorname{successor}(v)$ is set; $d(\cdot)$ did not change)
- Thus, | $d\left(v_{1}\right)$ | $=d\left(v_{2}\right)$ | $+c\left(v_{1}, v_{2}\right)$ | since algorithm |  |
| :--- | :--- | :--- | :--- | :--- |
| $d\left(v_{2}\right)$ | $=d\left(v_{3}\right)$ | $+c\left(v_{2}, v_{3}\right)$ | terminated |  |
| $\vdots$ | $\vdots$ | $\vdots$ |  |  |
|  | $d\left(v_{k-1}\right)$ | $=d\left(v_{k}\right)$ | $+c\left(v_{k-1}, v_{k}\right)$ |  |

Adding equations yields $d(s)=d(t)+c\left(v_{1}, v_{2}\right)+c\left(v_{2}, v_{3}\right)+\ldots+c\left(v_{k-1}, v_{k}\right) . \quad$.

$$
\begin{gathered}
\min \text { cost } \\
\text { of any } s \rightarrow t \text { path } \\
(\text { Theorem } 2)
\end{gathered} \overbrace{0}
$$

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- negative cycles in a digraph


## Distance vector protocols

Distance vector protocols. [ "routing by rumor" ]

- Each router maintains a vector of shortest path lengths to every other node (distances) and the first hop on each path (directions).
- Algorithm: each router performs $n$ separate computations, one for each potential destination node.

Ex. RIP, Xerox XNS RIP, Novell's IPX RIP, Cisco's IGRP, DEC's DNA Phase IV, AppleTalk's RTMP.

Caveat. Edge costs may change during algorithm (or fail completely).


## Path vector protocols

Link state routing.

- Each router also stores the entire path.
- Based on Dijkstra's algorithm.
- Avoids "counting-to-infinity" problem and related difficulties.
- Requires significantly more storage.

Ex. Border Gateway Protocol (BGP), Open Shortest Path First (OSPF).


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## Detecting negative cycles: application

Currency conversion. Given $n$ currencies and exchange rates between pairs of currencies, is there an arbitrage opportunity?

Remark. Fastest algorithm very valuable!


## Detecting negative cycles

Negative cycle detection problem. Given a digraph $G=(V, E)$, with edge weights $c_{v w}$, find a negative cycle (if one exists).


## Arbitrage opportunities

Currency conversion. Given $n$ currencies and exchange rates between pairs of currencies, is there an arbitrage opportunity?

Remark. Fastest algorithm very valuable!


## Detecting negative cycles

Lemma 5. If $\operatorname{OPT}(n, v)=\operatorname{OPT}(n-1, v)$ for all $v$, then no negative cycle can reach $t$.
Pf. Bellman-Ford algorithm. -

Lemma 6. If $\operatorname{OPT}(n, v)<\operatorname{OPT}(n-1, v)$ for some node $v$, then (any) cheapest path from $v$ to $t$ contains a cycle $W$. Moreover $W$ is a negative cycle.

Pf. [by contradiction]

- Since $O P T(n, v)<\operatorname{OPT}(n-1, v)$, we know that shortest $v \rightarrow t$ path $P$ has exactly $n$ edges.
- By pigeonhole principle, $P$ must contain a directed cycle $W$.
- Deleting $W$ yields a $v \rightarrow t$ path with $<n$ edges $\Rightarrow W$ has negative cost. -

$c(W)<0$


## Detecting negative cycles

Theorem 4. Can find a negative cycle in $\Theta(m n)$ time and $\Theta\left(n^{2}\right)$ space. Pf.

- Add new node $t$ and connect all nodes to $t$ with 0-cost edge.
- $G$ has a negative cycle iff $G^{\prime}$ has a negative cycle than can reach $t$.
- If $\operatorname{OPT}(n, v)=O P T(n-1, v)$ for all nodes $v$, then no negative cycles.
- If not, then extract directed cycle from path from $v$ to $t$. (cycle cannot contain $t$ since no edges leave $t$ ) •



## Detecting negative cycles

Theorem 5. Can find a negative cycle in $O(m n)$ time and $O(n)$ extra space. Pf.

- Run Bellman-Ford for $n$ passes (instead of $n-1$ ) on modified digraph.
- If no $d(v)$ values updated in pass $n$, then no negative cycles.
- Otherwise, suppose $d(s)$ updated in pass $n$.
- Define $\operatorname{pass}(v)=$ last pass in which $d(v)$ was updated
- Observe pass $(s)=n$ and pass(successor(v)) $\geq \operatorname{pass}(v)-1$ for each $v$.
- Following successor pointers, we must eventually repeat a node.
- Lemma $4 \Rightarrow$ this cycle is a negative cycle. -

Remark. See p. 304 for improved version and early termination rule. (Tarjan's subtree disassembly trick)

