Memory Hierarchy

CSE 373
Data Structures & Algorithms
Ruth Anderson
Spring 2008

Today's Outline

- · Admin:
 - HW #4 due Thursday at 11:59pm
 - Printouts and written problems due at the beginning of class Friday
 - Late Penalty = -25% per 24 hours, submit via email (including electronic version of written problems)
- · Memory Hierarchy and Locality

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Why do we need to know about the memory hierarchy/locality?

- One of the assumptions that Big-Oh makes is that all operations take the same amount of time.
- · Is that really true?

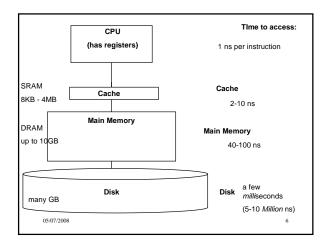
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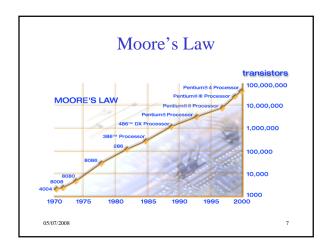
Definitions

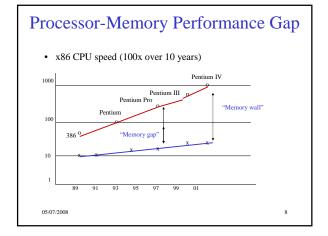
Cycle – (for our purposes) the time it takes to execute a single simple instruction. (ex. Add 2 registers together)

Memory Latency – time it takes to access memory

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What can be done?

- Goal: Attempt to reduce the number of accesses to the slower levels.
- How?

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Locality

Temporal Locality (locality in time) – If an item is referenced, it will tend to be referenced again soon.

Spatial Locality (locality in space) – If an item is referenced, items whose addresses are close by will tend to be referenced soon

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Caches

• Each level is a **sub-set** of the level below.

Cache Hit – address requested is in cache
Cache Miss – address requested is NOT in cache
Cache line size (chunk size) – the number of
contiguous bytes that are moved into the cache at one
time

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Examples

$$x = a + 6;$$
 $x = a[0] + 6;$
 $y = a + 5;$ $y = a[1] + 5;$
 $z = 8 * a;$ $z = 8 * a[2];$

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Locality and Data Structures

• Which has (at least the potential for) better spatial locality, arrays or linked lists?

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