# Computer Systems Architecture
## Assignment 7
## Due: Tuesday, June 3

   This assignment is meant to test your understanding of cache coherency protocols by designing cache protocols for different situations and evaluating their performance. Evan will probably send out a little more detail on some of the problems.

   The assignment is to do **Problems 6.5, 6.6**, **6.7a** and **6.7b**, **6.26a** and **6.26b** all by yourself. Think about **6.23**, **6.30** and **6.34**, but don't write out the answer (it's in the back of the book).

   **6.5**. If you would rather draw the finite state diagrams as two separate diagrams (like Figure 6.11), this is fine. The single diagram can get pretty cluttered. Just label the diagrams, so that Evan knows which is which. This is called the Illinois protocol; what is/are its advantage(s)?