# CSE 599I Course Notes: Spring 2024

# ETH, SETH and Fine-Grained Complexity, Lifting

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June 5, 2024

## **Overview**

**Exponential time hypotheses and fine-grain complexity: Beyond P vs NP** Despite the best efforts of researchers for over 50 years since the P vs NP question was formulated, the best algorithms we have for SAT and other NP-complete problems are still exponential in the worst case and barely improve on brute force. If these are representative of the correct level of complexity, which seems a reasonable stronger conjecture than  $P \neq NP$ , our usual ways of proving relationships between NP problems need to be rethought, both from a theoretical and practical point of view, and radically new relationships between problems emerge, a subject termed "fine-grain complexity". This yields surprising connections that have produced a web of problem-solving relationships well beyond the usual resource-focused complexity classes, for example, showing how improving existing polynomial-time algorithms for well-known problems is tied to improving exponential-time algorithms for SAT.

In this part of the course we will first discuss algorithms with different approaches to SAT and the best analysis with respect to their worst-case behavior. We will then consider the Exponential-Time Hypothesis (ETH) for SAT originally formulated by Impagliazzo and Paturi, analyze its robustness and implications for other NP problems.

After that we will then focus on a much more powerful Strong Exponential-Time Hypothesis (SETH) which, though it not as robust as ETH, has nearly the same level of evidence, and has wide-ranging implications that would allows to pinpoint the the complexity of many problems in P with a high degree of accuracy, proving that current algorithms are not far from the best possible. This is based on so-called fine-grained reductions and analysis of the complexity of these problems. (In general, fine-grained complexity encompasses both the consequences of SETH, but also of a small number of other key hypotheses, but in this course we will only focus on those that relate to SETH, or other closely related hypotheses.)

Depending on time we may also briefly discuss how even much smaller algorithmic improvements over brute force in solving satisfiability for restricted classes of circuits yield circuit lower bounds – an "ironic" consequence that algorithms imply lower bounds.

**Lifting** A number of longstanding problems in computational complexity have been resolved in the last decade by showing how simple forms of function composition let us convert hardness results proven in weak models of computation into hardness results for more powerful models of computation, a methodology that has been termed "lifting". We discuss lifting techniques and the some of the longstanding problems resolved using them. We then focus on a number of open problems and approaches to resolving them.

The main idea of lifting is conceptually simple: Given a Boolean function f or search problem (relation) R defined on  $\{0,1\}^n$ , one can take a simple Boolean function g (a "gadget") and define the composition  $f \circ g^n$  or  $R \circ g^n$ , by  $f(z_1, \ldots, z_n)$  where  $z_i = g(x_i)$  or  $z_i = g(x_i, y_i)$ depending on context where  $x_i$  and  $y_i$  may involve multiple bits.

The resulting function  $f \circ g^n$  is called a "lift" of f. It can easily be computed in a composed manner also: Given a computation of f, whenever f needs to access  $z_i$ , the computation for the lift of f can access the  $x_i$  (possibly also  $y_i$ ) and compute g instead to produce the value for

 $z_i$ . The general idea is to translate the complexity of computing f to its lifted version but in a stronger computational model for example converting the complexity of computing f using queries (decision trees) to the complexity of the lift of f in a communication complexity model. The simple algorithm of computing a  $z_i$  each time it is needed yields a communication protocol that has communication complexity equal to the product of the query complexity of f and the communication complexity of g.

A key question is whether this can be improved. Lifting results have shown that under certain general conditions it cannot be improved. Applications of this to circuit complexity and proof complexity follow from the fact that lifting applies to search problems as well as to Boolean functions.

# The worst-case complexity of SAT

We can ask this question for many different input formats, each a special case of CIRCUIT-SAT where the input is a circuit *C* defined on Boolean variables in  $\{0, 1\}^n$ .

The obvious brute force algorithm for CIRCUIT-SAT has complexity  $|C| \cdot 2^n$ .

All of the special cases of CIRCUIT-SAT we will discuss are NP-complete. We first focus on k-SAT for  $k \ge 3$ . How much can we improve on this brute force algorithm?

## 1 Best current SAT algorithms

We review the ideas behind the best current algorithms.

### The PPZ Algorithm

The first is an extremely simple randomized algorithm, the PPZ algorithm of Paturi, Pudlak, and Zane.

Algor	Algorithm 1 The PPZ-algorithm.				
Input k-CNF formula F in n Boolean variables $x_1, \ldots, x_n$ .					
1: ft	1: function PPZ(F)				
2:	<b>repeat</b> $n^2 2^{n-n/k}$ times:				
3:	$\alpha \leftarrow \emptyset$				
4:	$F' \leftarrow F$ > The current partial assignment				
5:	while $F' \neq \bot$ and $F'$ has an unassigned variable <b>do</b> $\triangleright F'$ is not identically false				
6:	Choose an unassigned variable $x_i$ uniformly at random				
7:	if $F'$ contains the unit clause $x_i$ then				
8:	$b \leftarrow 1$ $\triangleright x_i$ 's value is forced to 1.				
9:	else if F' contains the unit clause $\overline{x_i}$ then				
10:	$b \leftarrow 0$ $\triangleright x_i$ 's value is forced to 0.				
11:	else				
12:	Choose $b \in \{0, 1\}$ uniformly at random				
13:	Add $x_i = b$ to $\alpha$				
14:	$F' \leftarrow F' _{x_i \leftarrow b}$ $\triangleright$ Set $x_i$ to $b$ in $F'$ and simplify				
15:	<b>if</b> $F' = \top$ <b>then</b> Halt and output satisfying assignment $\alpha$ $\triangleright$ <i>F</i> is satisfied				
16:	until				

**Theorem 1.1.** If F is a satisfiable k-CNF formula, the PPZ algorithm finds a satisfying assignment for F with probability 1 - o(1).

Before we prove this, we need a few definitions.

**Definition 1.2.** For  $x \in \{0,1\}^n$ , write  $x^{\oplus i}$  for the element with the *i*-bit flipped. For a *n*-bit Boolean function  $f, i \in [n]$ , and  $x \in \{0,1\}^n$ , bit *i* is *sensitive for* f at x iff  $f(x^{\oplus i}) \neq f(x)$ . The *sensitivity of* f at x is the number of bits *i* that are sensitive for f at x, denoted  $s_f(x)$ . We extend this to circuits and formulas by applying the definition to the associated Boolean function.

**Proposition 1.3.** Suppose that x is a satisfying assignment for CNF formula F. Then there are  $s_F(x)$  distinct critical clauses of F, one clause  $C_{x,i}$  for each sensitive bit i of F at x, such that  $C_{x,i}(x) = 1$  but  $C_{x,i}(x^{\oplus i}) = 0$ . In particular,  $C_{x,i}$  contains  $x_i$  or  $\overline{x_i}$ .

*Proof.* The existence of critical clauses is immediate from the definition. The fact that the clauses must be distinct follows from the fact a clause containing both variables  $x_i$  and  $x_{i'}$  for i and i' sensitive on x cannot be critical for either variable since the clause would have two true literals on input x.

**Proposition 1.4.** Let S be the set of satisfying assignments of F. Then  $\sum_{x \in S} 2^{s_F(x)} \ge 2^n$ .

*Proof.* By induction on *n*. For n = 0 there is exactly one string in  $\{0, 1\}^n$  which has sensitivity 0 so the statement holds. Suppose it is true for n - 1 and consider the subsets  $S_b = \{x' \in \{0, 1\} \mid x'b \in S\}$  for b = 0, 1. Then  $S_b$  is the set of satisfying assignments of  $F_b = F|_{x_n=b}$ . If  $S_0$  is empty then  $S = \{x'1 \mid x' \in S_1\}$ . Then by definition  $s_F(x'1) = s_{F_1}(x') + 1$  and

$$\sum_{x \in S} 2^{S_F(x)} = \sum_{x' \in S_1} 2^{s_F(x'1)} = \sum_{x \in S_1} 2^{s_{F_1}(x')+1} \ge 2 \cdot 2^{n-1}$$

by the induction hypothesis for  $F_1$ . The same bound holds if  $S_1$  is empty. Finally, if both  $S_0$  and  $S_1$  are non-empty, then

$$\sum_{x \in S} 2^{S_F(x)} = \sum_{x' \in S_0} 2^{s_F(x'0)} + \sum_{x' \in S_1} 2^{s_F(x'1)} \ge \sum_{x' \in S_0} 2^{s_{F_0}(x')} + \sum_{x' \in S_1} 2^{s_F(x')} \ge 2^{n-1} + 2^{n-1} = 2^n$$

by the inductive hypothesis applied to  $F_0$  and  $F_1$ .

*Proof of Theorem 1.1.* Let *S* be the set of satisfying assignments for *F*. Fix some  $x^* \in S$  and let  $j = s_F(x^*)$ . There are *j* critical clauses on input  $x^*$ . Each execution through the **repeat** loop of the PPZ algorithm induces a uniformly random permutation  $\pi$  on the variables.

Define  $E_1$  be the event that for at least j/k of the j critical clauses  $C_{x^*,i}$  on input  $x^*$ , the critical variable  $x_i$  occurs last among the variables in the clause under the permutation  $\pi$ . Let  $\ell$  be the random variable for this number. For each critical clause, the critical variable occurs last with probability at least 1/k since each clause has size at most k. Therefore  $\mathbb{E}[\ell] \ge j/k$ . We claim that with probability that  $E_1$  holds is at least  $1/(kj - j + 1) \ge 1/(kn)$ : Since j, k, and  $\ell$  are integers, a value of  $\ell$  below j/k must be most (j - 1)/k. Since  $\ell$  is an integer bounded by j, the probability that  $\ell < j/k$  is at most the probability that applying Markov's inequality to random variable  $j - \ell$ , whose expected value is  $\leq j - j/k = (k - 1)j/k$ , is at least j - (j - 1)/k = ((k - 1)j + 1)/k which is ((k - 1)j + 1)/k. This means that if  $\ell$  is below its

expected value, then  $j - \ell$  is at least ((k-1)j+1)/((k-1)j) times its expected value which, by Markov's inequality, occurs with probability at most 1 - 1/((k-1)j+1).

Now assume that  $E_1$  holds and consider the probability that the assignment chosen in the **while** loop agrees with  $x^*$ . At each iteration, if the value of  $x_i$  is forced then it certainly agrees with  $x^*$ ; otherwise it agrees with with  $x^*$  with probability 1/2. Since  $E_1$  holds, there are at most n - j/k assignments to agree with  $x^*$  that are not forced since the  $\ge j/k$  variables in the critical clauses witnessing event  $E_1$  will all be forced. Therefore this occurs with probability at least  $2^{j/k-n} = 2^{s_F(x^*)/k-n}$  and hence the probability that a single iteration of the **repeat** loop finds satisfying assignment  $x^*$  is at least  $\frac{1}{kn}2^{s_F(x^*)/k-n}$ . Putting it all together we have

Pr[a repeat iteration outputs a satisfying assignment]

$$= \sum_{x \in S} \Pr[a \text{ repeat iteration outputs satisfying assignment } x]$$
  

$$\geq \frac{1}{kn} \sum_{x \in S} 2^{s_F(x)/k-n}$$
  

$$= \frac{1}{kn} 2^{n/k-n} \sum_{x \in S} 2^{(s_F(x)-n)/k}$$
  

$$\geq \frac{1}{kn} 2^{n/k-n} \sum_{x \in S} 2^{s_F(x)-n}$$
  

$$\geq \frac{1}{kn} 2^{n/k-n} \text{ by Proposition 1.4.}$$

Since PPZ runs this  $n^2 2^{n(1-1/k)}$  times it succeeds with probability 1 - o(1).

In particular when k = 3, PPZ has a running time of  $2^{2n/3+o(n)}$  which is at most  $1.5875^n$  for large *n*.

 $\square$ 

PPZ also show how their randomized algorithm can be made deterministic with essentially the same complexity but the details make the algorithm substantially messier.

#### The PPSZ algorithm

This algorithm was improved by Paturi, Pudlak, Saks, and Zane to yield the PPSZ algorithm, which replaces the unit clause test for forced variables with a slightly different criterion tests than unit clauses. It checks whether  $x_i$  or  $\overline{x_i}$  can be derived by bounded *resolution* on constant-size clauses. The resolution rule is given by

$$\frac{A \lor x, \ B \lor \overline{x}}{A \lor B}$$

and lets one add the new clause  $A \lor B$  if both clauses  $A \lor x$  and  $B \lor \overline{x}$  are known. This is a sound rule since variable *x* connect make both of the original clauses true. In general, this can increase the sizes of the derived clauses and have exponential size, but if there is a constant size limit on the size of the derived clauses (e.g. a limit of 5 variables when k = 3) then all derived clauses can be found in polynomial time.

The success probability of each iteration for PPSZ is  $2^{cn/k-n-o(n)}$  for  $c = \pi^2/6$  and hence the savings in the exponent is roughly cn/k. The original PPSZ analysis only applied when the sensitivity of every satisfying assignment was n (the satisfying assignments are all isolated). This was extended by Hertli to the general case and subsequent papers have improved the analysis only very slightly. For the special case that k = 3, the current best general bound due to Scheder is 1.306973<sup>*n*</sup>. Again this algorithm can be determinized without significant loss.

### Schöning's Random Walk Algorithm

The basic idea of this algorithm is repeated local search starting from a random initial assignment.

**Theorem 1.5.** Suppose that F is a satisfiable k-CNF formula with each clause of size exactly k. Then Schöning's algorithm finds a satisfying assignment for F with probability 1 - o(1).

Algorithm 2 Schöning's algorithm.						
U	0					
Input <i>k</i> -CNF formula <i>F</i> in <i>n</i> Boolean variables $x_1, \ldots, x_n$ .						
1: <b>f</b>	1: <b>function</b> SCHOENING( <i>F</i> )					
2:	<b>repeat</b> $30 \cdot 2^{n} (1 - 1/k)^{n}$ times:					
3:	Choose an assignment $\alpha \in \{0,1\}^n$ uniformly at random					
4:	repeat 3n times					
5:	if F is satisfied by $\alpha$ then					
6:	Halt and return satisfying assignment $\alpha$ .					
7:	else					
8:	Let C be a clause of F such that $C(\alpha) = 0$					
9:	Choose a uniformly random variable $x_i$ in C.					
10:	$\alpha \leftarrow \alpha^{\oplus i}$ $\triangleright$ Flip assignment $\alpha_i$ to make	C true.				
11:	until					
12:	until					

*Proof.* Suppose that *F* has some satisfying assignment  $\alpha^*$ . For each clause *C* of *F*, we identify one special variable  $x_c$  whose associated literal in *C* is set to true by assignment  $\alpha^*$ . Each time Step 9 is executed, since clause *C* has exactly *k* variables, the special variable  $x_c$  in *C* is chosen with probability exactly 1/k. Fix an iteration of the outer **repeat** loop. For t = 0, ..., 3n let  $X_t \in \{0, 1, ..., n\}$  be the random variable counting the Hamming distance between the current assignment  $\alpha$  and  $\alpha^*$ . If  $X_t$  is not satisfying then  $X_{t+1} = X_t - 1$  with probability at least 1/kcorresponding to the case that the special variable is chosen. If some  $X_t = 0$  then the algorithm halts and succeeds. (It may also succeed if it finds some other satisfying assignment.) Since the initial  $\alpha$  is chosen uniformly at random:

$$\Pr[X_0 = j] = \binom{n}{j} 2^{-n}.$$

Define a Markov chain  $Y_t$  such that  $Y_0 = X_0$  and for 0 < j satisfies

$$\Pr[Y_{t+1} = j-1 \mid Y_t = j] = \frac{1}{k} \quad \text{and} \quad \Pr[Y_{t+1} = j+1 \mid Y_t = j] = 1 - \frac{1}{k}, \quad (1)$$

and if  $Y_t = 0$  then  $Y_{t+1} = 0$ . Clearly  $X_t \le Y_t$ . so

$$\Pr[\exists t \in [0, 3n], X_t = 0] \ge \Pr[\exists t \in [0, 3n], Y_t = 0].$$

If k = 2 then the walk  $Y_t$  is an unbiased random walk and will reach 0 with high probability in  $O(n^2)$  steps. For  $k \ge 3$ , the walk is biased away from 0 and will be far away from 0 if we let it run too long, but there is some probability of reaching 0 in the early stages. If  $Y_0 = j$ then every  $i \ge 0$ , if t = 2i + j the probability that  $Y_t = 0$  and  $Y_{t'} > 0$  for t' < t is equal to the probability that the Markov chain has *i* increasing steps and i + j decreasing steps. For each fixed pattern of increasing and decreasing steps that does not have prefix that reaches 0, this occurs with probability

$$\left(\frac{1}{k}\right)^{i+j} \cdot \left(1 - \frac{1}{k}\right)^i.$$

If we didn't have the condition that all proper prefixes have value > 0, there would be  $\binom{2i+j}{i}$  choices for the pattern of increasing/decreasing steps.

A standard theorem called the Ballot Theorem gives the following:

**Claim 1.6.** The number of good sequences that have *i* increasing steps, i + j decreasing steps and no proper prefix with an excess of *j* decreasing steps is

$$\binom{2i+j}{i}\frac{j}{2i+j}$$

*Proof of Claim.* The general idea to prove the claim is to show that for any particular vector  $v \in \binom{2i+j}{i}$  exactly *j* of the cyclic shifts of *v* are good. We prove this by induction on *i*. For i = 0, all *j* of the shifts of *v* are the same (and are good). We write +1 for each of the *i* increasing steps and -1 for each of the i + j decreasing steps. For i > 0, there must be some consecutive steps in cyclic order on *v* consisting of an increasing step followed by a decreasing step. A good cyclic shift of *v* cannot end on either of these two steps since for the first it would be less than -j one step before the end and for the second would be equal to -j two steps before the end. If we let v' be the string with these two steps removed, then the end points of the good cyclic shifts of v' are precisely those that are good for *v*. By induction exactly *j* of the 2(i-1) + j shifts of v' are good and hence *j* shifts of *v* are good.

Therefore

$$\begin{aligned} &\Pr[\exists t \in [0, 3n], X_t = 0] \\ &\geq \Pr[\exists t \in [0, 3n], Y_t = 0] \\ &= \sum_{j=0}^n \Pr[X_0 = j] \cdot \sum_{t=2i+j \le 3n} \binom{2i+j}{i} \frac{j}{2i+j} (\frac{1}{k})^{i+j} \cdot (1-\frac{1}{k})^i \\ &\geq \sum_{j=0}^n \Pr[X_0 = j] \cdot \sum_{i=0}^j \binom{2i+j}{i} \frac{j}{2i+j} (\frac{1}{k})^{i+j} \cdot (1-\frac{1}{k})^i \\ &= \sum_{j=0}^n \Pr[X_0 = j] \cdot \sum_{i=0}^j \binom{2i+j}{i} \frac{j}{2i+j} (\frac{1}{k})^{i+j} \cdot (1-\frac{1}{k})^i \\ &\geq \frac{1}{3} \sum_{j=0}^n \Pr[X_0 = j] \cdot \sum_{i=0}^j \binom{2i+j}{i} (\frac{1}{k})^{i+j} \cdot (1-\frac{1}{k})^i. \end{aligned}$$

For k = 3 it turns out that the dominant term in

$$\sum_{i=0}^{J} \binom{2i+j}{i} \left(\frac{1}{k}\right)^{i+j} \cdot \left(1-\frac{1}{k}\right)^{i}$$

occurs when i = j. That term is  $\binom{3j}{j}(1/3)^{2j}(2/3)^j$ . Using Stirling's formula  $\binom{3j}{j}$  is asymptotically  $\frac{2}{\sqrt{3\pi j}}3^{3j}/2^{2j}$  so the term is asymptotically equal to  $\frac{2}{\sqrt{3\pi j}}2^{-j} \ge \frac{1}{\sqrt{5n}}2^{-j}$ . Plugging this in we get that the probability of success is asymptotically at least

$$\frac{1}{3\sqrt{5n}}\sum_{j=0}^{n} \Pr[X_0=j] \cdot 2^{-j} = \frac{2^{-n}}{3\sqrt{5n}}\sum_{j=0}^{n} \binom{n}{j} 2^{-j} = \frac{2^{-n}}{3\sqrt{5n}} (3/2)^n.$$

which is roughly  $(3/4)^n$  so the running time is roughly  $(4/3)^n$ . The savings over brute force search is roughly a  $(2/3)^n$  factor.

More generally, the dominant term occurs when *i* is roughly j/(k-2) so that i+j is roughly j(k-1)/(k-2) and *i* is roughly j/(k-2) so the ratio of the two roughly matches the ratio of the corresponding probabilities. For this dominant term, the Stirling approximation gives that  $\binom{2i+j}{i}$  is asymptotically at least

$$\frac{1}{\Theta(\sqrt{n})} \cdot k^{2i+j}/(k-1)^{i+j}$$

and the corresponding term is  $\frac{1}{\Theta(\sqrt{n})} \cdot (k-1)^{-j}$ . Plugging this in we get that the probability of success is asymptotically at least

$$\frac{2^{-n}}{\Theta(\sqrt{n})} \sum_{j=0}^{n} \binom{n}{j} (k-1)^{-j} = \frac{2^{-n}}{\Theta(\sqrt{n})} (k/(k-1))^{n}.$$

With a bit more care, one can observe that the  $\Theta(1/\sqrt{j})$  factor (which became  $1/\sqrt{n}$ ) can be removed because there are roughly  $\Theta(\sqrt{j})$  terms of roughly equal size. The savings over brute force is roughly  $(1-1/k)^n$ .

Observe that as k grows 1 - 1/k is roughly  $e^{-1/k} = 2^{-1/(k \ln 2)}$ . Therefore the savings over brute force for Shöning's algorithm is a  $2^{-n/(k \ln 2)} \ge 2^{-1.4427n/k}$  factor. This is better than PPZ but not as good as PPSZ since  $\pi^2/6$  is roughly 1.6645.

### Better *k*-SAT algorithms?

In the three algorithms discussed so far, there is a running time savings of  $2^{\Theta(n/k)}$  versus brute force search. There is a later algorithm of Chan and Williams based on a completely different approach, that de-randomizes probabilistic polynomials; it also gets savings of this character, though with a substantially worse constant in the exponent. This algorithm not only determines satisfiability, it also computes the exact number of satisfying assignments.

There is no general algorithm known that does better, though Viya and Williams have shown that for random *k*-CNF formulas, the basic PPZ algorithm almost surely has savings at least  $2^{\Omega(\log k)n/k}$ . It is not clear what the hardest instances would be for the PPZ algorithm.

# 2 The Exponential Time Hypothesis

The *Exponential Time Hypothesis (ETH)* of Impagliazzo and Paturi is simply the hypothesis that the worst-case complexity of 3-SAT on formulas in *n* variables is at least  $2^{\delta n}$  for some constant  $\delta > 0$ .

The ETH was motivated by the fact that the best algorithms for 3-SAT known at the time all had running times at least  $c^n$  for some constant c > 1. In the roughly 25 years since it was formulated, that state of affairs has not changed. Impagliazzo and Paturi considered it a hypothesis rather a conjecture, in that they simply found it plausible; they were most concerned about what its consequences would be if it were true.

What does the ETH imply for NP-complete problems? Consider the implication for the problem INDEPENDENT-SET. Consider the standard reduction  $3-SAT \leq_p INDEPENDENT-SET$  which maps a 3-CNF formula to a graph with 3m vertices, one per literal occurrence, with edges joining every pair of occurrences of the same variable with opposite sign.

Suppose that we tried to claim that ETH implies that INDEPENDENT-SET requires  $2^{\delta' n}$  time for some  $\delta' > 0$ . We would need to show that it is impossible for INDEPENDENT-SET to be solved in time  $2^{\delta N}$  for *N*-node graphs for some  $\delta < \delta'$  given the ETH. However, the graph size in the reduction has N = 3m where *m* could be as large as  $\Theta(n^3)$ . A solution in time  $2^{\delta N}$  would then be in time  $2^{3\delta m}$  which would be much larger than the trivial SAT algorithm which runs in time  $2^n poly(m)$  and hence would not imply anything new.

However ETH *would* imply that INDEPENDENT-SET on *N*-node graphs requires time  $2^{\Omega(N^{1/3})}$  using the same reduction.

### Sparsification

To get around this problem, Impagliazzo, Paturi, and Zane devised a way to *sparsify* formulas to show that ETH is equivalent to ETH for *sparse* 3-SAT formulas with n variables and only O(n) clauses.

In general, one cannot reduce a 3-CNF formula F with n variables and m clauses to one with only O(n) clauses, so one needs to reduce the formula F to multiple other formulas. The total work on these formulas must also not be not too large. Impagliazzo, Paturi, and Zane showed that this approach does work and Calabro, Impagliazzo, Paturi significantly improved the analysis (full details in Calabro's dissertation) yielding the following theorem:

**Theorem 2.1** (Sparsification Lemma). Let  $\varepsilon > 0$  and  $k \ge 3$  be a constant. There is a  $2^{\varepsilon n} \operatorname{pol} y(n)$  time algorithm that takes a k-CNF formula F on n variables and produces  $2^{\varepsilon n}$  k-CNF formulas,  $F_1, \ldots, F_{2^{\varepsilon n}}$ , such that F is satisfied if and only if  $\bigvee_i F_i$  is satisfied and each  $F_i$  has n variables and  $(k/\varepsilon)^{O(k)}n$  clauses. In fact, each variable is in at most  $\operatorname{pol} y(1/\varepsilon)$  clauses, and the  $F_i$  are over the same variables as F.

**Corollary 2.2.** The ETH implies that there exist some constants  $c, \delta > 0$  such that satisfiability of 3-CNF formulas in n variables and at most cn clauses (sparse formulas) requires time at least  $2^{\delta n}$ .

*Proof.* Each of the formulas  $F_i$  in the Sparsification Lemma has O(n) clauses in n variables and hence is sparse. Suppose that the conclusion is false. If 3-SAT is easy for sparse formulas, say with running time at most  $2^{\varepsilon' n}$  for every  $\varepsilon' > 0$ , then one could solve 3-SAT in time  $2^{\varepsilon'' n'}$  time for any  $\varepsilon'' > 0$  and arbitrary clause density as follows:

Applying the Sparsification Lemma with  $\varepsilon = \varepsilon' = \varepsilon''/3$ , computing all the formulas  $F_i$ . Check the satisfiability of each one in turn in time  $2^{\varepsilon' n}$  and output YES iff all of the  $F_i$  are satisfied. The total runtime would be  $poly(n) \cdot 2^{2\varepsilon'} = poly(n)2^{2\varepsilon''/3} < 2^{\varepsilon''}$ . This would violate the ETH, so it must be the case that ETH is true even for sparse formulas.

Here is a sampling of the consequences/equivalent formulations of the ETH given the Sparsification Lemma.

**Corollary 2.3.** *The ETH is equivalent to the following:* 

- For every k ≥ 3 there is a constant s<sub>k</sub> > 0 such that k-SAT on n variable formulas requires time at least 2<sup>s<sub>k</sub>n-o(n)</sup>.
- For each of the following NP-complete problems there is a constant c > 0 such that the problem requires time at least  $2^{cN}$ :
  - INDEPENDENT-SET, VERTEX-COVER and DOMINATING-SET on graphs with N edges,
  - CLIQUE on N-node graphs,
  - 3-COLOR on graphs with N edges,
  - k-COLOR on graphs with N edges,
  - SUBSET-SUM with N integers of at most N bits each,
  - HAMILTONIAN-PATH, HAMILTONIAN-CYCLE on graphs with N edges.

We now focus on the proof of the Sparsification Lemma. The basic idea is a kind of tree search where we branch on the value of variables (or subclauses), simplifying the formula on each branch as we go. This will be effective in reducing the number and lengths of clauses while the simplified formulas are not sparse, yielding a frontier where we stop where the formulas are all sparse. Each final  $F_i$  is the conjunction of the path to the simplified formula at this frontier. It remains to argue that this frontier is small and that all the formulas at the frontier are sparsified.

At each step the algorithm will introduce new short clauses that may subsume (imply) other longer clauses that contain them. Whenever we do so we will reduce the formula by deleting all subsumed clauses. We call this operation  $reduce(\cdot)$ . We will view each CNF formula as a set of clauses using an implicit conjunction.

The key idea of the branching algorithm is that formulas with many clauses must have large sets of clauses that share a non-trivial subclause on which they overlap and that this overlap can be pulled out using the distributive law: either the common subclause is satisfied, which makes all of the clauses true, or the remaining part of each clause must be satisfied. This is related to but different from the kinds of intersections in the sunflower lemma, where the core (common intersection) may be empty and the remaining sets (petals) must be disjoint.

Here we identify each clause with a set of literals and have the notion simply of an (h, p)flower, which is a collection  $\geq \theta_p^{\varepsilon,k}$  clauses of size exactly (h + p) that all share a common subclause H, the *heart* of the flower, of size  $h \geq 1$ . We write P, the set of *petals* of the flower, for the set of *p*-clauses that remain from the flower when H is removed. The values  $\theta_p^{\varepsilon,k}$  are parameters that we set later. We identify an (h, p)-flower by its heart H.

At each stage, there may be many different flowers that can be branched on. The algorithm always chooses to branch using flowers with the smallest current clause size that have that smallest petals (largest heart) so that the potential subsumptions are maximized.

#### Algorithm 3 Sparsification algorithm.

Input a *k*-CNF formula *F* in *n* Boolean variables  $x_1, \ldots, x_n$ .

Assumes a set of parameters  $\theta_p^{\varepsilon,k}$  for p = 1, ..., k that give the threshold for the number of petals of size p at which subformulas  $F^*$  are designated as (h, p)-flowers.

1: <b>function</b> Sparsify(F')					
2:	$F' \leftarrow reduce(F')$	$\triangleright$ Remove subsumed clauses from $F'$			
3:	if there is some $(h, p)$ -flower $F^*$ in $F'$ then				
4:	Choose an $(h, p)$ -flower $F^*$ such that $h + p$ is minimized and then $h$ is maximized.				
5:	Let <i>H</i> be the heart of $F^*$ and <i>P</i> be the set of petals of $F^*$ .				
6:	Sparsify $(F' \cup \{H\})$	▶ First branch is the case that <i>H</i> is set to true.			
7:	Sparsify $(F' \cup P)$	▷ Second branch is the case that all petals in <i>P</i> are set to true.			
8:	else				
9:	Append formula $F'$ to the list of output formulas.				

The values of the parameters  $\theta_p^{\varepsilon,k}$  algorithms depend on k and  $\varepsilon$  solely via a parameter  $\alpha = \alpha_{\varepsilon,k} \ge 2$  that we will define later. For simplicity of notation, we will drop the k and  $\varepsilon$  and just write  $\theta_p$  instead of  $\theta_p^{\varepsilon,k}$ . These are defined in terms of auxiliary parameters  $\beta_{k'}$  and are defined by

$$\beta_1 = 2$$
  

$$\beta_{k'} = \sum_{h=1}^{k'-1} 4\alpha \beta_{k'-h} \beta_h \quad \text{for } 2 \le k' < k$$
  

$$\theta_0 = 2$$
  

$$\theta_{k'} = \alpha \beta_{k'} \quad \text{for } 1 \le k' < k.$$

Note that since  $\theta_0 = 2 > 1$ , an (h, p)-flower must have p > 0.

For  $k' \le k$ , we say that a formula F' is k'-sparsified iff for every  $j \le k'$  and every h with 0 < h < j, F' does not contain an (h, j - h)-flower.

The execution of the sparsification algorithm produces a binary tree with each node u labelled by a formula  $F_u = reduce(F')$  where F' is the formula at which the node is called. By

definition, *u* is a leaf of this tree if and only if  $F_u$  is *k*-sparsified. We write  $H_u$  for the heart found during the call at node *u* and  $P_u$  for the set of petals at node *u*. If *u* is not a leaf and *v*, *w* are the left and right children of *u*, respectively, then  $F_v = reduce(F_u \cup \{H_u\})$  and  $F_w = reduce(F_u \cup P_u)$ .

The following proposition is immediate from the fact that the  $F' \equiv reduce(F')$  and that any truth assignment that satisfies a flower satisfies its heart or satisfies all of its petals.

**Proposition 2.4.**  $F_{\mu} \equiv F_{\nu} \lor F_{\mu}$ .

**Lemma 2.5.** Suppose that  $F_u$  is k'-sparsified.

- (a) If C' is an arbitrary clause, with  $|C'| = h < j \le k'$ , there are fewer than  $\theta_{j-h}$  j-clauses  $C \in F_u$  containing C'.
- (b) For  $j \le k'$ ,  $F_u$  contains fewer than  $2\theta_{i-1}n/j$  clauses of length j.

*Proof.* For (a), assume that there are at least  $\theta_{j-h}$  *j*-clauses in  $F_u$  that contain C'. Let H be their common intersection. Then  $C' \subseteq H$  so  $|H| \ge h$ . Since  $\theta_{j-|H|} \le \theta_{j-h}$ , this would contradict the assumption that  $F_u$  is k'-sparsified.

For (b), suppose that  $F_u$  contains at least  $\frac{2n}{j}\theta_{j-1}$  *j*-clauses. Therefore there are at least  $2n\theta_{j-1}$  total literals occurring in these *j*-clauses. It follows that at least one literal occurs in at least  $\theta_{j-1}$  clauses which would be a (1, j - 1) flower contradicting the assumption that  $F_u$  is k'-sparsified.

Since the formulas output by the algorithm are *k*-sparsified, we can immediately apply part (b) of the above lemma to obtain the following.

**Corollary 2.6.** Every formula output by the sparsification algorithm contains at most  $c_{k,\varepsilon}$  n clauses for  $c_{k,\varepsilon} = \sum_{j=1}^{k} \frac{2\theta_{j-1}}{j}$ .

It remains to show that the total number of leaves is small and to bound  $c_{k,\varepsilon}$ ... In any of the formulas  $F_u$ , we say that a clause  $C \in F_u$  is *new* iff it is not in the original formula at the root; that is, it is either the heart or one of the petals introduced along the path from the root to u. In moving from  $F_u$  to  $F_v$  or  $F_w$ , the new clauses added may eliminate clauses from  $F_u$  when the *reduce* operation is applied.

**Lemma 2.7.** If a new clause C' with |C'| = i < j eliminates any new j-clause from  $F_u$  then it eliminates at most  $2\theta_{i-i} - 2$  total j-clauses, both original and new.

*Proof.* Suppose that C' eliminates some new *j*-clauses and at least  $2\theta_{j-i} - 1$  total *j*-clauses. Consider the first node u' along the path from the root to u that contains all of the eliminated *j*-clauses. The node u' cannot be the root since C' eliminates at least one new *j*-clause so it has some parent u'' along the path.

At that parent u'', the formula  $F_{u''}$  must be *j*-sparsified since some new *j*-clause is added at u' and the sparsification procedure chooses the minimal clause size for the flowers it selects, which must have been larger than *j*.

By Lemma 2.5(a), there are at most  $\theta_{j-i} - 1$  *j*-clauses of  $F_{u''}$  that contain *C*'. Since there are at least  $2\theta_{j-i} - 1$  total *j*-clauses in  $F_{u'}$  that contain *C*', at least  $\theta_{j-1}$  of them must have been added in moving from  $F_{u''}$  to  $F_{u'}$ .

Now u' cannot be a left child of u'' since only a single clause  $H_{u''}$  is added to  $F_{u''}$  and  $\theta_{j-i} \ge \theta_0 \ge 2$ . On the other hand, if u' is a right child of u'', we must have a set  $P' \subseteq P_{u''}$  of at least  $\theta_{j-i}$  petals that contain C' and these petals in P' must have size j. Let  $h = |H_{u''}|$  so the  $\ge \theta_{j-i}$  clauses  $F'_{u''} \subseteq F_{u''}$  corresponding to P' must have size exactly h + j. All of these clauses contain  $H' = H \cup C'$  which, since H and C' must be disjoint, has size h+i. Therefore the subset  $F'_{u''}$  is an (h + i, j - i) flower in  $F_{u''}$  contradicting the maximality of h in choosing a flower at node u''.

**Lemma 2.8.** For j < k, the number of new clauses of size  $\leq j$  that ever get created on a root-leaf path is at most  $\beta_i n$ .

*Proof.* The proof is by induction on j. Let  $N_j$  be number of such clauses. For j = 1, there are at most 2n 1-clauses that could possibly be created which is at most  $\beta_1 n$  since  $\beta_1 = 2$ . Now suppose that it is true for j - 1. The number of new clauses created on the path is at most the number of clauses at the end plus the number that were created and then deleted, therefore, if we let  $E_{j,k'}$  be the number of k' clauses on the path eliminated by j-clauses, we have

$$N_{j} \leq N_{j-1} + \sum_{i=1}^{j-1} E_{i,j} + \frac{2n}{j} \theta_{j-1} \qquad \text{by Lemma 2.5(b)}$$
$$\leq N_{j-1} + \sum_{i=1}^{j-1} (2\theta_{j-i} - 2)N_{i} + 2\theta_{j-1}n/j$$

since there are at most  $N_i$  new clauses of size *i* along the path and each eliminates at most  $2\theta_{i-1} - 2$  *j*-clauses by Lemma 2.7,

$$\leq \beta_{j-1}n + \sum_{i=1}^{j-1} (2\theta_{j-i} - 2)\beta_i n + 2\theta_{j-1}n/j \qquad \text{by induction hypothesis}$$
$$< \beta_{j-1}n + 2\sum_{i=1}^{j-1} \alpha\beta_{j-i}\beta_i n + 2\alpha\beta_{j-1}n \qquad \text{since } \theta_i = \alpha\beta_i \text{ for } i \geq 1$$
$$\leq 4\sum_{i=1}^{j-1} \alpha\beta_i\beta_{j-i}n \qquad \text{since } \alpha \geq 2,$$
$$= \beta_j n$$

as required.

**Corollary 2.9.** Every root-leaf path is of length at most  $\beta_{k-1}n$ .

*Proof.* Each step creates at least one new clause.

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**Lemma 2.10.** There are at most  $(k-1)n/\alpha$  petal steps on every root-leaf path.

*Proof.* By Lemma 2.8, for  $j \le k - 1$ , there are at most  $\beta_j n$  total new *j*-clauses created along the path. Each petal steps that creates *j*-clauses, creates  $\theta_j$  such *j*-clauses, so the number of petal steps that create *j*-clauses is at most  $\beta_j n/\theta_j = n/\alpha$ . There are only k - 1 possibilities for *j*, which gives the bound.

Proof of Sparsification Lemma. We suppose that  $0 < \varepsilon \le 1$  and  $k \ge 3$ . Define  $\alpha = \frac{2(k-1)^2}{\varepsilon} \lg \frac{32(k-1)^2}{\varepsilon} \ge 2$ . Though it is a pain to actually argue, one can show that  $\beta_j \le 4(32\alpha)^{j-1}$ . (This is not simply by induction with this bound.) We can describe each path to a leaf by a sequence of at most  $\beta_{k-1}n$  steps with at most  $p \le (k-1)n/\alpha$  petal moves. We use the fact that  $\sum_{j=0}^{\ell} {n \choose j} \le 2^{H_2(\ell/n)n}$  where  $H_2(\gamma)$  is the binary entropy function equal to  $\gamma \lg(1/\gamma) + (1-\gamma)\lg(1/(1-\gamma))$ . It follows that the total number of leaves is at most

$$\sum_{p=0}^{(k-1)n/\alpha} \binom{\beta_{k-1}n}{p} \le 2^{H_2(\frac{k-1}{\alpha\beta_{k-1}})\beta_{k-1} n}.$$

It therefore suffices to show that  $H_2(\frac{k-1}{\alpha\beta_{k-1}})\beta_{k-1} \leq \varepsilon$ . For  $\gamma < 1/2$ , it isn't hard to show that  $H_2(\gamma) \leq \gamma \lg(4/\gamma)$ , so

$$\begin{aligned} H_2(\frac{k-1}{\alpha\beta_{k-1}})\beta_{k-1} &\leq \frac{k-1}{\alpha} \lg(\frac{4\alpha\beta_{k-1}}{k-1}) \\ &\leq \frac{\varepsilon}{2(k-1)\lg\frac{32(k-1)^2}{\varepsilon}} \cdot \lg(\frac{16\cdot 32^{k-2}\alpha^{k-1}}{k-1}) \qquad \text{by definition of } \alpha \text{ and the bound on } \beta_{k-1} \\ &\leq \frac{\varepsilon}{2\lg\frac{32(k-1)^2}{\varepsilon}} \cdot \lg(\frac{32\alpha}{k-1}) \leq \varepsilon \qquad \text{since } k-1 \geq 2 \end{aligned}$$

and hence the bound follows.

#### k-SUM

ETH has some surprising consequences inside P.

The *k*-SUM problem is a parameterized variant of SUBSET-SUM that given *n* integers of  $O(\log n)$  bits each and a target *t* asks whether or not there are *k* integers that sum to *t*.

**Proposition 2.11.** *k*-SUM can be solved in  $O(n^{\lceil k/2 \rceil} \log n)$  time.

*Proof.* Here is an algorithm: Compute the sums of all the  $\binom{n}{\lceil k/2 \rceil}$  subsets of the input list of size  $\lceil k/2 \rceil$ . Sort these sums. For each of the  $\binom{n}{\lfloor k/2 \rceil}$  subsets *S* of  $\lfloor k/2 \rfloor$  input integers run binary search on the sorted list for the value  $t - \sum_{i \in S} x_i$ . The cost is dominated by the time to compute the sorted list.

**Theorem 2.12.** ETH implies that there is a constant  $\varepsilon > 0$  such that k-SUM requires time  $\Omega(n^{\varepsilon k})$ .

*Proof.* The general idea of the argument is that we will see how to use algorithms for *k*-SUM to decide 3-SAT on formulas with *n* variables and O(n) clauses. To do this we first reduce 3-SAT to 1-in-3-SAT which will be more convenient; A 3-CNF formula for 1-in-3-SAT will be a yes instance iff there is an assignment that makes exactly one literal true in each clause. To reduce 3-SAT to 1-in-3-SAT we add 4 new variables for each original 3-clause and replace  $(x \lor y \lor z)$  by  $(\neg x \lor a \lor b)(y \lor b \lor c)(\neg z \lor c \lor d)$ . The resulting formula has O(n) variables and O(n) clauses.

(We have several cases: Suppose that the assignment satisfying  $x \lor y \lor z$  satisfies y; then we can extend it by setting b and c to false and setting b = x and s = z. Suppose that the assignment does not satisfy y; if it also does not satisfy x then we set a = b = 0, c = 1 and d = 0 since it must set z to true. The subcase that z is not satisfied is symmetric. If both x and z are satisfied, then we set a = 0, b = 1, c = 0, d = 1. Conversely consider any 1-in-3 satisfying assignment of the new formula. If it sets b = c = 0 then y must be true which satisfies  $x \lor y \lor z$ . It it sets b = 1, then we must gave  $\neg x = 0$  and hence x = 1 which satisfies  $x \lor y \lor z$ . If it sets c = 1 then we must have  $\neg z = 0$  which again satisfies  $x \lor y \lor z$ .)

The main idea of the reduction is that we break up the *n* variables of the 1-in-3-SAT formula into *k* chunks of n/k variables each,  $V_1, \ldots, V_k$  and have one integer corresponding to each partial assignment to those n/k variables for a total of  $N = k2^{n/k}$  integers. We think of each integer as being written in base k + 1 so there won't be any carries when we add up *k* of these numbers. There will be one digit for each of the m = O(n) clauses and *k* digits to correspond to which chunk a number corresponds to. For each truth assignment to  $V_i$  the number will have the digit in chunk position *i* set to 1, with the rest of the chunk bits 0, and will have a 0 or 1 for each clause digit depending on whether it sets 0 or 1 literals in the clause to true. (If it sets more than one literal in a clause to true, we don't include a number for that partial truth assignment since it cannot possibly be extended to 1-in-3-satisfying.) The target *t* will simply be the all 1's string of length k + m in base k + 1. These numbers are easy to compute in  $O(km2^{n/k}) = O(Nm) = O(kN \log N)$  time.

By construction, each number takes  $O(m \log k)$  bits to represent which is  $O(k \log k \log N)$  bits and hence  $O(\log N)$  for k fixed.

It is easy to see by construction that there are k numbers that sum to t iff the original formula has a 1-in-3-satisfying assignment iff the original formulas is satisfiable. A running time of  $N^{o(k)}$  for k-SUM would give a running time of  $(k2^{n/k})^{o(k)}$  for n variable 3-SAT which would be  $2^{o(n)}$  contradicting the ETH.

## **3** The Strong Exponential-Time Hypothesis (SETH)

The *Strong Exponential-Time Hypothesis (SETH)* is that the sequence of constants  $s_k$  for k-SAT given by Corollary 2.3 satisfies  $\lim_{k\to\infty} s_k = 1$  or, equivalently, that for every  $\varepsilon > 0$  there is a k such that k-SAT requires time at least  $2^{(1-\varepsilon)n}$ .

SETH was stated as a possibility at the end of the original Impagliazzo-Paturi paper that defined the ETH. Since our best upper bounds on  $s_k$  are at least 1 - O(1/k), SETH seems consistent with the best algorithms we know.

Unlike the ETH, which we have shown is very robust w.r.t. the choice of NP-complete problems, SETH is specialized to satisfiability problems. Nonetheless, we will see that SETH has many strong and surprising consequences.

#### **Orthogonal Vectors**

The (Boolean) Orthogonal Vectors (OV) problem takes as input a set of *n* vectors in  $\{0, 1\}^d$  and asks whether there is a pair of vectors *a*, *b* in the set such that the inner product  $a \cdot b = 0$  over the integers.

The obvious algorithm for OV takes time  $\Theta(n^2d)$  by simply computing all of the  $\binom{n}{2}$  inner products. The following theorem of Ryan Williams shows that SETH implies that this is nearly optimal for vectors of  $O(\log n)$  bits each.

**Theorem 3.1.** SETH implies that for every  $\varepsilon > 0$ , there is a constant c > 0 such that OV with  $d \le c \log_2 n$  requires time at least  $n^{2-\varepsilon}$ .

*Proof.* By SETH, we can choose *n* and *k* sufficiently large that *k*-SAT on sparse *n* variable formulas requires time at least  $2^{(1-\varepsilon/3)n}$ . We can assume that these hard formulas have  $m = O_k(n)$  clauses and that *n* is even. The basic idea will be to split the variables of such a formula *F* into two parts  $V_1$  and  $V_2$  of size n/2 and associate one input vector with each partial truth assignment to  $V_1$  or  $V_2$  respectively. Each input vector will have length m + 2.

A vector will begin with 01 if it corresponds to an assignment to  $V_1$  and 10 if it corresponds to an assignment to  $V_2$ . For each of the remaining *m* positions, the vector will have a 0 in position *j* if the partial assignment satisfies the *j*-th clause of *F* and will have a 1 in position *j* if it does not. Given a formula *F*, the set of all such vectors can be computed in time O(Nm).

There are a total of  $N = 2^{n/2+1}$  vectors and m + 2 is  $O_k(\log N)$  and hence  $O_{\varepsilon}(\log N)$ .

Correctness is easy to see: If there is a satisfying assignment to F then we choose the pair of vectors corresponding to this assignment. Each clause will be satisfied by one or both halves of the assignment so at least one of the two vectors must have a 0 in the clause position and the first two bits of the two vectors each have one 0.

Conversely, if there are two orthogonal vectors, since there are no cancellations, they must correspond to assignments to opposite halves of input bits and, because they are orthogonal, one or the other half of the assignment (or both halves) must satisfy every clause.

An OV algorithm running in time  $N^{2-\varepsilon}$  on these inputs would therefore give a *k*-SAT algorithm running in time time  $(2^{n/2+1})^{2-\varepsilon}O(m) = 2^{n(1-\varepsilon/2)+2-\varepsilon}O(m) < 2^{n(1-\varepsilon/3)}$  since *n* is sufficiently large, contradicting our choice of *k*.

Though the Orthogonal Vectors problem appears to have nothing at all to do with k-SAT, this reduction shows that one can view any algorithm for OV as an algorithm for k-SAT.

Rather than having just one set, for OV, we often find it convenient to separate the two sets of vectors into U and V of size N and m coordinates each and the problem asks whether there is a pair of orthogonal vectors, one from U and one from V. Clearly the same reduction shows the same lower bound based on SETH.

#### Approximating Diameter

We can use the two set variant of OV to get a lower bound for graph problems: Consider the problem of computing the *diameter* of an undirected graph with n vertices and m edges. By computing BFS from each vertex gives an O(mn) algorithm for diameter. Can we do better if we only want to approximate it?

**Theorem 3.2.** SETH implies that one cannot decide diameter 2 versus diameter 3 on n vertex *m*-edge (for  $m = O(n \log n)$ ) in less than  $mn^{1-\varepsilon}$  time for any  $\varepsilon > 0$ .

*Proof.* We use the split version of OV with  $d = O(\log n)$ . We have one node for each element of *U*, one for each element of *V*, one node for each of the set *C* of *d* coordinates and two extra nodes *X* and *Y* that are neighbors. *X* is joined to every element of *U* and *C*. *Y* is joined to to every element of *V* and *C*. So far the graph has diameter exactly 3 since *U* to *V* has exactly distance 3 and *C* has distance 2 from *U* and *V*. Now we join every vector  $u \in U$  with  $u_j = 1$  to node *j* in *C* and every vector  $v \in V$  with  $v_j = 1$  to node *j* in *C*.

The resulting graph has O(n) vertices and  $m = O(nd) = O(n \log n)$  edges. If every pair  $u \in U$  and  $v \in V$  have a shared 1, the diameter will be 2; if there is an orthogonal pair  $u \cdot v = 0$ , the diameter will be 3. An algorithm deciding diameter 2 versus diameter 3 running in time  $mn^{1-\varepsilon}$  would be time  $O(n^{2-\varepsilon} \log n)$  algorithm for split OV, violating SETH.

### **Fine-grained reductions**

The reduction from SETH to OV is of a radically different form from our usual format that involves only small changes in input sizes based on complexity bounds. The Sparsification Lemma also involved a very different form of reduction in that we took one problem instance, a n-variable m-clause k-CNF formula and produced a larger number of n variable sparse k-CNF formulas in order to reduce ETH to sparse ETH.

The notion of fine-grained reduction incorporates both of these ideas along with the idea that we only care about the high-order part of the complexity for each problem:

**Definition 3.3.** Given computational problems *A* and *B* and complexity bounds  $a, b : \mathbb{N} \to \mathbb{R}^+$ , we say that *A* (a, b)-reduces to *B* iff for every  $\varepsilon > 0$  there is a  $\delta > 0$  such that there is an  $O(a(n)^{1-\delta})$  time algorithm that takes an input *x* of size *n*, and produces  $y_1, \ldots, y_k$  of sizes  $n_1, \ldots, n_k$  such that  $x \in A$  iff every  $y_j \in B$  and  $\sum_{i=1}^k b(n_j)^{1-\varepsilon} \le a(n)^{1-\delta}$ .

The following is essentially immediate from the definition.

**Proposition 3.4.** If *B* can be solved in time  $b(n)^{1-\varepsilon}$  for some  $\varepsilon > 0$ , and *A* (*a*, *b*)-reduces to *B* then *A* can be solved in time  $O(a(n)^{1-\delta})$  for some  $\delta > 0$ .

In particular, we have shown that sparse CNF-SAT  $(2^n, n^2)$ -reduces to OV on  $O(\log n)$ -bit words.

## 4 Algorithms for Orthogonal Vectors?

Write  $OV_{n,d}$  for the split Orthogonal Vectors problem where we have two set U and V of n vectors in  $\{0,1\}^d$ . We've shown that for every  $\varepsilon > 0$ , SETH implies that  $OV_{n,d}$  requires time  $n^{2-\varepsilon}$  time for  $d \ge c_{\varepsilon} \log n$  for some constant  $c_{\varepsilon} > 0$ . In particular SETH also implies that there is no  $n^{2-\varepsilon} \operatorname{pol} y(d)$  algorithm or indeed  $n^{2-\varepsilon} 2^{o(d)}$  algorithm for  $OV_{n,d}$ .

Other related problems: Subset-Query: Given a collection  $S_1, \ldots, S_n \subseteq [d]$  and a database  $\mathcal{D}$  of *n* subsets of [d], is there a set  $T \in \mathcal{D}$  such that  $S_i \subseteq T$ ?

Partial-Match: Given *n* queries  $x_1, \ldots, x_n \in \{0, 1, \star\}^d$  and a size *n* database  $\mathcal{D} \subseteq \{0, 1\}^d$ , is there a  $y \in \mathcal{D}$  that matches some  $x_i$  in all of  $x_i$ 's non- $\star$  positions.

#### Lemma 4.1. OV, Subset-Query, and Partial-Match problems are equivalent up to a factor 2 in d.

*Proof.* For the reductions between OV and Subset-Query, observe that we can identify the sets  $S_1, \ldots, S_n$  with their characteristic vectors in the set U and identify the elements of  $\mathscr{D}$  with their complement vectors in V. The condition that u and v are orthogonal implies that whenever  $u_i = 1$ , we must have  $v_i = 0$  and therefore the complement vector  $\overline{v}$  has  $\overline{v}_i = 1$ , which is precisely the condition for containment.

We first reduce OV to Partial-Match, by replacing every 0 in a vector  $u \in U$  by  $\star$  to get the corresponding  $x_j$  vector which is in  $\{\star, 1\}^d$  and replace each  $v \in V$  by  $\overline{v}$  to get  $\mathcal{D}$ . The OV property ensures that some element of  $\mathcal{D}$  has 1's wherever the corresponding  $x_j$  does which is a partial match.

Reducing Partial-Match to OV, we use two coordinates for each coordinate of each  $x_j$  and each element of  $\mathcal{D}$ : A 0 in the  $x_j$  in Partial-Match becomes 01, a 1 becomes 10 and a  $\star$  becomes 00. On the other hand, a 0 in  $\mathcal{D}$  becomes 10 and a 1 becomes 01. This ensures that the  $\star$  coordinates in Partial-Match can never cause non-trivial inner product but that other values will match iff they do not cause non-trivial inner product.

**Lemma 4.2.** Let  $\varepsilon > 0$ . For  $d \leq (1 - \varepsilon) \log n$ ,  $OV_{n,d}$  can be solved in time  $\tilde{O}(n^{2-2\varepsilon})$ .

*Proof.* With these parameters there are at most  $n^{1-\varepsilon}$  distinct numbers in each of the lists. We simply mark all elements of  $\{0,1\}^d$  that appear in each list and compare the pairs. This takes time O(dn) to compute the two lists and  $d \cdot 2^{2d}$  to compare all the pairs which is  $O(2^{2-2\varepsilon} \log n)$ .

What if the dimension *d* is  $c(n)\log n$  for some function  $c(n) \ge 0$ ? The following is the best algorithm known for  $OV_{n,d}$  due to Abboud, Williams, and Yu. It uses some clever tricks to speed things up based on probabilistic polynomials.

**Theorem 4.3.**  $OV_{n,d}$  for  $d = c(n)\log n$  can be solved in time  $n^{2-\Theta(1/\log c(n))}$  by a randomized algorithm with small error.

The algorithm will show that for sufficiently small sets of vectors, we can repeat the computation of the solution of  $OV_{s,d}$  so that the total cost is much less than doing it  $O(n^2/s^2)$  times. We split the  $OV_{n,d}$  problem into  $\theta(n^2/s^2)$  subproblems of the form  $OV_{s,d}$  as follows: Split *U* and *V* into  $q = \lceil n/s \rceil$  sets  $U_1, \ldots, U_q$  and  $V_1, \ldots, V_q$  where each  $U_i$  and  $V_i$  has size *s* and solve all  $q^2$  subproblems.

We can express the subproblem involving  $U_i$  and  $V_j$  as a depth 3 circuit involving the bits of the vectors in the two sets: In particular there is a big OR of width  $s^2$  at the top, then an AND of width d for the coordinates and a  $\neg u_k \lor \neg v_k$  for each  $k \in [d]$  for each  $u \in U_i$  and  $v \in V_i$ .

The algorithm will first replace this depth 3 formula using low-degree polynomials and the following result of Razborov that was used by Razborov and Smolensky to prove circuit lower bounds for  $AC^{0}[2]$  circuits.

**Lemma 4.4.** For  $\ell > 0$ , and randomly chosen  $r_{ij} \in \{0, 1\}$  for  $i \in [t]$  and  $j \in [\ell]$ , define  $\mathbb{F}_2$  polynomials  $O_t(y_1, \ldots, y_\ell) = 1 + \prod_{i=1}^t (1 + \sum_{j=1}^\ell r_{ij}y_j)$  and  $A_t(y_1, \ldots, y_\ell) = \prod_{i=1}^t (1 + \sum_{j=1}^\ell r_{ij}(1 + y_j))$ . Then for all  $y_1, \ldots, y_\ell$ ,  $\Pr_r[O_t(y_1, \ldots, y_\ell) \neq \bigvee_{i=1}^\ell y_j] \leq 2^{-t}$  and  $\Pr_r[A_t(y_1, \ldots, y_\ell) \neq \bigwedge_{i=1}^\ell y_j] \leq 2^{-t}$ .

*Proof.* If  $\bigvee_{j=1}^{\ell} y_j = 0$  then  $O_t(y_1, \dots, y_\ell) = 0$  since every  $\sum_{j=1}^{\ell} r_{ij} y_j = 0$ . Now suppose that  $\bigvee_{j=1}^{\ell} y_j = 1$ . Then there is some j such that  $y_j = 1$ . For each fixed  $r_{ik}$  for  $k \neq j$ , exactly one of the two choices of  $r_{ij}$  will make  $\sum_{k=1}^{\ell} r_{ik} y_k = 1$  since  $y_j = 1$ , so a single term in the product will be 0 with probably 1/2. The t terms are independent so the probability that the product is not 0 will be  $2^{-t}$  and hence the polynomial will be 1 with probability exactly  $1 - 2^{-t}$  as required. The properties of polynomial  $A_t$  follow since  $AND(y_1, \dots, y_\ell) = OR(\overline{y}_1, \dots, \overline{y}_\ell)$  and  $\overline{y}_i = 1 + y_i$  as an  $\mathbb{F}_2$  polynomial.

We will choose a probabilistic polynomial approximating  $OV_{s,d}$  as follows: For the bottom gates  $\neg u_j \lor \neg v_j$ , we write the polynomial  $1 + u_j v_j$  and feed this into the polynomials for the higher level gates. For each of the fan-in *d* AND gates at level 2 of the circuit for  $OV_{s,d}$  we choose  $t = \lceil 3 \log s \rceil$  and for the fan-in  $s^2$  top gate we choose t = 2. There are  $s^2$  second level AND gates so by a union bound, for each fixed set of inputs, the probability that there is an error in any second level gates is at most  $s^2/s^3 = 1/s$ . The probability that the output gate computes an incorrect value given correct inputs is at most 1/4, so the total error probability is at most  $1/4 + 1/s \le 1/3$  for  $s \ge 12$ . (We can reduce this error probability to polynomially small by repeating independent trials  $O(\log n)$  times and take the majority answer. Note that the errors go in both directions.)

When we expand the resulting polynomial for  $OV_{s,d}$ , it will be important to understand the number of monomials we get. When expanded, the polynomials  $A_t$  and  $O_t$  are multilinear and have degree t in  $\ell$  variables and therefore has at most  $\sum_{i=0}^{t} {\binom{\ell}{i}} \leq (\ell e/t)^t$  distinct monomials. At the middle level we observe that  $t = 3 \log s$  and  $\ell = d$ . The level 1 monomials are of the form  $1+u_jv_j$  which means that there are still only  $(ed/3 \log s)^{3\log s}$  monomials in the resulting formula (since the  $u_j$  and  $v_j$  variables are always paired with each other). Finally, these monomials are substituted into the top level polynomial which has t = 2 and  $\ell = s^2$  and only  $O(s^4)$  monomials of degree 2. The final number of monomials M(s, d) is therefore  $O(s^4(de/(3 \log s)^{6\log s}))$  which is at most  $s^5(d/\log s)^{6\log s}$ .

This number of monomials can be much bigger than the original  $O(s^2d)$  circuit size depending on the relationship between *s* and *d*. How do we get a savings? The idea will be that

computing this polynomial on  $q^2$  pairs of inputs will be less expensive than simply repeating it  $q^2$  times.

The underlying idea of this algorithm will be to use a particular form of fast matrix multiplication due to CopperSmith that we will use to multiply polynomials with few monomials quickly:

**Theorem 4.5** (Coppersmith 1982). One can multiply an  $N \times N^{0.172}$  matrix by an  $N^{0.172} \times N$  matrix in  $O(N^2 \log^2 N)$  arithmetic operations.

**Corollary 4.6.** Given a polynomial  $P(x_1, ..., x_\ell, y_1, ..., y_\ell)$  over  $\mathbb{F}_2$  with at most  $N^{0.1}$  monomials and two list of vectors  $U = \{u_1, ..., u_N\} \subseteq \{0, 1\}^\ell$  and  $V = \{v_1, ..., v_N\} \subseteq \{0, 1\}^\ell$  we can compute all the values  $P(u_i, v_j)$  for  $u_i \in V$  and  $v_j \in V$  in time  $\tilde{O}(N^2)$ .

*Proof.* Let *m* be the number of monomials of *P*. Create an  $N \times m$  matrix with each row *r* corresponding to an element of *U* with the value of the *j*-th entry equal to the value of the part of the *j*-th monomial involving  $U_r$ . Similarly define an  $m \times N$  matrix using the same monomial order for the rows and with *c*-th column having entry *j* that is the value of the part of the *j*-th monomial involving  $V_c$ . The matrices can be constructed in time  $Nm\ell$ . Using Coppersmith's algorithm, their matrix-product can be produced in time  $\tilde{O}(N^2)$  and the entry (i, j) of their product taken modulo 2 clearly contains  $P(u_i, v_j)$  by construction.

In our calculations we will have  $N = q = \lceil n/s \rceil$  and the number of monomials m = M(s, d) which is at most  $s^5(d/3\log s)^{6\log s}$ .

We set  $s = n^{\delta/\log c(n)}$  for some small  $\delta > 0$ . Then  $\log s$  is  $\delta(\log n)/\log c(n) = \delta d/(c(n)\log c(n))$ . Hence  $d/\log s$  is at most some constant times  $c(n)\log c(n)$ . When we raise this to the power  $6\log s$  we get some  $2^{\delta'\log n}$  for a small  $\delta' > 0$  which is  $n^{\delta'}$ . Putting this all together, even when multiplied by  $s^5$  we get that the result is less than  $(n/s)^{0.1}$ . By the lemma, the total cost is  $\tilde{O}(q^2) = \tilde{O}(n^2/s^2)$  which is  $n^{2-\Theta(1/\log c(n))}$ .

Chan and Williams (SODA 2016, ACM ToA 2021) showed how to compute deterministic polynomials that give correct modular counts of the sum of many OR functions and these polynomials can be used instead of the probabilistic polynomials used here.

## 5 More on ETH and Parameterized Complexity

Many NP-complete problems have natural parameters in addition to the input size. For example, we can consider the *k*-CLIQUE problem that asks whether there is a clique of size *n* in an input graph. We can do the same thing with *k*-INDEPENDENT-SET, *k*-DOMINATING-SET, *k*-VERTEX-COVER. Each of these has an  $O(n^k)$  algorithm that is polynomial in *n* for each fixed value of *k*. We identify these parameterized problems as *fixed-parameter tractable* iff there is an algorithm with running time  $f(k)n^{O(1)}$  for some function *f*. These algorithms will run efficiently even for large *n* when *k* is small.

**Lemma 5.1.** There is an algorithm for k-VERTEX-COVER with running time  $O(2^k n)$ .

*Proof.* We define a simple binary search tree of height *k* as follows:

If *G* has no edges halt and accept. Else if k = 0 return (failed branch) Else Choose some edge (u, v) in *G*. Try vertex *u* in the cover: Recursively search for a vertex cover of size k - 1 in  $G \setminus N(u)$ . Try vertex *v* in the cover: Recursively search for a vertex cover of size k - 1 in  $G \setminus N(v)$ .

Clearly this algorithm takes linear time to modify *G* at each step and has only  $O(2^k)$  calls.

Exercise: Show that ETH implies that *k*-VERTEX-COVER cannot be solved in time  $2^{o(k)}n^{O(1)}$ .

On the other hand we can prove that ETH implies that k-CLIQUE is not fixed-parameter tractable in a strong sense. This was shown by Chen, Chor, Fellows, Huang, Juedes, Kanj, and Xia:

**Theorem 5.2** (Chen et al. 2004). *ETH implies that there is no*  $f(k)n^{o(k)}$  algorithm for k-CLIQUE.

*Proof.* We have seen that ETH implies that there is  $2^{o(n)}$  algorithm for 3-COLOR. Suppose that there is an  $f(k)n^{k/\alpha(k)}$  algorithm for *k*-CLIQUE for some function  $\alpha(k)$  that goes to infinity with *k*. Define k(n) = the largest value of *k* such that  $f(k) \le n$  and  $k^{k/\alpha(k)} \le n$ . Clearly, k(n) is monotone increasing goes to infinity with *n*. We will set k = k(n).

Given a graph *G* on *n* vertices, we split the vertices of *G* into *k* groups of size n/k. We define a new graph *H* where each vertex of *H* corresponds to a 3-coloring of one of the *k* groups of vertices of *G*. *H* has  $k3^{n/k}$  vertices. We connect each pair of vertices of *H* iff the colorings don't conflict with respect to *G*. (Vertices for partial colorings that themselves are not consistent with *G* will be isolated.) It is easy to see that *H* has a *k* clique iff *G* has a proper 3-coloring.

The running time of the presumed *k*-CLIQUE algorithm will be  $f(k)(k3^{n/k})^{k/\alpha(k)} \le nk^{k/\alpha(k)}3^{n/\alpha k} \le n^23^{n/\alpha k(n)}$ . Since k(n) goes to infinity with n,  $\alpha(k(n))$  goes to infinity with n so this is a  $2^{o(n)}$  algorithm contradicting the ETH.

Using standard fixed-parameter reductions that are polynomial in the input size and change the parameter by at most a constant factor, one can get lower bounds for other problems also.

**Corollary 5.3.** ETH implies that there is no  $f(k)n^{o(k)}$  algorithm for k-SET-COVER, k-HITTING-SET, k-BIPARTITE-DOMINATING-SET, k-CONNECTED-DOMINATING-SET.

### 6 Longest-Common Subsequence

At STOC 2015, Backurs and Indyk showed that another problem that OV reduces to is is Edit Distance. Later at FOCS 2015, Abboud, Backurs, and Vassilevska-Williams, and Bringman and Kunneman extended this to finding the length of the *Longest Common Subsequence (LCS)*, with the latter showing that this hardness extends to the case of the binary alphabet. LCS

is equivalent to a special case of Edit Distance in which the cost of insertion or deletion is 1 and the cost of substitution is 2. More formally, given strings  $A, B \in \Sigma^*$  define LCS(A, B) to be the maximum k such that there are sequences  $i_1 < \ldots < i_k$  and  $j_1 < \ldots < j_k$  for which  $A_{i_1} = B_{i_1}, \ldots, A_{i_k} = B_{j_k}$ .

There are simple natural dynamic programming algorithm for Edit Distance (and hence LCS) that run in time  $O(n^2)$  where |A| = |B| and the best algorithms known only shave off a log *n* factor.

**Theorem 6.1.** SETH implies that LCS over binary strings does not have an  $O(n^{2-\delta})$  algorithmn for any  $\delta > 0$ .

The proof of this begins by looking at a problem closer to OV. Define LCS- $PAIR_{N,m}$  to be the problem: Given sequences  $a_1, \ldots, a_N, b_1, \ldots, b_N \in \Sigma^m$ , find the  $\max_{i,j} LCS(a_i, b_j)$ . We describe the reduction from  $OV_{N,m}$  to LCS-Pair along the lines given by Bringman and Kunneman.

**Lemma 6.2.**  $OV_{N,m}$  reduces in linear time to LCS-PAIR<sub>N,cm</sub> for some constant c.

*Proof.* The general idea is to produce a local substitution of each character of  $u_i$  and  $v_j$  according to different substitutions.

Define the strings  $0_u = 10011$ ,  $1_u = 11100$ ,  $0_v = 11001$ , and  $1_v = 00111$ . Observe that  $LCS(0_u, 0_v) = LCS(0_u, 1_v) = LCS(1_u, 0_v) = 4$  but  $LCS(1_u, 1_v) = 3$ .

We want to ensure that any LCS for  $a_i$  and  $b_j$  involves a character-by-character match of  $u_i$  and  $v_j$  in  $\{0, 1\}^m$ . To do this we define  $code_u(u_i)$  to be the string where we replace each 0 or 1 of  $u_i$  by the corresponding  $0_u$  or  $1_u$  and we separate each pair by, say, a string of three 2's; do the same for  $code_v(v_j)$  except we use  $0_v$  and  $1_v$  instead. Define  $a_i = code_u(u_i)$  and  $b_j = code_v(v_j)$ . In particular, if  $u_i = 001$  and  $v_j = 011$  then

$$a_i = code_u(001) = 100112221001122211100$$
  
 $b_i = code_v(011) = 110012220011122200111$ 

Therefore the total string length m' is 8m - 3. It is clear that every LCS of  $a_i$  and  $b_j$  and must match all the 2's, which means that corresponding coordinates much be matched. If  $u_i$  and  $v_j$  are orthogonal then  $LCS(a_i, b_j) = 4m + 3(m - 1) = 7m - 3$ , which we denote by S. On the other hand, if  $u_i$  and  $v_j$  are not orthogonal then the contribution is only 3 instead of 4 in all the coordinates with common 1's and hence  $LCS(a_i, b_j) \le S - 1$ . The reduction simply compares the length of the LCS to S.

To obtain a lower bound for LCS, we will need to concatenate these strings to a single pair of strings *A* and *B* so that the length of the LCS of the whole string will be larger iff there is *some* pair of orthogonal  $u_i$  and  $v_j$ . To do this we need to control things so that we know the contribution of each the failed matches also. With the above construction, the more overlapping 1's, the worse the value. To fix this we use a slight modification of the above construction that always guarantees a match of precisely 1 less than the maximum possible.

To do this we add an extra dummy coordinate to the encoding. Define  $code'_u(u_i) = code(u_i0)$ and  $code'_v(v_i) = code(v_i1)$  as well as an extra "easy string"  $e = code(0^m1)$ . Observe that  $(u_i, 0)$  and  $(v_j, 1)$  are orthogonal iff  $u_i$  and  $v_j$  are. Also every vector  $(v_j, 1)$  has precisely one coordinate with overlapping 1's with  $(0^m, 1)$ . Let m'' = 8m + 5 be the length of  $code'_u(u_i)$ . Now define

$$a'_{i} = code'_{u}(u_{i})3^{m''}e$$
  
 $b'_{j} = 3^{m''}code'_{v}(v_{j})3^{m''}$ 

The total length of each of  $a'_i$  and  $b'_j$  is  $\ell = 3m'' = 24m + 15$ . Observe that any LCS for the two strings must match one of the two groups of 3's in  $b_j$  in its entirely to the middle group of 3's in  $a_i$  and then include an LCS between  $code'_v(v_j)$  and either  $code'_u(u_i)$  of e. If  $u_i$  and  $v_j$  are orthogonal then we get a total contribution of S' = m'' + S + 7 where S is the value from the above lemma since the extra coordinate gives a total contribution of 7. In particular S' = 8m + 5 + 7m - 3 + 7 = 15m + 9. In the latter case there is precisely 1 segment where the contribution is 3 instead of 4 so we get m'' + S + 6 = S' - 1 (there is a contribution of 3 for the matching 2's and another 3 from the LCS between  $1_u$  and  $1_v$ ).

We now are in a position to describe the construction of the strings *A* and *B* for the proof of the theorem. The idea will be that the contribution will be to allow an arbitrary rotated alignment of the combined string of encodings  $b_1, \ldots, b_N$ , suitably separated, with the the string of encodings  $a_1, \ldots, a_N$ . This will necessitate two copies of one of the strings to allow for the orthogonal pair  $(u_i, v_j)$  to have i > j as well as  $i \le j$ .

We define the strings

$$A = a'_{1} 4^{\ell} a'_{2} 4^{\ell} \dots 4^{\ell} a'_{N} 4^{\ell} a'_{1} 4^{\ell} a'_{2} 4^{\ell} \dots 4^{\ell} a'_{N}$$
  
$$B = 4^{N\ell} b'_{1} 4^{\ell} b'_{2} 4^{\ell} \dots 4^{\ell} b'_{N} 4^{N\ell}.$$

In particular, both  $|A| = |B| = (4N - 1)\ell$  which is O(Nm). A has  $(2N - 1)\ell$  4's and  $2N\ell$  characters from the  $a'_i$  which is O(Nm), while *B* has has  $N\ell$  characters from the  $b'_j$  and  $(3N-1)\ell$  4's. Clearly one can match all of the  $(2N - 1)\ell$  4's in *A* in *B*.

If some pair  $u_i$  and  $v_j$  are orthogonal we can get a common subsequence as follows: Write  $\Delta = i - j + 1$ . We get a common subsequence by aligning the subsequence  $b_1 4^{\ell} \dots 4^{\ell} b_N$  with some  $a_{\Delta} 4^{\ell} \dots 4^{\ell} a_{\Delta-1}$  where we write  $a_k = a_{N+k}$  for  $k \leq 0$ . and matching up all the 4's outside of the subsequence in *A* with the 4's at the beginning and end of *B*. Observe that this aligns  $a'_i$  and  $b'_j$ . In total, this matches all  $4^{(2N-1)\ell}$  4's in *A* plus *S'* of the characters in  $a'_i$  and  $b'_j$  and at least S' - 1 characters for each of the other N - 1 positions for a total LCS length of  $\geq S'' = (2N - 1)\ell + NS' - (N - 1)$ .

On the other hand, suppose that there is no pair of orthogonal elements  $u_i$ ,  $v_j$ . Consider any LCS of *A* and *B* and consider how it matches up characters of *B* inside *A*. If there is some  $b'_j$  that has characters matching with more than one of the  $a'_i$  strings then it must NOT match any of the  $\ell$  4's in between them. This costs  $\ell$  in terms of the 4's but could potentially increase the number of matches inside the string  $b'_j$ , but since matching  $b'_j$  to just one of the  $a'_i$  would already give S'-1 so in the best case the amount of increase would be at most  $\ell - S' + 1$  which is strictly less than the loss of 4's. Therefore, any LCS of *A* and *B* must match and  $(2N-1)\ell$ 4's in *A* and each  $b'_i$  to at most one of the  $a'_i$  strings. No matter which *i* it is matched to, the longest part of the LCS inside  $b'_j$  can be at most S' - 1, so the the total length of the LCS is at most  $(2N - 1)\ell + N(S' - 1) = S'' - 1$ . (This length is actually achievable.)

Therefore,  $OV_{N,\log_2^2 n}$  is reducible in time  $O(n) = O(N^2 \log^2 n)$  to LCS on length *n* strings over  $\{0, 1, 2, 3, 4\}$ . This proves the theorem except for the reduction of the alphabet size to binary. That reduction requires a more subtle way to put the various strings in order to replace the symbols 2, 3, 4 by binary strings.

#### Encoding LCS using binary strings instead

The idea, due to Bringman and Künneman, is quite general. It assumes that we have two sets of binary codes, one for  $\mathscr{C}_x$  and one for  $\mathscr{C}_v$  with the following properties:

- $\mathscr{C}_x \subset \{0,1\}^{\ell_x}$  and has  $s_x$  1's.
- every string in  $\mathscr{C}_v \subset \{0, 1\}^{\ell_y}$  and has  $s_v$  1's.

For example,  $\mathscr{C}_x = \{0_u, 1_u\}$  we have  $\ell_x = 5$  and  $s_x = 3$  and  $\mathscr{C}_y = 0_v, 1_v$  has  $\ell_y = 5$  and  $s_y = 3$ . We saw that for single bits u and v,  $x = code_u(u) \in \mathscr{C}_x$  and  $y = code_v(v) \in \mathscr{C}_y$ , LCS(x, y) = 4 if  $u \cdot v = 0$  but LCS(x, y) = 3 if  $u \cdot v = 1$ .

The general idea is to build an *alignment gadget* to encode longer strings of elements from  $\mathscr{C}_x$  and  $\mathscr{C}_y$  such that the best way to build an LCS must involve matching individual codes. This will be done by separating these codes using very long blocks of 0's and very long blocks of 1's.

Define  $\gamma_1 = \ell_x + \ell_y$ ,  $\gamma_2 = 6(\ell_x + \ell_y)$ ,  $\gamma_3 = 10(\ell_x + \ell_y) + 2s_x - \ell_x$  and  $\gamma_4 = 13(\ell_x + \ell_y)$ .

For a string  $z \in \mathscr{C}_x \cup \mathscr{C}_y$ , we write  $G(z) = 1^{\gamma_2} 0^{\gamma_1} z 0^{\gamma_1} 1^{\gamma_2}$ . Observe that for  $x \in \mathscr{C}_x$  and  $y \in \mathscr{C}_y$ ,  $LCS(G(x), G(y)) = LCS(x, y) + 2\gamma_1 + 2\gamma_2 = LCS(x, y) + 14(\ell_x + \ell_y)$ . This follows because there is no advantage to NOT matching the corresponding strings of 0's and 1's at the beginning and end of the G(x) and G(y).

For  $n \ge m, x_1, \dots, x_n \in \mathscr{C}_x$  and  $y_1, \dots, y_m \in \mathscr{C}_y$  define

$$x = G(x_1)0^{\gamma_3}G(x_2)0^{\gamma_3}\cdots 0^{\gamma_3}G(x_n)$$
(2)

$$y = 0^{n\gamma_4} G(y_1) 0^{\gamma_3} G(y_2) 0^{\gamma_3} \cdots 0^{\gamma_3} G(y_m) 0^{n\gamma_4}.$$
 (3)

Observe that since  $x_i$  has  $s_x$  1's so  $G(x_i)0^{\gamma_3}$  so it has  $2\gamma_2 + s_x = 12(\ell_x + \ell_y) + s_x$  1's. It also has  $2\gamma_1 + \ell_x - s_x + \gamma_3 = 2(\ell_x + \ell_y) + \ell - s_x + 10(\ell_x + \ell_y) + 2s_x - \ell_x = 12(\ell_x + \ell_y) + s_x$  0's, so it is balanced. (Further observe that this is at most  $\gamma_4$ .) Also, every prefix of  $G(x_i)0^{\gamma_3}$  has at least as many 1's as 0's since  $G(x_i)$  begins with  $\gamma_2$  1's and  $G(x_i)$  has at most  $2\gamma_1 + \ell_x - s_x < \gamma_2$  0's.

By our observation, x has fewer than  $n[12(\ell_x + \ell_y) + s_x]$  0's which is at most  $n\gamma_4$ .

We want to claim that the best LCS of x and y is given by the max over all choices of  $0 \le \Delta \le n-m$  of the matching that aligns the  $G(x_{\Delta+1})0^{\gamma_3}\cdots 0^{\gamma_3}G(x_{\Delta+m})$  with  $G(y_1)0^{\gamma_3}\cdots 0^{\gamma_3}y_m$  and matches all the remaining 0's in x with the 0's at the beginning and end of y, since there are enough of them in total. That number of extra 0's matched at the ends of such an alignment is independent of the choice of  $\Delta$ , since the number of 0's in each  $x_i$  is exactly the same, and

the total length of the LCS is simply some fixed value plus  $\sum_{k=1}^{m} LCS(x_{i+\Delta}, y_i)$ , which is the sum of the shifted LCS alignments of the original encodings.

It remains to show that no other alignment can give a better LCS than one of these alignments  $\Delta$ . Consider some other alignment of x with y. Now for each of the blocks  $G(y_1), \ldots, G(y_m)$  of y we can define x(j) to be a substring of the x string that contains the portion of the LCS that matches  $G(y_j)$  and z(j) for  $j = 1, \ldots, m-1$  to be the substring of x that aligns with the  $0^{\gamma_3}$  after  $G(y_j)$ . Given this alignment, if x(j) contains more than half of some  $x_i$  string (a piece inside  $G(x_i)$  then, since there might be more than one such  $x_i$ , define  $i^*(j)$  to be the leftmost such i. This function  $i^*$  might not be defined for some values of j but, if it is, then  $i^*(j) > i^*(j')$  for j > j'. Thus  $i^*$  is a partial increasing 1-1 function from [m] to [n].

Claim: If  $i^*(j)$  is defined then there are at least as many characters of not matched between x(j) and  $G(y_j)$  as there are not matched between  $G(x_{i^*(j)})$  and  $G(y_j)$ .

If x(j) touches some  $G(x_i)$  other than  $G(x_{i^*(j)})$  then there was more than one candidate for  $i^*(j)$  then there is huge number of unmatched characters in x(j) since it contains the intervening sequence  $0^{\gamma_1}1^{\gamma_2}0^{\gamma_3}1$  if it is to the right or  $10^{\gamma_3}1^{\gamma_2}0^{\gamma_1}$  if it is to the left which yields a much greater distance number than  $\ell_x + \ell_y$  which is an upper bound on the number of unmatched characters between  $G(x_{i^*(j)})$  and  $G(y_j)$ .

If not, then the string x(j) is contained in  $0^{\gamma_3}G(x_{i^*(j)})0^{\gamma_3}$ . Since  $G(y_j)$  begins and ends with  $\gamma_2 > \ell_x + \ell_y$  1's, x(j) must begin and end with 1's or the LCS with  $G(y_j)$  will have more than  $\ell_x + \ell_y$  unmatched characters. Further x(j) must have more than  $|G(y_j)| - \gamma_2$  characters or there will again be more than  $\gamma_2$  unmatched characters. Note that the right substring  $x_{i^*(j)}0^{\gamma_1}1^{\gamma_2}$  or the left substring  $1^{\gamma_2}0^{\gamma_1}x_{i^*(j)}$  have length  $\ell_x + \gamma_1 + \gamma_2$ , they are too short, so the string x(j) must look like  $1^a 0^{\gamma_1}x_{i^*(j)}0^{\gamma_1}1^b$  for some a and b. It is easy to see that the fewest mismatches would occur when  $a = b = \gamma_2$  which is exactly the case of the claim.

Claim: If  $i^*(j)$  is not defined then x(j) and  $G(y_j)$  have at least  $\ell_x + \ell_y$  mismatches which is at least than the number of mismatches between  $G(y_j)$  and any  $G(x_j)$ .

Since  $i^*(j)$  is not defined then there is some *i* such that x(j) is contained in  $x_i 0^{\gamma_1} 1^{\gamma_2} 0^{\gamma_3} 1^{\gamma_2} 0^{|gamma_1} x_{i+1}$ and contains less than half of  $x_i$  and  $x_{i+1}$  (or an end case where it begins or ends with  $1^{\gamma_2}$ ). If x(j) contains 1's on both sides of the central  $0^{\gamma_3}$  then it contains  $\gamma_3$  0's which exceeds the number in  $G(y_j)$  by more than  $\ell_x + \ell_y$ . If x(j) only contains 1's on one side of the central  $)^{\gamma_3}$ then it contains at most  $s_x + \gamma_2 \le \ell_x + \gamma_2$  1's. However  $G(y_j)$  contains at least  $2\gamma_2$  1's and  $\gamma_2 - \ell_x > \ell_x + \ell_y$  so there are more than  $\ell_x + \ell_y$  unmatched characters.

Since the z(j) are perfectly aligned in the alignments based on  $\Delta$ , the middle sequence of y ignoring the 0's at the ends is always aligned at least as well as before. The bottom line from this is that the total cost is at least as large as one of the consecutive alignments.

### 7 LCS is hard even given a very weak SETH

Abboud, Hansen, Vassilevska-Williams, and Williams at STOC 2016 showed that LCS is hard even if very high complexity *CIRCUIT-SAT* is hard. We sketch a simpler proof of their theorem.

**Theorem 7.1.** If there is no  $2^{n-o(n)}$  algorithm to compute satisfiability for

- depth o(n) circuits,
- size 2<sup>o(n)</sup> Boolean formulas, or
- verifiers given by space  $o(\sqrt{n})$  nondeterministic Turing machines,

then LCS over an alphabet of size  $n^{o(1)}$  does not have an  $O(n^{2-\varepsilon})$  algorithm for any  $\varepsilon > 0$ .

One key observation is that the previous construction did not make use of the full range of parameters possible in constructing *A* and *B*. All we needed for the conclusion is that each  $a_i$  for the *LCS-PAIR* problem has size  $N^{o(1)}$  rather than restricting it to  $O(\log^2 N)$ .

We note that by the usual formula balancing construction, the first two classes are identical. The third follows from the first by the fact that  $NSPACE(S(n)) \subseteq DEPTH(S^2(n))$ .

*Proof Sketch.* We use the same framework to convert from *LCS-PAIR* to *LCS* so we just give the description for the  $a_i$  and  $b_j$  strings for *LCS-PAIR*. We use the same separation of the input variables into *U* and *V* and  $N = 2^{n/2}$  assignments to each player as before. (We will index assignments by  $\alpha$  and  $\beta$  so as not to confuse notation.) We will define  $a_{\alpha}$  and  $b_{\beta}$  recursively. We will define a  $a_{\alpha}^{\nu}$  and  $b_{\beta}^{\nu}$  for each gate  $\nu$  in the circuit. We will first produce them as weight formulas and then argue that the weights can be removed. We will assume wlog that the depth o(n) circuit has been converted to a balanced formula in which all negations have been pushed to the leaves and each gate at depth *k* has two predecessors at depth k - 1.

We create  $a_{\alpha}^{\nu}$  and  $b_{\beta}^{\nu}$  by induction on  $k = depth(\nu)$ . We will construct these so that  $LCS(a_{\alpha}^{\nu}, b_{\beta}^{\nu})$  is maximal iff  $\nu$  evaluates to 1 on input  $(\alpha, \beta)$ .

Suppose that k = 0. Then *u* is labelled by a literal  $\ell_i$  that is either  $x_i$  or  $\neg x_i$ . Set

$$a_{\alpha}^{u} = \begin{cases} * & \text{if } i > n/2 \text{ or } \ell_{i}(\alpha) = 1 \\ \$ & \text{otherwise,} \end{cases}$$

and

$$b_{\beta}^{u} = \begin{cases} * & \text{if } i \leq n/2 \text{ or } \ell_{i}(\beta) = 1 \\ \# & \text{otherwise,} \end{cases}$$

We will ensure that none of the *a* strings contain # and none of the *b* strings contain \$. Clearly both will have a \* iff literal  $\ell_i$  is set to true on assignment ( $\alpha, \beta$ ).

Now suppose that *u* has children *v* and *w* at depth k - 1. If  $u = v \land w$  then define

$$a^{u}_{\alpha} = P_{k}a^{v}_{\alpha}Q_{k}a^{w}_{\alpha}P_{k}$$
$$b^{u}_{\beta} = P_{k}b^{v}_{\beta}Q_{k}b^{w}_{\beta}P_{k}$$

where  $P_k$  and  $Q_k$  are new symbols of weight  $W_k = 3^k$ . Clearly any optimal LCS for this pair must align all of the  $P_k$  and  $Q_k$  and hence will have a maximal LCS iff both  $LCS(a^{\nu}_{\alpha}, b^{\nu}_{\beta})$  and

 $LCS(a^w_{\alpha}, b^w_{\beta})$  are maximal. If  $u = v \lor w$  then define

$$a^{u}_{\alpha} = P_{k}a^{v}_{\alpha}Q_{k}a^{w}_{\alpha}P_{k}$$
$$b^{u}_{\beta} = Q_{k}b^{w}_{\beta}P_{k}b^{v}_{\beta}Q_{k}$$

In this second case, the optimal LCS must match precisely one  $P_k$  and one  $Q_k$  and include the LCS of precisely one of the two pairs  $(a^v_{\alpha}, b^v_{\beta})$  or  $(a^w_{\alpha}, b^w_{\beta})$ . Hence it will be maximal iff at least one of the two matches is maximal. (Note that the target weight of the maximal match is different depending on whether the gate is an  $\lor$  or an  $\land$  gates, but this doesn't matter since we know the target size from the property of the circuit itself. We could alternatively simply use higher weight in the OR gadget to make them equal. We end up with o(n) different symbols.

The weighted  $a_{\alpha}$  and  $b_{\beta}$  strings are the ones for the output gate of the circuit. We can remove all the weights easily by replacing a symbol  $\sigma$  with weight w by w consecutive copies of  $\sigma$  without changing the LCS size. Clearly we can just use entire blocks corresponding the weighted LCS. The claim is that this is the best we can do. Suppose that we have an LCS that does not match things blockwise for  $\sigma$  and consider the leftmost *partial match* of  $\sigma$  between Aand B. That, say matches the *i*-th element in the block in A to the *j*-th element in the block in B. This (i, j) match splits the LCS. Suppose wlog that  $i \leq j$ . Clearly that are at most w - i + 1elements of the LCS that touch either of these two blocks since there is no match in the block to the left of the (i, j) match and matches in the LCS cannot cross each other. We can do at least well (obtaining w matched pairs in the blocks) by locally replacing all of those edges by a full matching of the two blocks.

Note that the total weight for depth *k* is  $c^k$  for some constant *c*, each resulting string has length  $2^{o(n)} = N^{o(1)}$ .

# 8 Circuit-SAT Algorithms Imply Lower Bounds

We consider Boolean circuits over the De Morgan basis consisting of binary  $\land$ ,  $\lor$  and  $\neg$ .

**Theorem 8.1** (Shannon). At least a 1-o(1) fraction of Boolean functions require circuit size at least  $2^n/n - o(2^n/n)$ .

*Proof.* We will not optimize the constants and assume for convenience that the negations have been pushed to the inputs via de Morgan's Law so we have access to literal gates  $x_1, \ldots, x_n, \overline{x_1}, \ldots, \overline{x_n}$ and all internal nodes are labelled via  $\wedge$  and  $\vee$ . We count the number of circuits with  $S \wedge$  and  $\vee$  gates. We can label each internal gate based on its two inputs which are each either gates or literals and its type. There fewer than  $(S+2n)^2$  choices of inputs per gate and 2 choices of gate label so there are  $[2(S+2n)^2]^S$  possible gates and *S* choices for the name of the output gate. In total, this is at most  $S^{O(S)} = 2^{O(S \log S)}$  different circuits of size at most  $S \ge 2n$ . On the other hand, there are  $2^{2^n}$  possible Boolean functions. Therefore for some constant  $\varepsilon > 0$  if  $S \log S$  is at most  $\varepsilon 2^n$ , the number of circuits of size at most *S* is a vanishingly small fraction of all functions. This implies that *S* is  $\Omega(2^n/n)$ . The sharper bounds follows from a more careful count since there are *S*! different ways of naming the internal gates; this shows that the number of distinct functions computed by circuits of size at most *S* is  $S^{S+o(S)}$ .

This is matched by a result of Lupanov.

**Theorem 8.2** (Lupanov). Every Boolean function f on n bits can be computed by a Boolean circuit of size  $2^n/n + o(2^n/n)$ .

Sketch. Observe that over all assignment  $\alpha$  to the last n-k variables there are only  $2^{2^k}$  possible functions among  $f(x_1, \ldots, x_k, \alpha)$ . We can compute all such functions using dynamic programming in total size at most  $2^{2^k}$ .

We now build up the Boolean functions on longer prefixes that we need using  $F(x_1, \ldots, x_{m+1}, \beta) = \neg x_{m+1} \land F(x_1, \ldots, x_m, 0, \beta) \lor x_{m+1} \land F(x_1, \ldots, x_m, 1, \beta)$ .) This add  $O(2^{n-k}) = O(2^n/2^k)$  gates in total. We choose k such that  $2^{2^k}$  is roughly  $2^n/n$ . In that case  $2^k$  is  $n - \log_2 n$  and the total number of gates is  $O(2^n/n)$ . Again, with a slightly sharper construction and analysis one can obtain the claimed bound.

Despite the fact that almost all functions are exponentially hard for circuits, getting our hands on any hard functions is quite elusive.

The largest classes of circuits where we have very simple explicit functions with strong lower bounds was the class  $AC^0[p]$  for p prime which consists of constant-depth circuits with unbounded fan-in AND gates, OR gates, and mod-p gates where a mod-p gate outputs 0 if the sum of its Boolean inputs is 0 modulo p and outputs 1, otherwise. (Alternatively, in the special case that p is a prime a mod-p gate with inputs  $y_1, \ldots, y_k$  computes the quantity  $(y_1 + \ldots y_k)^{p-1} \mod p$ . This does not nicely extend to cases of mod-m gates.) Razborov and Smolensky used the probabistic polynomials that we described before (and its generalization to other moduli other than 2) to show that any such circuit that computes the mod-q function for  $q \neq p$  prime requires exponential size. Though Shannon's bounds are far from explicit, if we go to a sufficiently high complexity class, we can find functions that require large circuits.

**Proposition 8.3.** There is a function family in EXPEXP (double exponential-time) that requires circuit size  $\Omega(2^n/n)$ .

*Proof.* The idea for an input x of size n is to search through all possible tables of Boolean functions on n bits until we find one that has circuit complexity at least  $S(n) = \varepsilon 2^n/n$ . We search through all possible  $2^{2^n}$  truth tables of such functions starting with the all 0's table in lexicographic order. For each such function we try all circuits of size < S(n) of which there are fewer than  $2^{2^n}$ . For each such circuit, we evaluate the function on all  $2^n$  inputs which takes only  $S(n)2^n$  time. If we find a circuit that is correct on all inputs we move on to the next truth table in order. The value of the function on input x is defined to be the entry for x in the first truth table where all of the circuits fail.

It is known that for every integer  $k \ge 0$ , the complexity class  $\Sigma_2^P = NP^{NP}$  contains functions that require size at least  $\Omega(n^k)$ . Using an exponential translation of this result, we can obtain that the complexity class *EXPEXP* in the above proposition can be reduced to  $E^{\Sigma_2^P}$  where  $E = DIIME(2^{O(n)})$ .

What about *NEXP*, nondeterministic exponential time? It is consistent with our knowledge that every problem in *NEXP* has polynomial-size circuit. (despite the fact that we expect that there are problems in *NP* that don't have polynomial-size circuits - which would be stronger than  $P \neq NP$  but incomparable with ETH and SETH). Ryan Williams proved the following stronger result.

**Theorem 8.4** (Williams). For any class of circuits  $\mathscr{C}$  that is closed under  $\vee$  and  $\wedge$  if  $\mathscr{C}$ -SAT for circuits of size  $n^k$  for all k can be solved deterministically in time  $2^n/n^{10}$  then NEXP does not have polynomial-size circuits in  $\mathscr{C}$ .

Note that this is only a polynomials savings over brute force which would take time  $O(2^n n^k)$  using standard CIRCUIT-SAT algorithms.

**Theorem 8.5** (Murray-Williams). There is a  $\varepsilon > 0$  such that for any class of circuits  $\mathscr{C}$  that is closed under  $\lor$  and  $\land$  if  $\mathscr{C}$ -SAT for circuits of size  $n^k$  for all k can be solved deterministically in time  $2^{n-n^{\varepsilon}}$  then  $NTIME(n^{polylogn})$  does not have polynomial-size circuits in  $\mathscr{C}$ .

**Theorem 8.6** (Murray-Williams). If C-SETH is false then for every k, NP contains functions that require C-circuits of size larger than  $n^k$ .

In the following, we sketch the most basic of the ideas for the arguments which are quite involved. Since the assumptions are algorithmic, we will need to leverage *some* lower bound. That lower bound is the nondeterministic time hierarchy theorem.

**Theorem 8.7** (Nondeterministic Time Hierarchy Theorem). For all T(n) that is the running time of some TM and functions t such that t(n+1) is o(T(n)),  $NTIME(T(n)) \not\subseteq NTIME(t(n))$ .

The proof of this theorem which is covered in CSE 531 is much trickier than the one for the deterministic time hierarchy theorem and it doesn't work for very fast growing functions.

#### **Corollary 8.8.** $NTIME(2^n) \not\subseteq NTIME(o(2^n))$ .

The general idea of the structure of the proof is to show that if we have both

- 1. NEXP has polynomial-size & circuits, and
- 2. There is a deterministic  $\mathscr{C}$ -SAT algorithm with running time  $2^n/n^{10}$  for all polysize  $\mathscr{C}$  circuits

then we can produce an  $NTIME(o(2^n))$  algorithm for any  $L \in NTIME(2^n)$ , which violates the nondeterministic time hierarchy theorem.

To use assumption 1, we apply the following result of Impagliazzo, Kabanets, and Wigderson.

**Lemma 8.9** (Easy Witness Lemma). If NEXP has polynomial-size circuits then for every NEXP verifier V for a language  $L \in NEXP$  (which runs in deterministic time  $2^{n^c}$  and given as input a string x of length n and accepts iff there is a witness string y of length  $2^{n^c}$  such that V(x, y) accepts), then for all large enough  $x \in L$  there is a circuit  $C_{V,x}$  with  $n^c$  inputs bits and size  $n^d$  for some constant d computing a Boolean function such that the truth table T of the function  $C_{V,x}$  is a witness for  $x \in L$ , namely V(x, T) accepts.

This lemma can be specialized to specific circuit classes also. The general idea of its proof is very complicated. The idea is that if it isn't true then you can nondeterministically find truth tables of very hard functions, then one can use this truth table as the basis of a pseudorandom generator (PRG). This PRG can let you derandomize a PCP for NEXP and get that MA (Merlin-Arthur which is like NP but with randomized verifiers) is in NSUBEXP ( $\bigcap_{\varepsilon>0} NTIME(2^{n^{\varepsilon}})$ ). The assumption that NEXP has polysize circuits directly implies that everything in EXP has polysize circuits, which implies that EXP is the same as MA via the Karp-Lipton theorem, which implies that EXP is in NSUBEXP. But the NEXP has polysize circuits means that NSUBEXP only needs circuits of some fixed polynomial size  $n^k$  for some fixed k. That contradicts what we know; in particular, we know that EXP requires circuits of arbitrarily large polynomial size since it contains  $\Sigma_2^p$ . Whew!

The point here is that we can reduce the amount of nondeterminism for  $NTIME(2^n)$  from  $2^{n^c}$  down to  $n^d$ , namely guessing the circuit that must exist. (Note that we only care that it is down to  $o(2^n$  when we started with c = 1.) This circuit gives us a nice efficient way to access any bits of the witness string also. The construction gives a  $\mathscr{C}$ -circuit of this size if NEXP has polynomial-size  $\mathscr{C}$ -circuits.

We haven't used the second of our assumptions yet, namely that there is a  $2^n/n^{10}$  deterministic algorithm for  $\mathscr{C}$ -CIRCUIT-SAT. We need to be able to replace the  $O(2^n)$  time for the verifier *V* for *L* with something shorter. If we don't, we won't save anything. To do this one needs a very special structured PCP for NEXP. The general idea of a PCP is that rather than having a verifier *V* that looks at the whole witness *y*, one encodes *y* in a larger string E(y) so

that one can examine some small randomly chosen portion of E(y) and be able to check that this should be accepted efficiently based on that small portion. For NEXP, the standard PCPs examine only a polynomial size portion of the exponentially long string E(y). These PCPs have the property that if  $x \in L$  then no matter what the portion examined in E(y) will cause the verifier to accept and any  $x \notin L$ , the string E will be rejected with constant probability. In particular, we need to argue that the entire process of taking the random bits r that let one choose which part of E(y) to look at, compute those bits based on the circuit that produces y, and make the decision about whether to reject can be done in some  $n^k$  size  $\mathscr{C}$ -circuit for some k. It happens that such very structured PCPs do exist once we know that we have an  $n^d$  size circuit for the bits of y.

We just need to know whether there is an r that causes us to reject, which is where we use the *C*-CIRCUIT-SAT algorithm. This runs in  $o(2^n)$  deterministic time. Therefore, the whole algorithm, both the guess and verifier run in  $o(2^n)$  time, which contradicts the nondeterministic time hierarchy theorem.

In the application, we even know that, say, either all the r choices are bad (cause the PCP to accept) or at least half of them cause the PCP to reject. In fact, we get the extra promise that if the circuit is satisfiable then at least half the assignments are satisfying.

Therefore we can replace all the  $\mathscr{C}$ -CIRCUIT-SAT problems with their "Gap" versions that have this promise.

Williams gave a clever algorithm using the polynomial method for computing satisfiability of circuits in the class  $ACC^0 \supset \bigcup_m AC^0[m]$  which consists of constant-depth unbounded fanin circuits of AND, OR, and mod-*m* gates for arbitrary many different *m*. (The ACC in  $ACC^0$  stands for Alternating Circuits with Counters.)

**Theorem 8.10.** For  $\varepsilon > 0$ , there is a deterministic algorithm for satsfiability of n variable  $ACC^0$  circuits of size at most  $2^{n^{\varepsilon}}$  in time  $2^{n-n^{\varepsilon}}$ .

**Corollary 8.11** (Williams, Murray-Williams). *NEXP* does not have polynomial-size  $ACC^{0}$ -circuits. In fact,  $NTIME(n^{polylogn})$  does not have such circuits.

# 9 SAT solvers

### **DPLL Algorithm**

This basic algorithm is a simplified version of one by Davis, Logeman, and Loveland (CACM 1962), modifying an approach of Davis and Putnam (CACM 1960) which was quite different. The original goal of both was as a component of a decision procedure for first-order logic. Confusion over the terminology and attribution for the algorithm in the 1990s was settled by agreement to reference all of the authors.

**Algorithm 4** The DPLL algorithm for satisfiability search. This is invoked as DPLL(F,nil). This is a *complete* algorithm in that failure of the search implies that F is unsatisfiable.

1: <b>function</b> DPLL( <i>F</i> , <i>A</i> )				
2:	while F contains a clause x of size 1 do			
3:	$F \leftarrow F_{x \leftarrow 1}; A \leftarrow (A, x)$	Unit propagation		
4:	if F is empty then			
5:	Halt and output satisfying assignment A			
6:	if <i>F</i> contains the empty clause $\perp$ <b>then</b>			
7:	return			
8:	<b>else</b> choose unset literal <i>x</i>	Decision literal		
9:	DPLL( $F_{x \leftarrow 1}$ , (A, x))			
10:	DPLL( $F_{x \leftarrow 0}$ , ( $A, \neg x$ ))			

The DPLL algorithm is closely related to a method for inferring new clauses from existing ones, called *Resolution*. This has precisely one inference rule, the *resolution rule*:

$$\frac{A \lor x; \ B \lor \overline{x}}{A \lor B}$$

This rule is sound since any truth value of x cannot make both of the given clauses true, so one of A or B must be made true. A *Resolution refutation* is a sequence of clauses ending in the empty clause  $\bot$ , each of which is either a given clause or follows from two prior clauses via the resolution rule.

**Proposition 9.1.** A given set F of clauses is unsatisfiable iff there is a Resolution refutation of F.

A Resolution refutation is *tree-like* iff each derived clause is used at most once in any resolution rule.

**Proposition 9.2.** The trace of every DPLL algorithm that fails to find a satisfying assignment for a CNF formula F corresponds to a tree-like refutation of F.

*Proof.* At each of the leaves of the tree of partial assignments A explored, the empty clause  $\perp$  corresponds to an original clause falsified by the corresponding assignment A. The unit propagation nodes each only have one child but we can make each such node a binary node

by adding a leaf node for the alternative polarity of the literal involved. That corresponds to falsifying the original clause that was a unit clause under assignment *A* and therefore that leaf can also be labelled by an original clause that the partial assignment that reaches it falsifies.

We now walk up the tree building the tree-resolution proof. Now consider the last decision literal x at node u above two leaf nodes v where x is set to true and w where x is set to false. Let  $C_v$  and  $C_w$  be the original clauses that are violated at nodes v and w. The partial assignment A that reaches node u together with setting x to true. Then A together with x falsifies  $C_v$  and Atogether with  $\overline{x}$  falsifies  $C_w$ . Since u is not a leaf, A does not falsify either  $C_v$  or  $C_w$ . Therefore  $C_v = D \lor \overline{x}$  and  $C_w = D \lor x$ , for some clauses D and E where A falsifies D and A falsifies E. We can apply the resolution rule to  $C_v$  and  $C_w$  to derive the clause  $C_u = D \lor E$  which we use to label node u. The assignment A falsifies  $C_u$ . Applying this operation inductively up the tree, we can label every node by a clause such that the partial assignment reaching the node falsifies the associated clause and each node is the resolvent of its two child, resolving on the literal being branched on.

At the end we obtain a clause labelling the root of the tree that is falsified by the empty partial assignment. This means that the clause labelling the root must be  $\perp$  and the whole thing is a tree-resolution refutation. Observe that the tree-resolution refutation exactly follows the structure of the DPLL execution.

Note that the above simulation works no matter how the decision literals are chosen. In fact, one can show that for every tree-resolution refutation of F can be pruned and converted into a DPLL execution of the same structure with some choice of decision literals the same size

#### **CDCL SAT solvers**

CDCL stands for Conflict-Directed Clause Learning. These are the most important practical algorithms for SAT solving and formal reasoning. In DPLL, when the search fails because of a conflict in line 7, the recursive calls simply backtrack and the last decision is simply undone. However, while the conflict was found using the last branching decision, it may not depend on any other recent branching decision, so changing those decisions may not impact the conflict. The general idea of conflict-directed clause learning, is to record somewhat more about the decisions and unit propagations make during the proof search, using a data structure called a *conflict graph* and replace line 7 of DPLL with a *conflict analysis* step which adds a new *learned clause* to *F*, which summarizes the reason for the conflict and can be used to simplify future searches:

The algorithm maintains the partial assignment *A*, which is called the *trail*. The *conflict graph* is a directed graph with one vertex for each literal in the trail *A*. Decision literals are source nodes. For every literal *x* in *A* assigned through propagation of a unit clause that was originally a clause *C* of *F*, all of the other literals *z* in *C* must have previously appeared as  $\neg z$  in *A*; in the conflict graph we put an edge from each of these  $\neg z$  literals to *x*. It is possible that this unit propagation produces the empty clause  $\bot$  rather than a literal, if a decision is made that causes an immediate contradiction.
**Definition 9.3.** Given a conflict graph G = (V, E), a *source-sink cut* in G is a set of vertices  $U \subset V$  such that

- *U* contains all sources (decision literals) in *G*,
- *U* does not contain the sink node labeled  $\perp$ .
- *U* there are no edges from V U to *U* in *G*.

Given such a cut U, let  $E_U$  be the set of edges that lead from U to V - U. Observe that  $E_U$  is a set of edges whose removal eliminates all paths from source nodes to  $\bot$ . Given such a cut U, we define *conflict clause*  $C_U$  to be the clause whose literals are negations of literals at tails of edges in  $E_U$ .

CDCL solvers choose one of these clauses that is guaranteed to cause immediate unit propagation at a level higher than the current level. Such a clause is called an *asserting*; the level where this unit propagation takes place is call the *assertion level* of the clause. Note that the clause consisting of the negations of all the decision literals will be asserting at a level one above the current level. If we add that clause, this will be the equivalent to DPLL since unit propagation will switch to the other assignment of the last decision literal.

Algorithm 5 Simplified CDCL ideas for satisfiability search.	
1:	function SIMPLE-CDCL(F)
2:	Set <i>A</i> to nil and conflict graph to empty.
3:	Set decision level to 0.
4:	while true do
5:	while <i>F</i> contains a clause consistent with <i>A</i> with only one literal <i>x</i> not set by <i>A</i> <b>do</b>
6:	$A \leftarrow (A, x)$
7:	Propagate( $x,F$ ) $\triangleright$ Unit propagation
8:	Add $x$ and edges to conflict graph
9:	if F has no active clauses then Halt and output satisfying assignment A
10:	if conflict graph has $\perp$ then
11:	if decision level=0 then Halt and output "unsatisfiable".
12:	Use conflict graph to find asserting conflict clause $C_U$ > Analyze Conflict
13:	Add $C_U$ to $F$
14:	Set decision level to assertion level of $C_U$ , pruning A and the conflict graph.
15:	else
16:	Choose unset literal $x$ according to decision heuristic $\triangleright$ Decision literal
17:	Increment decision level.
18:	$A \leftarrow (A, x).$
19:	Propagate $(x,F)$ .
20:	Add source $x$ to conflict graph tagged with decision level.

The asserting learned clause is typically found by using the 1UIP (1st Unique Implication Point) cut given by a single node (the first starting from  $\perp$ ) that separates  $\perp$  from the decision

literal at the last level. Example of a conflict graph and asserting learned clause ( $\overline{w} \lor \overline{v} \lor \overline{a}$ ) given by the 1UIP is the following:



**Proposition 9.4.** Learned clauses are derivable using a number of steps of the resolution rule at most the size of the last decision level.

*Proof.* We show the idea using the example. The conflict graph implies that clauses  $\overline{b} \vee \overline{c}$ ,  $\overline{y} \vee \overline{a} \vee b$ ,  $\overline{w} \vee c$  must be part of the formula. Resolving the 1st with the 2nd gives  $\overline{y} \vee \overline{a} \vee \overline{c}$  and resolving this with the 3rd gives the desired learned clause. The general case follows similarly working backwards from  $\bot$ .

There are a few optimizations that are critical for the good performance of CDCL. One is that the algorithm only maintains the trail rather than the residual (simplified) formula at each step. All that is required is that the algorithm be able to tell when a clause in the residual formula has at most one unset literal outside of *A*. Learned clauses can also get quite long, even if the original formula was in 3-CNF. To do this the algorithm maintains the names of just two *watched literals* for each original and learned clause that has not been satisfied by *A*, as well as a list of which active clauses have these literals. Every time that *A* sets a literal, each the watched literal pair for each in the list is updated. (If one of these is falsified by the newly assigned literal, then another literal in the clause is chosen to be watched if possible.) This has the advantage of being cache-efficient.

Another optimization is that the algorithm can periodically choose to restart from the root, but keeping around learned clauses so that the algorithm doesn't get stuck at large decision levels; a simple example might be if a clause of size 1 is learned which enforces the assignment to one of the variables, but it can also be useful if there simply have been a large number of decisions.

Good CDCL algorithms also use information about the conflict graph and learned clauses to help with the decision heuristic for new literals is based on which literals have shown up in recent learned clauses, using a tunable parameter. A popular one is called VSIDS, which uses multiplicative weight updates for variables. Depending on the application, decisions about which polarity to use for each literal may be to always try false first, or to use the same sign as was last used, or to randomize the choice. Also, it turns out that some frontier clauses include more literals than necessary because the negations of some of those literals are implied by the negations of other sets of literals in the clause. Simplifying such clauses is call *clause minimization*.

Finally, the number of learned clauses can grow very quickly, which would overwhelm the storage. To avoid this, CDCL algorithms usually have a cache pruning phase that periodically removes learned clauses that have not been used recently. (Often this is in the form of a marking algorithm that periodically halves the number of learned clauses, removing old learned clauses that have not been used since the last cache pruning.)

Alg	orithm 6 CDCL for satisfiability search.
1:	function CDCL(F)
2:	Choose two watched literals for each clause of F, if possible.
3:	Set <i>A</i> to nil and conflict graph to empty.
4:	Set decision level to 0.
5:	while true do
6:	while F contains a clause with only 1 watched literal x do
7:	$A \leftarrow (A, x)$
8:	Propagate( $x,F$ ) $\triangleright$ Unit propagation
9:	Add $x$ and edges to conflict graph
10:	if F has no active clauses then Halt and output satisfying assignment A
11:	if conflict graph has <i>bot</i> then
12:	<b>if</b> decision level=0 <b>then</b> Halt and output "unsatisfiable".
13:	Use conflict graph to find asserting conflict clause $C_U$ > Analyze Conflict
14:	Minimize $C_U$
15:	Add $C_U$ to F and add two watched literals for $C_U$
16:	Update decision heuristic
17:	if restart chosen then
18:	Set A to nil and conflict graph to empty.
19:	Set decision level to 0.
20:	else if clause pruning chosen then
21:	Remove oldest half of learned clauses unused since last pruning.
22:	else
23:	Set decision level to assertion level of $C_U$ , pruning A and the conflict graph.
24:	Update watched literals.
25:	else
26:	Choose unset literal $x$ according to decision heuristic $\triangleright$ Decision literal
27:	Increment decision level.
28:	$A \leftarrow (A, x).$
29:	Propagate(x,F).
30:	Add source $x$ to conflict graph tagged with decision level.

**Theorem 9.5.** The trace of every CDCL SAT solver run on an unsatisfiable CNF formula F yields a Resolution refutation of F of at most the same size.

# **10** Resolution Proofs

**Definition 10.1.** For a CNF formula *F*, we write Res(F) for the minimum number of clauses in any Resolution refutation of *F*. If *F* is satisfiable then  $\text{Res}(F) = \infty$ .

**Definition 10.2.** For a CNF formula *F* write  $w(F) = \max_{C \in F} |C|$  and for a resolution proof *P*, define the *width* of *P*,  $w(P) = \max_{C \in P} |C|$ .

For a CNF formula *F* define width(*F*) to be the minimum width w(*P*) over all resolution refutations *P* of *F* (and  $\infty$  if no such refutation exists).

- **Proposition 10.3.** (a) Let P be a resolution derivation of a clause C from CNF formula F. For any restriction  $\rho$  on the variables of F,  $P|_{\rho}$  is a resolution derivation of  $C|_{\rho}$  from  $F|_{\rho}$ .
  - (b) For literal z, if width( $F_{z\leftarrow 1}$ )  $\leq w$  then either width(F)  $\leq w$  or there is a resolution derivation of  $\neg z$  from F of width at most w + 1.

*Proof.* Part (a) is immediate. For part (b), let P' be a refutation of  $F_{z \leftarrow 1}$  of width at most w. For the new derivation, replace each input clause of  $F_{z \leftarrow 1}$  by the corresponding clause of F and retain the same sequence of resolution steps (which is possible since none of the resolution steps involve the literal z) to yield a proof P. The replacement of input clauses of  $F_{z \leftarrow 1}$  by those of F may add  $\neg z$  to some input clauses. It is immediate, inductively, that every clause of proof P' either stays the same or has  $\neg z$  added to it in P. If the output clause is still the empty clause  $\bot$ , then all the clauses of  $F_{z \leftarrow 1}$  leading to the output clause of P' were in the original formula F, so P' is the required refutation. Otherwise, the clause width of P is at most w + 1 and the output clause is  $\neg z$ .

**Theorem 10.4.** Let *F* be a CNF formula in *n* variables. If  $\text{Res}(F) \leq S$  then width(*F*)  $\leq \max(2\sqrt{2n \ln S}, \sqrt{2n \ln S} + w(F))$ .

*Proof.* Set  $W = \lceil \sqrt{2n \ln S} \rceil$ . We say that a clause *C* is *wide* iff  $w(C) \ge W$ . We prove the following claim by induction on *n* and *k*:

CLAIM: If  $(1 - W/2n)^k S < 1$  then any CNF formula *F* in *n* variables having a resolution refutation with  $\leq S$  wide clauses has width(*F*)  $\leq \max(W, w(F)) + k$ .

Before proving the claim, we observe that the case k = W is sufficient to prove the statement, since  $(1 - W/2n)^W < e^{-W^2/2n} \le 1/S$  by the choice of *W*.

The case k = 0 is trivial, since a refutation with no wide clauses has width at most *W*.

For the general case, let *P* be a resolution refutation of *F* with *n* variable and  $\leq S$  clauses of width  $\geq W$  and suppose that  $(1 - W/2n)^k S \leq 1$ . Choose the literal *z* appearing in the most wide clauses of *P*. Since there are  $\leq 2n$  possible literals and  $\geq W$  distinct literals per wide clause, *z* appears in  $\geq WS/2n$  wide clauses.

Consider the restrictions  $z \leftarrow 1$  and  $z \leftarrow 0$ : Then  $P_{z \leftarrow 1}$  is a resolution refutation of  $F_{z \leftarrow 1}$  and every clause of *P* containing *z* is satisfied and hence removed (and others are only shortened),

so  $P_{z \leftarrow 1}$  has  $S' \leq S - WS/2n = (1 - W/2n) S$  wide clauses. Therefore  $(1 - W/2n)^{k-1} S' \leq (1 - W/2n)^k S \leq 1$ . It follows by our inductive hypothesis with k' = k - 1, that

width
$$(F_{z\leftarrow 1}) \le \max(W, w(F_{z\leftarrow 1})) + k - 1 \le \max(W, w(F)) + k - 1.$$

By Proposition 10.3(b), either width(F)  $\leq \max(W, w(F)) + k$ , and we are done, or there is a derivation P' of  $\neg z$  from F of width  $\leq \max(W, w(F)) + k$ .

Now,  $P_{z \leftarrow 0}$  is a refutation of  $F_{z \leftarrow 0}$ . By the inductive hypothesis applied to  $F_{z \leftarrow 0}$ , which has n' = n - 1 variables, there is a refutation P'' of  $F_{z \leftarrow 0}$  of width at most  $\max(W, w(F)) + k$ . We next resolve  $\neg z$  with clauses of F containing z to produce every clause of  $F_{z \leftarrow 0}$  used in P''. This part requires width at most w(F).

Putting the parts together we obtain a single refutation of *F* of width at most max(W, w(F)) + k as required for the induction step.

**Corollary 10.5.** Let F be a CNF formula in n variables. If there is a resolution refutation of F or size at most S then there is an algorithm running in time  $n^{O(\sqrt{n\log S} + w(F))}$  that will find a resolution refutation of F.

*Proof.* Apply a width-increasing search on proofs up to the width bound  $W^*$  given in Theorem 10.4. That is, it applies all possible resolution inferences that yield clauses of width w beginning with w = w(F) and increasing until a refutation is found. The number of distinct clauses up to this width bound  $W^*$  is less than  $\sum_{w=0}^{W^*} \binom{2n}{w}$  and the running time is polynomial in this number of potential clauses. Plugging in the different values of  $W^*$  and using standard binomial bounds for the two cases yields the claimed running times.

We restate Theorem 10.4 in the following convenient form:

**Theorem 10.6.** For any CNF formula F in n variables,  $\operatorname{Res}(F) > e^{\min((\operatorname{width}(F) - w(F))^2/2n, \operatorname{width}(F)^2/8n)}$ 

This implies that sufficiently strong resolution width lower bounds suffice for proving resolution size lower bounds. It is known that the width-size relationship in Theorems 10.4 and 10.6 cannot be improved beyond a logarithmic factor in width or a polynomial factor in size.

#### Boundary expansion and resolution clause width

**Definition 10.7.** For a bipartite graph G = (L, R, E) and a set  $S \subseteq L$ , the *boundary* of *S*, denoted  $\partial S$ , is the set of all  $v \in R$  that have exactly one neighbor  $u \in S$ .

Graph G = (L, R, E) is an (r, c)-boundary expander iff for every  $S \subset L$  with  $|S| \leq r$ , the boundary  $\partial S$  satisfies  $|\partial S| \geq c|S|$ .

**Definition 10.8.** Any CNF formula *F* corresponds to a bipartite graph  $G_F = (L, R, E)$  where *L* is the set of clauses of *F*, *R* is the set of variables of *F*, and  $(C, x) \in E$  iff variable *x* appears in clause *C*.

Given a set of clauses *S*, the boundary of *S*,  $\partial S$ , in  $G_F$  is a set of variables, but since each occurs with a unique sign in the clauses of *S*, we can also interpret  $\partial S$  as a set of literals when it is convenient.

**Lemma 10.9.** If *F* is a CNF formula,  $C^*$  is a clause and there is a resolution derivation of  $C^*$  from *F* for which *S* is the set of clauses of *F* that have a path to  $C^*$ , or then  $|C^*| \ge |\partial S|$ .

*Proof.* Observe that the literals in the boundary variables of *S* pass through to  $C^*$  without cancellation.

Boundary expansion plays a role in a wide variety of lower bound arguments in proof complexity. In particular, it suffices to prove width lower bounds for resolution proofs and hence lower bounds on resolution proof size using Theorem 10.6.

**Theorem 10.10.** Any CNF formula F for which  $G_F$  is an (r, c)-boundary expander requires resolution refutation width > cr/2.

*Proof.* Without loss of generality we may assume that c > 0. Define the *complexity* of a clause *C* in the refutation of *F* to be the size of the subset of clauses *S* of *F* that have a path to *C* in the proof. In particular, since  $G_F$  is an (r, c)-boundary expander, any set *S* of clauses of size at most r has  $|\partial S| > c|S| > 0$  by Lemma 10.9 and hence  $C \neq \bot$ . Therefore, the empty clause at the root of the proof must have complexity > r. The input clauses have complexity 1. By the soundness of the resolution rule, complexity is sub-additive; that is, if *C* is derived from *A* and *B*, then the complexity of *C* is at most the sum of the complexities of *A* and *B*.

We therefore follow the proof back from the root, always taking the branch of larger complexity, which will be at least 1/2 of the previous complexity by sub-additivity. Eventually this must pass through a clause  $C^*$  with paths from a minimal subset S of clauses of F with  $r/2 < |S| \le r$  that yields  $C^*$ . Since  $G_F$  is an (r, c)-bipartite expander,  $|C^*| \ge |\partial S| \ge c|S| > cr/2$ as required.

## ETH holds for Resolution proofs

Let  $\mathscr{F}_n^{k,m}$  be a distribution of random *k*-CNF formulas with *m* clauses chosen uniformly randomly and independently from the set of  $2^k \binom{n}{k}$  possible clauses on *k* distinct variables.

**Lemma 10.11.** For  $m \ge 2^k \ln 2 \cdot n$ , random k-CNF formulas are unsatisfiable with probability 1 - o(1).

*Proof.* There are  $2^n$  possible assignments. For each such assignment, each clause is independently set true with probability  $1-2^{-k}$  so the probability that the assignment satisfies the entire formula is then  $(1-2^{-k})^m \le (1-2^{-k})^{2^k \ln 2 \cdot n}$  Since  $(1-2^{-k}) < e^{-2^{-k}}$  this is  $o(e^{-\ln 2 \cdot n})$  and hence  $o(2^{-n})$ . Therefore the expected number of assignments that satisfy the formula is o(1) which upper bounds the probability that the formula is satisfiable.

**Lemma 10.12.** Let  $\Delta > 0$  and  $m = \Delta n$ . For some 1 > c, c' > 0, with probability 1 - o(n) a random 3-CNF formula with m = cn is an (r, c)-boundary expander for r = c'n with probability 1 - o(1).

*Proof.* Fix a set of clauses  $\mathscr{C}$  of size s. We want to argue that  $|\partial \mathscr{C}| \ge c'n$  for some c'. Any variable appearing in  $\mathscr{C}$  that is not in boundary must appear at least 2 times among the 3s literals in  $\mathscr{C}$ , so it suffices to show that every subset  $\mathscr{C}$  of size  $s \le r = c'n$  contains more than q = (3 + c)s/2 distinct literals for some constant c > 0.

For a single  $C \in \mathscr{C}$ , Let  $p = \binom{q}{3} / \binom{n}{3} \le (q/n)^3$  be the probability that all variables of *C* are some fixed subset *Q* of size *q* and there are  $\binom{n}{q}$  such subsets. Now

$$Pr[|vars(\mathscr{C}| \le q] \le {\binom{n}{q}} p^{s}$$

$$\le {\left(\frac{ne}{q}\right)^{q}} {\left(\frac{q}{n}\right)^{3s}}$$

$$= e^{q} \cdot {\left(\frac{q}{n}\right)^{3s-q}}$$

$$= e^{(3+c)s/2} \cdot {\left(\frac{(3+c)s/2}{n}\right)^{(3-c)s/2}}$$

$$= a^{s} \cdot {\left(\frac{s}{n}\right)^{(3-c)s/2}}$$

for some constant *a* depending on *c*. Therefore, the probability that  $G_F$  is not an (r, c)-boundary expander is at most

$$\sum_{s=1}^{r} {\Delta n \choose s} a^{s} \cdot \left(\frac{s}{n}\right)^{(3-c)s/2} \le \sum_{s=1}^{r} \left(\frac{a \cdot e \cdot \Delta n}{s}\right)^{s} \cdot \left(\frac{s}{n}\right)^{(3-c)s/2}$$
$$= \sum_{s=1}^{r} \left[a \cdot e \cdot \Delta \cdot \left(\frac{s}{n}\right)^{(1-c)/2}\right]^{s}$$
$$= \sum_{s=1}^{r} \left[b \cdot \Delta \cdot \left(\frac{s}{n}\right)^{(1-c)/2}\right]^{s}$$

for  $b = a \cdot e$  which depends only on *c*.

To bound this quantity we split the sum into two cases depending on whether *s* is small or large.

We can choose constant c' > 0, depending on  $\Delta$  and c so that the term in the sum for  $s \le r = c'n$  is at most  $2^{-s}$ . We use this for any threshold t = t(n) that grows with n to bound the sum of all of the terms for s > t(n) by  $2^{-t(n)}$ , which goes to 0 with n. For definiteness we just choose  $t(n) = \log n$ .

On the other hand, there are only  $\log n$  terms for  $s \le t(n)$  and each is  $O((\frac{\log n}{n})^{(1-c)/2})$  since  $s \ge 1$ , so that sum also is o(1) in n.

**Theorem 10.13.** There is a constant  $\delta > 0$  such that random 3-CNF formulas F with O(n) clauses with probability 1 - o(1), require  $\text{Res}(F) \ge 2^{\delta n}$ .

*Proof.* By Lemma 10.12, there are constants c, c' > 0 such that with probability 1 - o(1),  $G_F$  is an (r, c)=boundary expander for r = c'n and hence, by Theorem 10.10, width(F) > cr/2 =

c'cn/2. Plugging this width lower bound into Theorem 10.6 yields the claimed bound with  $\delta = (c'c)^2/32$ . □

# 11 Communication Complexity and Lifting

**Definition 11.1.** A (*deterministic*) decision tree T with inputs in  $\{0, 1\}^n$  is a rooted binary tree with each internal node labeled by a variable  $x_i$  for  $i \in [n]$ , with the two out-edges labelled 0 and 1 indicating the value of  $x_i$ . Each root-leaf path in T defines a partial assignment that is the concatenation of the assignments on all edges in the path. The output of the decision tree on a truth assignment x is the label of the leaf reached by the unique root-leaf path whose associated partial assignment is consistent with x.

For a function f defined on  $\{0,1\}^n$ , we write the decision tree complexity of f,  $C^{dt}(f)$  to be the minimum height of any decision tree computing f and size<sup>dt</sup>(f) to be the minimum number of leaves in any such decision tree,

**Definition 11.2** (False Clause Search). Given an unsatisfiable CNF formula *F* in *n* variables with clauses  $C_1, \ldots, C_m$ , we define the search problem  $Search_F$  which takes as input  $x \in \{0, 1\}^n$ , which must falsify *F*, and outputs any index  $i \in [m]$  such that *x* falsifies clause  $C_i$ .

 $Search_F$  is an example of a relation R. In general we write R(x) for the set of legal outputs of R on input x. For any relation R, write  $C^{dt}(R)$  and  $size^{dt}(R)$ , for the minimum  $C^{dt}(f)$  and  $size^{dt}(f)$  for any function f that is consistent with R.

**Proposition 11.3.** DPLL run on an unsatisfiable input F is a decision tree for the search problem  $Search_F$ . In particular size<sup>dt</sup>(Search<sub>F</sub>) is the minimum number of nodes in any DPLL run on input F (equivalently the minimum size of any tree-resolution refutation of F) and  $C^{dt}(Search_F)$  is the minimum height of the DPLL search tree on input F.

## (Two-Party) Communication Complexity

Here were have two *players* or *parties*, usually designated as Alice and Bob who cooperate in order to compute based on shared inputs. Alice receives an input  $x \in X$  and Bob receives  $y \in Y$ .

A (deterministic) 2-party communication protocol on  $X \times Y$  is a rooted binary tree, with each internal node v labelled either by a function  $\alpha_v : X \to \{0, 1\}$ , or a function  $\beta_v : Y \to \{0, 1\}$ . The two out-edges of v are labelled 0 and 1 respectively. The root-leaf paths in a protocol followed on input x and y is given by following the out-edges given by  $\alpha_v(x)$  (Alice speaks) or  $\beta_v(y)$ (Bob speaks) depending on which kind of function labels v. The output of the protocol on input  $(x, y) \in X \times Y$  is the label of the leaf.

For a function f defined on  $X \times Y$ , we write the deterministic communication complexity of f,  $C^{Cc}(f)$  to be the minimum height of any 2-party communication protocol computing f and size<sup>*cc*</sup>(f) to be the minimum number of nodes in any deterministic 2-party protocol computing f. We extend these definitions to relations defined on  $X \times Y$  also.

Examples: Consider  $X = Y = \{0, 1\}^n$ . Then for any f on  $X \times Y$  has  $C^{cc}(f) \le n+1$  since Alice can simply send her entire input to Bob after which he computes the answer.  $Parity_n(x, y)$  that gives the total parity check for the string xy, has  $C^{cc}(Parity_n) = 2$ .  $Maj_n(x, y)$  which has value 1 if there are at least as many 1's as 0's in xy has  $C^{cc}(Maj_n) = O(\log n)$ .

What about  $EQ_n(x, y)$  which is 1 if x and y are equal and 0 otherwise?

In general given a function f defined on  $X \times Y$ , we define the *communication matrix of* f,  $M_f$  as the  $|X| \times |Y|$  matrix whose (x, y) entry is f(x, y).

In particular  $M_{EQ_n}$  is the  $2^n \times 2^n$  identity matrix.

**Definition 11.4.** A (*combinatorial*) *rectangle* in  $X \times Y$  is a set of the form  $A \times B$  for  $A \subseteq X$  and  $B \subseteq Y$ . A rectangle *R* is 1-rectangle of *f* iff *f* evaluates to 1 on every element of *R*, and a 0-rectangle of *f* iff *f* is always 0 on *R*. *R* is *monochromatic* iff *f* is constant on *R*.

**Lemma 11.5.** The set of inputs in  $X \times Y$  that reach each node v of a communication protocol is a combinatorial rectangle.

*Proof.* We show this by induction starting at the root *r* which is rectangle  $R_r = X \times Y$ . Suppose inductively that  $R_u = A_u \times B_u$  is the rectangle of inputs reaching node *v* in the protocol tree and let the child following outedge 0 be  $v_0$  and the one following out-edge 1 be  $v_1$ .

Case Alice: If v is labelled by some  $\alpha_v : X \to \{0, 1\}$ , then the set of nodes reaching  $v_0$  is precisely  $A_{v0} \times B_{v0}$  where  $A_{v0} = A_v \cap \alpha_v^{-1}(0)$  and  $B_{v0} = B_v$  and reaching  $v_1$  is  $A_{v1} \times B_{v1}$  where  $A_{v1} = A_v \cap \alpha_v^{-1}(1)$  and  $B_{v1} = B_v$ .

Case Bob: If *v* is labelled by some  $\beta_v : X \to \{0, 1\}$ , then the set of nodes reaching  $v_0$  is precisely  $A_{v0} \times B_{v0}$  where  $A_{v0} = A_v$  and  $B_{v0} = B_v \cap \beta_v^{-1}(0)$  and reaching  $v_1$  is  $A_{v1} \times B_{v1}$  where  $A_{v1} = A_v$  and  $B_{v1} = B_v \cap \beta_v^{-1}(1)$ .

**Corollary 11.6.** Let f be defined on  $X \times Y$ . Then  $X \times Y$  can be partitioned into at most  $2^{C^{cc}(f)}$  monochromatic rectangles of f, corresponding to the leaves of a protocol with this complexity.

**Corollary 11.7.**  $C^{cc}(EQ_n) = n + 1$ 

*Proof.*  $M_{EQ_n}$  is a  $2^n \times 2^n$  diagonal matrix and no two entries on the diagonal can be in the same rectangle. This gives  $2^n$  1-rectangles, plus there is at least one 0-rectangle giving more than  $2^n$  such rectangles. Taking logarithms and rounding up we obtain the lower bound; the upper bound is true for all *n*-bit Boolean functions.

Other important problems of interest in communication complexity include

- Disj<sub>n</sub>(x, y) = V<sup>n</sup><sub>i=1</sub> x<sub>i</sub> ∧ y<sub>i</sub>, called the disjointness function. The terminology comes from viewing x and y as characteristic functions of two subsets S<sub>x</sub>, T<sub>y</sub> ⊆ [n], since Disj<sub>n</sub>(x, y) = 0 iff the sets S<sub>x</sub> and S<sub>y</sub> are disjoint.
- $IP_n(x, y) = \sum_{i=1}^n x_i y_i \mod 2$ , the inner product function mod 2.
- $Index_m : [m] \times \{0,1\}^m$  where  $Index_m(x, y) = y_x$ .

**Proposition 11.8.**  $C^{cc}(Disj_n) = C^{cc}(IP_n) = n + 1$  and  $C^{cc}(Index_m) = m$ .

#### **Karchmer-Wigderson Games**

We now connect communication complexity of search problems to the complexity of Boolean circuits over the De Morgan basis of binary  $\land$ ,  $\lor$  and  $\neg$  gates. By De Morgan's law we can push all of the negations in a circuit or formula to the leaves so that we can assume that the inputs for a circuit are literals and all interior gates are binary  $\land$  or  $\lor$ . We write C(f) for the minimum circuit size for Boolean function f in that basis, D(f) for the minimum circuit depth for f and L(f) for the minimum number of leaves in any Boolean formula for f.

We also will consider *monotone* circuits which do not use any negated literals. Monotone circuits can only compute monotone Boolean functions, which are functions for which flipping any input bit from 0 to 1 cannot decrease the function value. For monotone f, we write  $C^m(f)$ ,  $D^m(f)$ , and  $L^m(f)$  for the monotone circuit complexity, depth complexity, and formula size of f.

**Definition 11.9.** Given a Boolean function  $f : \{0,1\}^n \to \{0,1\}$  we define the *Karchmer-Wigderson relation*  $KW_f : X \times Y$  to [n] where  $X = f^{-1}(1)$  and  $Y = f^{-1}(0)$  be the relation

$$KW_f(x, y) = \{i \mid x_i \neq y_i\}.$$

If *f* is monotone then we know that for any input *x*, *y* for which f(x) = 1 and f(y) = 0, there must be some bit  $x_i = 1$  and  $y_i = 0$ . We can therefore define the *monotone Karchmer-Wigderson* relation  $mKW_f : X \times Y$  to [n] where

$$mKW_f(x, y) = \{i \mid x_i > y_i\}.$$

**Theorem 11.10.** For every Boolean function f,  $C^{cc}(KW_f) = D(f)$  and  $size^{cc}(KW_f) = L(f)$ . For every monotone Boolean function f,  $C^{cc}(mKW_f) = D^m(f)$  and  $size^{cc}(f) = L^m(f)$ .

*Proof.* Without loss of generality the optimal circuit depth is given by a Boolean formula (a tree), since we can simply replicate gates. There are two directions here:

Given a Boolean formula *T* for *f*, the protocol tree for  $KW_f$  will have exactly the same structure as *T*. For each gate of the circuit *v*, we write  $f_v$  for the Boolean function computed at the node.

We maintain the invariant that the rectangle of inputs  $R_u$  that reaches node u is contained in  $f_u^{-1}(1) \times f_u^{=1}(0)$ . To do this, every  $\vee$  gate becomes a node for Alice and every  $\wedge$  gate becomes a node for Bob. If gate u is an  $\vee$  gate, where  $f_u = f_v \vee f_w$ , since any string y reaching this node has  $f_u(y) = 0$ , we know that  $f_v(y) = f(w) = 0$ . On the other hand any string x reaching it has  $f_u(x) = 1$  so at least one of  $f_v(x) = 1$  or  $f_w(x) = 1$ . We define the function  $\alpha_u(x)$  is 0 if  $f_v(x) - 1$  and 1 otherwise.

The situation if the gate if  $\wedge$  is dual: Alice's input *x* evaluates to 1 on both children, while Bob has to indicate one of *v* or *w* such that  $f_u(y) = 0$ .

Applying this at a leaf we reach a literal or its negation. We label the leaf by the index *i* of that literal, which must be correct for  $KW_f$  since *x* and *y* evaluate differently on that literal. If the circuit is monotone we must have  $x_i = 1$  and  $y_i = 0$  as required for  $mKW_f$ .

Hence the complexities of the Karchmer-Wigderson relations are at most that of the corresponding depth and size measures.

We now do the reverse simulation to show the other direction. We convert the protocol trees for  $KW_f$  or  $mKW_f$  to Boolean formulas with exactly the same structure as the protocol trees, replacing every node where Alice speaks by  $\lor$  and every node where Bob speaks by  $\land$ . We work bottom-up arguing that we can maintain the property that the function  $f_u$  satisfies  $R_u \subseteq f_u^{-1}(1) \times f_u^{-1}(0)$  where  $R_u$  is the rectangle of inputs reaching node u.

To get started we need to specify the leaf labels. At the leaves of the protocol for  $KW_f$  with output *i*, every input (x, y) in the rectangle  $R_\ell$  associated with that leaf  $\ell$  must have  $x_i$  and  $y_i$  must have opposite values. The values of both must be unique since this is a rectangle. If  $x_i = 1$  and  $y_i = 0$  in the rectangle then the circuit has the positive literal *i* at  $\ell$  and if  $x_i = 0$  and  $y_i = 1$  then we have the negation of literal *i* at  $\ell$ . This ensures that the property holds for  $f_\ell$ .

Suppose that we have the property for children v and w of node u at which Alice speaks; then  $f_u = f_v \lor f_w$  by construction. Suppose that  $R_u = A_u \times B_u$ . Then  $R_v = A_v \times B_u$  and  $R_w = A_w \times B_u$ with  $A_u = A_v \cup A_w$  where  $f_v(A_v) = 1$  and  $f_w(A_w) = 1$  by the inductive property so therefore  $f_u(A_u) = 1$ . Since both  $f_v(B_u) = 0$  and  $f_w(B_u) = 0$ , we have  $f_u(B_u) = 0$ . The same applies dually to the nodes where Bob speaks. The fact that this correctly computes f follows from the inductive hypothesis applied to the root.

## **12** Deterministic Lifting

**Definition 12.1.** Given a Boolean function f or search problem (relation) R defined on  $\{0, 1\}^n$ , and a Boolean function  $g : X \times Y \to \{0, 1\}$  (called the *gadget* function). We define the composition  $f \circ g^n$  or  $R \circ g^n$  on the set  $X^n \times Y^n$  by  $f \circ g^n(x, y) = f(z_1, ..., z_n)$  where  $z_i = g(x_i, y_i)$  for  $(x_i, y_i) \in X \times Y$ .

Note that in the composed function  $f \circ g^n$ , Alice receives all of  $x_1, \ldots, x_n$  and Bob receives all of  $y_1, \ldots, y_n$ .

**Proposition 12.2.** *Let*  $f : \{0, 1\}^n \to \{0, 1\}$ *. Then* 

$$C^{cc}(f \circ g^n) \leq C^{dt}(f) \cdot C^{cc}(g).$$

More generally if R is a search problem defined on  $\{0, 1\}$  then

$$C^{cc}(R \circ g^n) \leq C^{dt}(R) \cdot C^{cc}(g).$$

*Proof.* The communication protocols are simple: The two players simulate the best decision tree protocol for f (or R) on input  $z_1, \ldots, z_n$ ; whenever that protocol queries  $z_i$ , the players compute it using the best communication protocol for g using  $C^{cc}(f)$  bits of communication.

**Key lifting question:** Find broad circumstances in which the above algorithm is (close to) optimal; that is, when  $C^{cc}(f \circ g^n)$  is  $\Omega(C^{dt}(f))$  or  $\Omega(C^{dt}(f) C^{cc}(g))$ .

There are simple examples of f and g where the protocol of Proposition 12.2 is very far from optimal: Suppose that both functions are parity functions; that is,  $f = \bigoplus_n$  and  $g(x, y) = Parity_m(x, y)$  on  $\{0, 1\}^m \times \{0, 1\}^m$ . In that case,  $C^{dt}(f) = n$  and  $C^{cc}(g) = 2$  so the upper bound from Proposition 12.2 would be 2n. However, the function  $f \circ g^n$  is simply  $Parity_{nm}$  so  $C^{cc}(f \circ g^n) = 2$ .

Our focus will be on general theorems showing that for some function g, Proposition 12.2 holds for *all* functions f and *all* relations R. We clearly cannot do this if g is  $Parity_m$ .

The first such general lifting theorem was proved by Raz and McKenzie was proven in order to derive lower bounds on the depth complexity of monotone circuits by showing lower bounds on monotone Karchmer-Wigderson game mKW of the composed function  $f \circ g^n$ . It therefore was important that the gadget g be monotone.

**Theorem 12.3** (Raz-McKenzie 1999). For every search problem R defined on  $\{0, 1\}^n$  and m that is a sufficiently large polynomial in n we have

$$C^{cc}(R \circ Index_m^n) = \Omega(C^{dt}(R) \log m).$$

Recall that  $Index_m : [m] \times \{0,1\}^m$  is given by  $g(x,y) = y_x$  so it is a monotone function and hence yields a monotone function for every monotone function f. It has  $C^{cc}(Index_m) = \log m + 1$  so it shows that Proposition 12.2 is asymptotically tight for  $Index_m$ .

Before proving this theorem we discuss one of its applications.

Raz and McKenzie used Theorem 12.3 together with an explicit sequence  $F_n$  of unsatisfiable 4-CNF formulas (based on pebbling pyramid graphs) that was known to require large depth Resolution refutations and defined a monotone Boolean function based on  $f = F_n \circ Index_m^n$ such that the relation  $mKW_f$  is precisely  $Search_{F_n} \circ Index_m^n$  and used the above theorem to derive new depth lower bounds on monotone circuits.

Given an unsatisfiable k-CNF formula  $F_n$  with n variables, for the 2-party communication problem  $Search_{F_n} \circ Index_m^n$ , Alice gets  $x_1, \ldots, x_n \in [m]^n$ , Bob gets  $y_1, \ldots, y_n \in \{0, 1\}^n$  and the goal is to output the index i of some clause of  $F_n$  that evaluates to false on the input  $Index_m(x_1, y_1), \ldots, Index_m(x_n, y_n) = (y_{1x_1}, \ldots, y_{nx_n})$ . In other words, this just a protocol for  $Search_{F_n}$  with each of whose input bits being determined by the vector of pointers  $x_1, \ldots, x_n$ .

A general form of the Raz-McKenzie construction of a monotone Boolean function was given by Göös and Pitassi as follows:

**Lemma 12.4** (Göös-Pitassi 2012). Let  $F_n$  be an unsatisfiable k-CNF formula in n variables with t clauses and let  $m \ge 2$  be an integer. There is an explicit monotone Boolean function  $f_{F_n,m}$  with  $N = tm^k$  input bits such that the communication complexity of  $Search_F \circ Index_m^n$  is at most that of  $mKW_{f_{F_n,m}}$ .

*Proof.* The function  $f_{F_n,m}$  takes as an input a string  $\alpha$  that is thought of as describing an *k*-CSP (constraint satisfaction problem with constraint size *k*) on the input space  $[m]^{[n]}$  using the variable structure of the formula  $F_n$ . Each clause  $C_\ell$  for  $\ell \in [t]$  involves some subset  $S_\ell$  of

indices in [*n*]. Fix such an  $\ell$ . The string  $\alpha$  will have one bit for each way of choosing the *k* elements in  $[m]^{S_{\ell}}$  indicating the truth table of the constraint indexed by  $\ell$  in the *k*-CSP.

Sometimes the *k*-CSP defined by  $\alpha$  will be satisfiable and sometimes it is not. We define  $f_{F_n,m}(\alpha) = 1$  iff the *k*-CSP defined by  $\alpha$  is satisfiable. Clearly changing 0's to 1's in  $\alpha$  can only make the *k*-CSP defined by  $\alpha$  more satisfiable so  $f_{F_n,m}$  is a monotone function of  $\alpha$ .

In order to simulate an algorithm for  $Search_{F_n}^n \circ Index_m^n$ , on input assignment  $(x, y) \in [m]^n \times (\{0, 1\}^m)^n$  with  $x = (x_1, \ldots, x_n) \in [m]^n$  and  $y = (y_1, \ldots, y_n) \in (\{0, 1\}^m)^n$ , Alice creates the string  $\alpha_x$  corresponding to the k-CSP whose  $\ell$ -th constraint is 1 iff for every  $i \in S_\ell$ , the input is consistent with  $x_i$ . The k-CSP given by  $\alpha_x$  is satisfied by x so  $f_{F_n,m}(\alpha_x) = 1$ . On the other hand, Bob creates the string  $\alpha_y$  corresponding to the unsatisfiable k-CSP whose  $\ell$ -th constraint evaluates to 1 iff for the vector  $x'_{S_\ell} \in [m]^{S_\ell}$ , the partial assignment  $(y_{ix'_i})_{i \in S_\ell}$  satisfies  $C_\ell$ . Since  $F_n$  is unsatisfiable, for every  $x' \in [m]^n$ , the formula on  $F_n$  with inputs  $y_{1x'_1}, \ldots, y_{nx'_n}$  is not satisfied by y and hence the k-CSP in given by  $\alpha_y$  is unsatisfiable so  $f_{F_n,m}(\alpha_y) = 0$ .

The  $mKW_{f_{F_{n,m}}}$  protocol for input pair  $(\alpha_x, \alpha_y)$  produces some index  $(\ell, x'_{S_\ell}) \in [t] \times [m]^k$ such that  $\alpha_x$  has value 1 and  $\alpha_y$  has value 0. Since  $\alpha_x$  has value 1, by definition  $x'_{S_\ell} = x_{S_\ell}$ . However, since  $\alpha_y$  has value 0, by definition of  $\alpha_y$ , the partial assignment  $(y_{ix_i})_{i \in S_\ell}$  does not satisfy  $C_\ell$  which means that  $\ell$  is a correct output for  $Search_{F_n} \circ Index_m^n$ .

**Corollary 12.5.** If  $F_n$  is an unsatisfiable family of k-CNF formulas requiring Resolution refutation depth D then for  $m = n^c$  for sufficiently large c,  $f_{F_n,m}$  requires monotone circuit depth  $\Omega(D \log n)$ .

*Proof.* The assumption implies that  $C^{dt}(Search_{F_n}) \ge D$ . By Theorem 12.3,  $C^{cc}(Search_{F_n} \circ Index_m^n)$  is  $\Omega(D \log n)$ . By Lemma 12.4, this implies that  $C^{cc}(mKW_{f_{F_n,m}})$  is  $\Omega(D \log n)$  which implies that  $f_{F_n,m}$ , requires monotone circuit depth  $\Omega(D \log n)$ .

Note that when *k* is constant and we can prove some Resolution depth lower bound of the form  $D = n^{\epsilon}$  for  $\epsilon > 0$  then by choosing some  $m = n^{c}$  we obtain a lower bound of the form  $N^{\delta}$  for some  $\delta > 0$  on the explicit monotone Boolean function *f* defined by this construction.

Göös, Pitassi, and Watson improved the lower bound in Theorem 12.3 using a refined version of the argument to show that one can take *m* as small as  $n^2 \log n$ . One can find explicit 3-CNF Boolean formulas in *n*-variables and a linear number of clauses that require Resolution depth  $\Omega(n/\log n)$  but this still yields a monotone function *f* with a fixed polynomial blow-up in *n* and so the lower bound is only of the form  $n^{\delta}$  for some small  $\delta < 1$ .

# A Deterministic Lifting Theorem using Thickness We first observe that $Index_m$ is a universal gadget.

#### **Proposition 12.6.** Any function $g : [m] \times [m] \rightarrow \{0, 1\}$ is a subfunction of $Index_m$ .

*Proof.* On input  $(x, y) \in [m] \times [m]$ , define  $y' \in \{0, 1\}^m$  by  $y'_i = g(i, y)$ . Then by definition  $Index_m(x, y') = y'_x = g(x, y)$ .

Therefore, any lifting theorem involving such a gadget *g* that has communication complexity  $\Omega(\log m)$  yields a lifting theorem for  $Index_m$ .

Rather than proving the version of the lifting theorem given by Raz-McKenzie and Göös-Pitassi-Watson using  $Index_m$ , we prove the following lifting theorem using the Inner Product function  $IP_d$  for  $d = O(\log n)$  which was proved independently by Chattopadhyay, Koucký, Loff, and Mukhopadhyay and by Wu, Yao, and Yuen. The description of the argument is based on a version due to Rao and Yehudayoff, but the key ideas are the same as all of the other proofs.

**Theorem 12.7.** *Let*  $f : \{0, 1\}^n \to \{0, 1\}$  *for*  $n \ge 64$  *and*  $d \ge 7 \log n$ *, then* 

$$C^{cc}(f \circ IP_d^n) = \Omega(C^{dt}(f) d).$$

Similarly, if R is a relation on  $\{0, 1\}^n$  then

$$C^{cc}(R \circ IP_d^n) = \Omega(C^{dt}(R) d)$$

By the universality of  $Index_m$ , this implies Theorem 12.3 for  $m \ge n^7$ . The method of proof of Theorem 12.7 is constructive. In particular, we show how to take any communication protocol *P* computing  $f \circ IP_d^n$  with *C* bits of communication and produces a decision tree *T* for *f* of height at most 20C/d.

In particular, the protocol *P* operates on inputs  $x, y \in (\{0, 1\}^d)^n$ , while the decision tree *T* built from *P* operates on inputs  $z \in \{0, 1\}^n$ . *T* is built from *P* by simulation.

The general idea is that we begin the simulation at the root of both the protocol *P* and the as-yet-unbuilt tree *T*. At each node *v* in *P* and the corresponding nodes *v'* of *T* we maintain sets  $A = X_{v,v'} \subseteq X$  and  $B = Y_{v,v'} \subseteq Y$  such that the rectangle  $A \times B$  of inputs consistent with the current simulation. We follow a path in *P* until the protocol *P* has "learned too much" about the portion of *x* and *y* in some coordinate  $j \in [n]$ . At that point we label the current node *v'* in *T* by a query to  $z_j$  and create two child nodes in *T*,  $v'_0$  for the answer  $z_=0$  and  $v'_1$  for the answer  $z_i = 1$ . For  $b \in \{0, 1\}$  and each node  $v'_b$  we find some fixed pair of strings  $(x_j^b, y_j^b) \in IP_d^{-1}(b)$  and define  $X_{v,v'_b} = X_{v,v'}|_{x_j = x_j^b}$  and  $Y_{v,v'_b} = Y_{v,v'_b}|_{y_j = y_j^b}$  and continue the simulation. The goal is to show that  $\Omega(d)$  steps of the protocol *P* are required per coordinate queried.

The simulation keeps track of a set  $S \subseteq [n]$  of unqueried coordinates. The simulation depends on the sizes of the projections  $A_{S'}$  and  $B_{S'}$  of A and B on subsets  $S' \subseteq S$  of unqueried coordinates. In particular, the simulation focuses on the sets of cases  $S' = S \setminus \{j\}$  which we write as S - j for simplicity.

We say that *A* is *j*-abundant iff  $|A_S|/|A_{S-j}| \ge 2^{6d/7}$  and *j*-sparse otherwise. *B* is *j*-abundant iff  $|B_S|/|B_{S-j}| \ge 2^{6d/7}$  and *j*-sparse otherwise. For  $a \in A_{S-j}$  we say that *a* is thin iff *a* has  $< 2^{4d/7}$  extensions in  $A_S$ . Similarly for  $b \in B_{S-j}$  we say that *b* is thin iff *b* has  $< 2^{4d/7}$  extensions in  $B_S$ . We say that *A* and *B* are thick iff there are no thin *a* or thin *b* for either *A* or *B*.

Note that *A* being *j*-abundant is about the average number of extensions for elements  $a \in A_{S-j}$ , whereas being thick bounds the worst-case number of number of extensions for all  $a \in A_{S-j}$ .

For a coordinate  $j \in S$ , and a value  $c \in \{0, 1\}$  we say that that a rectangle  $U_j^b \times V_j^b$  is *c*-good iff it is a *c*-rectangle for  $IP_d$  and the set  $A^c \times B^c$  consisting of the elements  $(x, y) \in A \times B$  such that  $x_j \in U_j^c$  and  $y_j \in V_j^c$  satisfies  $|A_{S-j}^c \times B_{S-j}^c| \ge |A_{S-j} \times B_{S-j}|/2$ .

Algorithm 7 Deterministic simulation for IP lifting.

1: Initialize: 2: For every node *u* of *P*, let  $R_u = A_u \times B_u$  be the associated rectangle. 3:  $S \leftarrow [n]$ 4: *A*, *B* ←  $(\{0, 1\}^d)^n$ 5:  $v \leftarrow \text{root of } P$ 6: Create *T* be a tree with one root node v'7: **procedure** BUILD-TREE(A, B, S, v, v')while  $S \neq \emptyset$  and v is not a leaf of P do 8: 9: if for every  $j \in S$ , A and B are both j-abundant then Let  $v_0$  and  $v_1$  be the children of v in P. ▷ Simulate another step of *P*. 10: Choose  $b \in \{0, 1\}$  for which  $|(A \times B) \cap R_{\nu_h}| \ge |A \times B|/2$ . 11: 12:  $v \leftarrow v_b$  $A \leftarrow A \cap A_{\nu_h}$  and  $B \leftarrow B \cap B_{\nu_h}$ 13: else if there is some  $j \in S$  and thin  $a \in A_{S-i}$  or thin  $b \in B_{S-i}$  then 14: while there is a thin *a* for *A* or thin *b* for *B* do  $\triangleright$  Prune the sets A and B 15: Remove all elements of *A* that extend *a*. 16: Remove all elements of *B* that extend *b*. 17: ▶ Both *A* and *B* are thick but at least one is *j*-sparse 18: else Create a query to  $z_i$  at node v', adding children  $v'_0$  and  $v'_1$  for query answers 0 19: and 1. Let  $U_i^0 \times V_i^0$  be a 0-good rectangle for  $A \times B$ . Requires proof 20:  $A^0 \leftarrow$  the vectors in A with *j*-th coordinate in  $U_j^0$ . 21:  $B^0 \leftarrow$  the vectors in B with j-th coordinate in  $V_i^0$ . 22: BUILD-TREE  $(A^0, B^0, S - j, v, v'_0)$ . 23: Let  $U_i^1 \times V_i^1$  be a 1-good rectangle for  $A \times B$ . ▷ Requires proof 24:  $A^1 \leftarrow$  the vectors in A with *j*-th coordinate in  $U_i^1$ . 25:  $B^1 \leftarrow$  the vectors in B with j-th coordinate in  $V_i^1$ . 26: BUILD-TREE  $(A^1, B^1, S - j, v, v'_0)$ . 27: Make v' a leaf labelled by the label of leaf v (or the value of f(z) or some element of 28: R(z) if  $S = \emptyset$ ).

**Analyzing the simulation** Observe that when the procedure BUILD-TREE is called initially, both *A* and *B* are thick, every  $a \in A_{S-j}$  and  $b \in B_{S-j}$  has  $2^d$  extensions in  $A_S$  and  $B_S$  respectively and  $A_S/A_{S-j} = B_S/B_{S-j} = 2^d$ . Therefore, *A* and *B* are both *j*-abundant and thick. This will cause the procedure to do simulation according to steps 10-13. At each step either *A* and *B* will decrease by a factor of at most 2 and this will change both the abundance and thickness properties by at most a factor of 2 per bit of communication. Therefore, it will take  $\Omega(d)$  steps of communication in order to create a thin *a* or *b* or to make *A* or *B j*-sparse. Therefore it takes  $\Omega(d)$  communication steps before the first query.

We need to keep versions of this property in order to allocate the cost of each query to  $\Omega(d)$  bits of communication. Moreover, in the simulation in addition to ensuring this allocation that the rectangles  $U_j^0 \times V_j^0$  and  $U_j^1 \times V_j^1$  in steps 20 and 24 both exist and have good properties assuming that both *A* and *B* are thick and either *A* or *B* is *j*-sparse.

**Lemma 12.8.** Let A and B be the sets before a prune step and A' and B' be the sets after the prune step. Then  $|A_S \times B_S| \ge (7/8)^2 |A'_S \times B'_S|$ .

*Proof.* Observe that the only way that a prune step occurs in the simulation is if the preceding step was a simulation step because prune steps don't follow each other and query steps only reduce  $A \times B$  based on coordinates j that are removed from S and so don't contribute to the creation of thin a or b. Let  $A^-$  and  $B^-$  be the sets prior to that preceding simulation step. Then  $|A_S^-|/|A_{S-j}^-| \ge 2^{6d/7}$  and  $|B_S^-|/|B_{S-j}^-| \ge 2^{6d/7}$  for all  $j \in S$ . The simulation step implies that either  $|A| \ge |A^-|/2$ ,  $B = B^-$  or  $A = A^-$  and  $|B| \ge |B^-|/2$ . In particular,  $|A_S|/|A_{S-j}| \ge 2^{6d/7}/2$  and  $|B_S|/|B_{S-j}| \ge 2^{6d/7}/2$ . Rewriting, we obtain that  $|A_{S-j}| \le 2^{16/7}$  and  $|B_{S-j}| \le 2^{16/7}/2$ . Each thin a (respectively b) defined on S - j results in the removal of fewer than  $2^{4d/7}$  elements of A (respectively B). Therefore the total number of elements removed from A is less than

$$\sum_{j \in S} |A_{S-j}| 2^{4d/7} \le \sum_{j \in S} 2|A_S| / 2^{2d/7} \le 2n|A_S| / 2^{2d/7} = 2n|A_S| / n^2 = 2|A_S| / n \le |A_S| / 8 \le 2n|A_S| / 2^{2d/7} \le 2n|A_S| /$$

since  $d \ge 7 \log n$  and  $n \ge 16$ . Similarly we have the total number of elements removed from *B* being at most  $|B_S|/8$ . In particular, this implies that  $|A'_S| \ge 7|A_S|/8$  and  $|B'_S| \ge 7|B_S|/8$  which yields the claimed property.

**Corollary 12.9.** Assume that elements in steps 20 and 24 can always be found. For every step of the BUILD-TREE procedure, if A, B and S are the values at the beginning of the step and A', B' and S' are the values at the end of the step, then  $|A'_{S'} \times B'_{S'}| \ge |A_{S'} \times B_{S'}|/2$ .

*Proof.* In the simulate step, the value of *S* does not change and the property holds by construction in Step 11. In the prune step the set *S* also does not change, and the property holds by Lemma 12.8, Finally, in each of the branches of the query step, the condition of the values found in Steps 20 and 24 being 0-good and 1-good respectively ensure that the property holds on both branches.

**Lemma 12.10.** Assuming that Steps 20 and 24 always are achievable the depth of the decision tree T is at most 20C(P)/d.

Proof. The measure of progress at each step will be based on

$$\Phi = \frac{|A_S \times B_S|}{2^{2|S|d}} \le 1.$$

At the start,  $\Phi = 1$ . Observe that if *A* is *j*-sparse then  $|A_{S-j}| > |A_S|/2^{6d/7}$  and we always have that  $|A_{S-j}| \ge |A_S|/2^d$ ; analogous properties hold for *B*. Therefore after a query step to  $z_j$ , the new value of  $\Phi$  for  $z_j = c$  is

$$\begin{aligned} \frac{|A_{S-j}^{c} \times B_{S-j}^{c}|}{2^{2(|S|-1)d}} &\geq \frac{|A_{S-j} \times B_{S-j}|}{2^{2(|S|-1)d+1}} & \text{ since the restrictions on coordinate } j \text{ are } b \text{-good} \\ &= \frac{|A_{S-j}| \cdot |B_{S-j}|}{2^{2(|S|-1)d+1}} \\ &> \frac{|A_{S}| \cdot |B_{S}|}{2^{6d/7} \cdot 2^{d} \cdot 2^{2(|S|-1)d+1}} & \text{ since one of } A \text{ or } B \text{ is } j \text{-sparse} \\ &= 2^{d/7-1} \frac{|A_{S} \times B_{S}|}{2^{2|S|d}}. \end{aligned}$$

Therefore, each query step increases the progress measure  $\Phi$  by a factor of at least  $2^{d/7-1}$ . Note that since  $d = 7 \log n$  and  $n \ge 16$ , we have  $d/7 - 1 \ge d/7 - d/28 = 3d/28$ . There are at most C(P) simulate steps and at most C(P) prune steps along any path in T in the course of the algorithm. Each simulate step or prune step leaves the set S unchanged and therefore reduces  $\Phi$  by at most a factor of 2. Therefore there must be at most 2C(P)/(d/7 - 1) query steps in total which is at most 56C(P)/(3d) < 20C(P)/d query steps on any root-leaf path.

Clearly, by construction every input in the set  $A \times B$  always is consistent with the partial assignment to the variables  $z_i$  for  $i \in [n] \setminus S$  and therefore the construction yields a correct decision tree for f.

Note that the choices made by the simulation only depend on  $A_s$  and  $B_s$  so it always suffices to keep one extension in A for each vector in  $A_s$  and one in B for each vector in  $B_s$ , so we could always ensure that  $|A| = |A_s|$  and  $|B| = |B_s|$ , though the above simulation in steps 21,22,25,26 does not do this.

It merely remains to prove that 0-good and 1-good rectangles can be found in Steps 20 and 24. Since *A* and *B* are both thick, every  $a \in A_{S-j}$  and  $b \in B_{S-j}$  has many extensions (at least  $2^{4d/7}$  of them) on coordinate *j* in the corresponding set, so (a, b) has a rectangle  $A_j^{(a)} \times B_j^{(b)}$  of possible extensions on coordinate *j* inside  $A \times B$ .

We want to ensure that at least 1/2 of such pairs (a, b) there is some pair of extensions in the *j*-th coordinate for which  $IP_d$  evaluates to 0 and some pair of extensions in the *j*-coordinate for which  $IP_d$  evaluates to 1. Note that different pairs (a, b) may have different possible extensions to the *j*-th coordinate. This uses the following lemma:

**Lemma 12.11.** Let  $U \times V$  be a rectangle in  $(\{0,1\}^d)^2$  with  $|U|, |V| \ge 2^{4d/7}$ . There are probability distributions  $\mathscr{R}^0$  on 0-rectangles of  $IP_d$  and  $\mathscr{R}^1$  on 1-rectangles of  $IP_d$  such that for  $c \in \{0,1\}$ ,

$$\Pr_{R \sim \mathscr{R}^c} [R \cap (U \times V) \neq \emptyset] \ge 1 - 4/2^{d/14}.$$

Before proving this lemma we see how it yields 0-good and 1-good rectangles in Steps 20 and 24.

**Corollary 12.12.** If A and B are thick there are 0-good and 1-good rectangles  $U_j^0 \times V_j^0$  and  $U_j^1 \times V_j^1$  for  $A \times B$  (and hence  $|A_{S-j}^0 \times B_{S-j}^0| \ge |A_{S-j} \times B_{S-j}|/2$  and  $|A_{S-j}^1 \times B_{S-j}^1| \ge |A_{S-j} \times B_{S-j}|/2$ ).

*Proof.* For each pair  $(a, b) \in A_{S-j} \times B_{S-j}$  the set of extensions  $A_j^{(a)} \times B_j^{(b)}$  in coordinate *j* satisfies the properties of  $U \times V$  in Lemma 12.11. Therefore for c = 0 or c = 1, if we choose  $R \in \mathscr{R}^c$  then  $\Pr_{R \subseteq \mathscr{R}^c}[R \cap (A_j^{(a)} \times B_j^{(b)}) = \varnothing] \ge 1 - 4/2^{d/14} \ge 1/2$  since  $d = 7 \log n \ge 42$  for  $n \ge 64$ . Therefore, there is some fixed rectangle  $R = U_j^c \times V_j^c$  in the support of  $\mathscr{R}^c$  such that the

Therefore, there is some fixed rectangle  $R = U_j^c \times V_j^c$  in the support of  $\mathscr{R}^c$  such that the fraction of pairs  $(a, b) \in A_{S-j} \times B_{S-j}$  for which  $A_j^{(a)} \times B_j^{(b)}$  contains an element in R is at least 1/2 and hence  $|A_{S-j}^c \times B_{S-j}^c| \ge |A_{S-j} \times B_{S-j}|/2$ . This rectangle will be *c*-good.

It remains to prove Lemma 12.11 which involves defining the distributions  $\mathscr{R}^c$  for c = 0, 1.

*Proof of Lemma 12.11.* We first define  $\mathscr{R}^0$ . Assume that *d* is even. Choose *M* to be a random invertible  $d \times d$  matrix over  $\mathbb{F}_2$ . Define  $R_M = A_M \times B_M$  where  $A_M$  is the d/2-dimensional subspace spanned by the first d/2 rows of *M* and  $B_M$  is the d/2-dimensional subspace spanned by the last d/2 rows of  $M^{-1}$ . Therefore for every  $x \in A_M$  and  $y \in B_M$ ,  $x \cdot y = 0$  in  $\mathbb{F}_2$  so  $R_M$  is a 0-rectangle of  $IP_d$ .

If  $A_M$  were a random subset of  $\{0, 1\}^d$  of size  $|A_M|$  we would expect  $A_M \cap U$  to have an intersection with U of size  $|U||A_M|/2^d = |U|/2^{d/2} \ge 2^{4d/7}/2^{d/2} = 2^{d/14}$ . The situation with  $A_M$  which is a random subspace of dimension d/2 is quite close to that bound. T The proof that the intersection size is non-empty uses a 2nd moment method. If  $0^d \in U$  then  $A_M \cap U$  is trivially non-empty, so assume that  $0^d \notin U$ .

Let  $W = |A_M \cap U|$ . Then  $W = \sum_{u \in U} W_u$  where  $W_u$  is the indicator variable for  $u \in A_M$ . Now  $|A_M \setminus \{0^d\}| = 2^{d/2} - 1$  and every one of the  $2^d - 1$  non-zero *d*-bit vectors is in  $A_M$  with equal probability so

$$\mathbb{E}[W_u] = \Pr[W_u = 1] = \frac{2^{d/2} - 1}{2^d - 1} = \frac{1}{2^{d/2} + 1}$$

Therefore  $\mathbb{E}[W] = |U|/(2^{d/2} + 1) \ge 2^{4d/7}/(2^{d/2} + 1) < 2^{d/14-1}$ . Similarly, for non-zero  $u \ne u' \in \{0, 1\}^d$ ,

$$\mathbb{E}[W_{u}W_{u'}] = \Pr[W_{u} = W_{u'} = 1] = \binom{2^{d/2} - 1}{2} / \binom{2^{d} - 1}{2} = \frac{2^{d/2} - 1}{2^{d} - 1} \cdot \frac{2^{d/2} - 2}{2^{d} - 2} < \mathbb{E}[W_{u}]\mathbb{E}[W_{u'}].$$

Therefore since each  $W_u$  is an indicator variable.

$$\mathbb{E}[W^2] < \sum_{u \in U} \mathbb{E}[W_u^2] + \sum_{u \neq u' \in U} \mathbb{E}[W_u] \mathbb{E}[W_{u'}] = \sum_{u \in U} \mathbb{E}[W_u] + \sum_{u \neq u' \in U} \mathbb{E}[W_u] \mathbb{E}[W_{u'}]$$
$$= \mathbb{E}[W] + \sum_{u \neq u' \in U} \mathbb{E}[W_u] \mathbb{E}[W_{u'}] < \mathbb{E}[W] + \mathbb{E}[W]^2.$$

Therefore, the variance of W,  $\mathbb{E}[W^2] - \mathbb{E}[W]^2 < \mathbb{E}[W]$ .

The probability that  $|A_M \cap U| = W = 0$  is then at most the probability that  $W - \mathbb{E}[W] > \mathbb{E}[W]$ . By Chebyshev's inequality, this is at most  $1/\mathbb{E}[W]$  which is at most  $2/2^{d/14}$ . The same argument in dual form says that the probability that  $B_M \cap V| = 0$  is at most  $2/2^{d/14}$  and the probability that  $R_M = A_M \cap B_M$  does not hit  $U \times V$  is at most  $4/2^{d/14}$  as required.

To obtain the same result for c = 1, the distribution for  $\mathscr{R}^1$  is similar except that the set  $A_M$  is the span of the affine subspace given by shifting the span of rows 2 through d/2 of M by the first row of M and  $B_M$  is given by shifting the span of columns  $d/2+2, \ldots, d$  of  $M^{-1}$  by the first column of  $M^{-1}$ . Since the inner product of the first row of M and first column  $M^{-1}$  yields 1 and the other vectors are orthogonal, every  $(x, y) \in R \sim \mathscr{R}^1$  has an  $IP_d(x, y) = 1$ . Though the dimensions of these affine subspaces are smaller by 1 than in the case that c = 0, we do not need to deal with u = 0 or v = 0 separately and the same  $2^{d/14-1}$  upper bounds the analogous  $\mathbb{E}[W]$ .

# 13 Lifting with small deficiency/large min-entropy rate

It turns out that there are other measures of the *quality* of the rectangle  $A \times B$  being maintained that can help to prove lifting theorems. These measures are used in more powerful theorems that allow us to lift randomized decision trees to randomized communication complexity and even to extend this to DAG-like protocols, which allow us to extend lower bounds to *circuits* and general unrestricted proofs rather than just tree proofs, both for Resolution and for a more powerful system called *Cutting Planes* proofs that refutes CNF formulas using linear inequalities over the reals with integer coefficients instead of clauses.

**Definition 13.1.** The *entropy deficiency* (sometimes simply *deficiency*) of a distribution  $\mathscr{X}$  on universe X,  $D_{\infty}(\mathscr{X})$ , is  $\log_2 |X| - H_2(\mathscr{X})$  where  $H_2$  is the Shannon entropy. In particular for a subset  $A \subseteq X$ , we write  $D_{\infty}(A)$  for the uniform distribution on A, which is then  $\log_2(|X|/|A|)$ .

Intuitively if, as in the simulation steps for the lifting theorem in the previous section, we move to the node  $v_c$  with the larger of the two rectangles, we see that the deficiency of one of *A* and *B* goes up by at most 1 per step of simulation and the other is unchanged. In the typical simulation as we reduce the set *S* of unqueried variables, we focus on the deficiency of  $A_s$ , which will be uniform over  $A_s$  when we retain only one extension for each element of  $A_s$ . We remove coordinate *j* from *S* when much of the deficiency for  $A_s$  is due to missing values in coordinate *j*, so the deficiency of  $A_{s-j}$  will be less than that of  $A_s$ .

**Definition 13.2.** The *min-entropy* of a distribution  $\mathscr{X}$  on X,  $H_{\infty}(\mathscr{X})$ , is

$$\min_{x \in X} \log_2(1/\Pr_{\mathscr{X}}(x)).$$

For a distribution  $\mathscr{X}$  on a set  $X^S$ , the *min-entropy rate* of  $\mathscr{X}$  is the maximum  $\tau$  such that for every  $J \subseteq S$ ,  $H_{\infty}(\mathscr{X}_J) \geq \tau |J| \log_2 |X|$  or, equivalently, such that for all  $\alpha_J \in X^J$ ,

$$\Pr_{\mathbf{x}\sim\mathscr{X}}[x_J=\alpha_J] \le 1/|X|^{\tau|J|}.$$

Typically, the distributions we obtain in simulation arguments for lifting, like those in defining min-entropy rate, are induced by a simple uniform distribution on a larger set of coordinates and weighting projections from that set onto a smaller set of coordinates based on the number of their extensions.

The general idea of this other class of lifting arguments is that if there is some  $\alpha_J$  that has become too likely, which indicates that too much information has been learned about the coordinates in *J*, we should add queries to *J* to the tree *T*. These simulation algorithms will query those coordinates and continue.

#### **Improving values of** m for lifting with $Index_m$ ?

There is an inherent limitation in the lower bounds proving using the  $Index_m$  gadget because m is a large polynomial in n,  $n^7$  is the proof based on inner product so the best lower bound

possible is  $\tilde{\Theta}(n)$  which is well below the number of input bits N = nm, namely  $N^{1/8}$ . The best lower bound proven using thickness has  $m = n^2$  so the lower bound would be  $N^{1/3}$ .

We will now discuss an alternative approach based on robust sunflower theorems that works when *m* is as small as  $\Theta(n \log n)$  which at best yields a roughly  $\sqrt{N}$  communication lower bound. The lower bounds that we show for depth also be extended to monotone circuit size lower bounds (not just formula size or depth) and the quality of those bounds are limited by the having large values of *n*.

**Open Problem 13.3.** Can we prove lifting theorems with  $Index_m$  gadgets for m constant? What about for  $m = \log^{O(1)} n$ ?

Note that by the universality of  $Index_m$ , lifting with any constant-size gadget implies lifting with constant-size  $Index_m$  gadgets. Such lifting lower bounds would give us much stronger monotone depth lower bounds as well as stronger lifting theorems: There is an additional  $m^k$ factor that shows up in the number of input bits for the function  $f_{F_n,m}$  where  $k \ge 3$  is a fixed constant that contributes to further degradation in the best current lower bounds possible with  $m \ge n$ . Lifting with constant m would yield near-optimal  $\Omega(n)$  lower bounds and with  $m = \log^{O(1)} n$  would yield  $\tilde{\Omega}(n)$  lower bounds and many other consequences.

#### Lifting with Sunflowers

**Theorem 13.4.** Let  $m \ge n^{1+\varepsilon}$ . For every search problem R defined on  $\{0, 1\}^n$  we have

$$C^{cc}(R \circ Index_m^n) = \Omega(C^{dt}(R) \log m).$$

The basic structure of this sunflower-based lifting theorem due to Lovett, Meka, Mertz, Pitassi, and Zhang is very similar to those other more advanced lifting theorems, despite its unique use of a recently improved lemma about sunflowers. In the following we prove this for  $m \ge n^{1.2}$ .

The invariant that this simulation maintains is based on the following definition:

**Definition 13.5.** For a restriction  $\rho$  defined on  $\{0, 1\}^n$ , a rectangle  $A \times B \subseteq [m]^n \times \{0, 1\}^{nm}$  is  $\rho$ -structured iff for the set *S* of variables unset by  $\rho$ :

- For every  $i \in \overline{S}$ ,  $A_i \times B_i$  contains precisely one pair of values  $(x_i, y_i)$  for which  $Index_m(x_i, y_i) = \rho(i)$ .
- $A_{\rm S}$  has min-entropy rate at least 0.9.
- $B_S$  has deficiency at most  $2n \log m$ . That is,  $|B_S| \ge 2^{m|S|-2n \log m}$ .

In particular, when the simulation is being executed, if a rectangle is associated with a node v' of the decision tree being built for f and  $\rho$  is the restriction associated with the path from the root to v' in T, then the rectangle maintained in the simulation will be  $\rho$ -structured.

The outline of the algorithm is given below. The basic simulation does not require queries when the min-entropy rate of  $A_s$  is high. However, when that drops there is an entire subset

*J* of coordinates that is at risk and must be queried. The simulation algorithm chooses some maximal subset *J* that violates the min-entropy rate condition for some fixed partial assignment  $\alpha_J$ . The algorithm adds queries for all of the elements of *J* to the tree *T*. Because of the maximality of the set *J* being chosen, the min-entropy rate after  $A_S$  is forced to  $\alpha_J$  on *J* will necessarily be be restored on the coordinates of S - J.

In order to continue, for every assignment  $\beta \in \{0, 1\}^J$ , we will need that the algorithm can continue if the query answers give  $z_J = \beta$ . In particular, this will hold because of the conditions on *A* and *B* in the previous step:

**Lemma 13.6** (Full Range Lemma). Let c > 0 be a constant and  $m \ge n^{1.2}$ . Suppose that  $A' \times B'$ is a  $\rho$ -structured rectangle with unset $(\rho) = S$  and  $A \times B \subseteq A' \times B'$  satisfies  $|A \times B| \ge |A' \times B'|/2^c$ . Then for every  $J \subseteq S$  there exists an  $x_J^* \in A_J$  such that for every  $\beta \in \{0, 1\}^J$  there is a  $y_\beta \in B_J$ with  $Index_m^J(x_J^*, y_\beta) = \beta$ .

We postpone the proof of the full range lemma (which is based on sunflower properties) until after completing the description of the simulation.

However, in order to maintain the invariant that we have a  $\rho\beta$ -structured rectangle associated with each leaf of the queries to  $Z_J$  in T, we also need that for every assignment  $\beta$  to J, the set of  $y \in B_S$  for which  $Index_m^J(\alpha_J, y_J) = \beta$  not only is non-empty, it is roughly at a  $2^{-|J|}$ fraction of the inputs in B; we can't quite do that but we will guarantee that for every  $\beta$ , it is at least an  $m^{-|J|}$  fraction of Y. That condition is what we mean for the choice in Step 16 in the outline of the simulation to be "good".

The fact that it is even possible to ensure that all  $2^{|J|}$  sets  $B^{\beta}$  have small deficiency comes from a key notion in the advanced lifting theorems: A *density restoring rectangle partition*. This is achieved by the following algorithm that is executed as part of Step 16:

The following lemma which we use to analyze the progress is almost immediate:

**Lemma 13.7.** For all  $\ell < L$ , the deficiency of  $A_{S-J_{\ell}}^{\ell}$  given by DENSITY-RESTORING-PARTITION is at least  $0.1|J_{\ell}|\log m - 1$  smaller than that of  $A_S$ .

*Proof.*  $A^{\ell} = \{x \in A^{\geq \ell} \mid x_{J_{\ell}} = \alpha_{J_{\ell}}\}$  and we had  $|A^{\ell}| > |A^{\geq \ell}| / m^{0.9|J_{\ell}|} \ge |A| / (2m^{0.9|J_{\ell}|})$ . Since  $|A^{\ell}| = |A_{S-J_{\ell}}^{\ell}$ , the deficiency of  $A_{S-J_{\ell}}^{\ell}$  is at most

$$(|S - J_{\ell}|)\log m - (\log_2 |A| - 0.9|J_{\ell}|\log m - 1) = |S| - \log_2 A - (0.1|J_{\ell}|\log m - 1)$$

which is what we wanted to show since  $|A|_S = |A|$ .

We now show that one of the choices of  $\ell$  in the partition gives us rectangles that are good enough to allow us to continue the simulation:

**Lemma 13.8** (Worst-Case Size Lemma). Let c > 0 be a constant and  $m \ge n^{1.2}$ . Suppose that  $A' \times B'$  is a  $\rho$ -structured rectangle with unset $(\rho) = S$  and that  $A \times B \subseteq A' \times B'$  satisfies  $|A \times B| \ge |A' \times B'|/2^c$ . Let  $\mathscr{F}$ ,  $(A^{\ell})_{\ell < L}$ ,  $(B^{\ell,\beta})_{\ell < L,\beta \in \{0.1\}^{J_{\ell}}}$  be the output of DENSITY-RESTORING-PARTITION(A, B, S). Then there is a  $(J_{\ell}, \alpha_{J_{\ell}}) \in \mathscr{F}$  such that for every  $\beta \in J_{\ell}$ ,  $|B^{\ell,\beta}| \ge |B|/m^{|J_{\ell}|}$ .

#### Algorithm 8 Outline of Deterministic min-entropy based simulation for Index lifting.

1: Initialize: 2: For every node *u* of *P*, let  $R_u = A_u \times B_u$  be the associated rectangle. 3:  $S \leftarrow [n]$ 4:  $A \leftarrow [m]^n$ ;  $B \leftarrow (\{0,1\}^m)^n$ 5:  $v \leftarrow \text{root of } P$ 6: Create *T* be a tree with one root node  $\nu'$ 7: **procedure** BUILD-TREE-INDEX(A, B, S, v, v') while  $S \neq \emptyset$  and v is not a leaf of P do 8:  $\rho \leftarrow$  label of path from root to  $\nu'$  in T 9: if *A* has min-entropy rate  $\geq 0.9$  then 10: Let  $v_0$  and  $v_1$  be the children of v in P.  $\triangleright$  Simulate another step of *P*. 11: Choose  $b \in \{0, 1\}$  for which  $|(A \times B) \cap R_{y_1}| \ge |A \times B|/2$ . 12: 13:  $v \leftarrow v_b$ 14:  $A \leftarrow A \cap A_{\nu_h}$  and  $B \leftarrow B \cap B_{\nu_h}$  $\triangleright$  *A* has min-entropy rate < 0.9 else 15: Choose some "good" maximal subset  $J \subseteq S$  and  $\alpha_J \in [m]^J$  s.t.  $\Pr_{x_J \sim A_J}[x_J = \alpha_J] >$ 16:  $m^{-0.9|J|}$ .  $A \leftarrow \{x \in A \mid x_I = \alpha_I\}$ 17: Extend tree *T* from v' by adding queries to all variables  $z_i$  for  $j \in J$ . 18: for all assignments  $\beta \in \{0, 1\}^J$  do 19:  $v'_{\beta} \leftarrow$  the leaf of *T* following  $\beta$  from  $\nu'$ . 20:  $B^{\beta} \leftarrow \{ y \in B \mid Index_m^J(\alpha_J, y_J) = \beta \}$ ▷ Not obvious that this is non-empty 21: BUILD-TREE-INDEX $(A, B^{\beta}, S - J, v, v_{\beta}')$ 22: Make v' a leaf labelled by the label of leaf v (or the value of  $f(\rho)$  or some element of 23:  $R(\rho)$  if  $S = \emptyset$ ).

Algorithm 9 Computes a partition

$$A \times B = \bigcup_{(J_{\ell}, \alpha_{J_{\ell}}) \in \mathcal{F}} \bigcup_{\beta \in \{0, 1\}^{J_{\ell}}} (A^{\ell} \times B^{\ell, \beta}) \bigcup (A^{\geq L} \times B^{\geq L})$$

such that  $|A^{\geq L}| \geq |A|/2$ , every  $A^{\ell}$  is 0.9 dense,  $A_{J_{\ell}}^{\ell} = \alpha_{J_{\ell}}$  and  $Index_{m}^{J_{\ell}}(A^{\ell} \times B^{\ell,\beta}) = \beta$ 1: **function** DENSITY-RESTORING-PARTITION(*A*, *B*, *S*)  $\mathscr{F} \leftarrow \varnothing; \ell \leftarrow 1; A^{\geq 1} \leftarrow A$ 2: while  $|A^{\geq \ell}| \geq |A|/2$  do. 3: Phase I Let  $J_{\ell} \subseteq S$  be a maximal set for which the min-entropy of  $A_{J_{\ell}}$  is less than  $0.9|S|\log m$ . 4: Let  $\alpha_{J_{\ell}} \in [m]^J$  witness this low min-entropy; that is,  $\Pr_{x_{J_{\ell}} \sim A_{J_{\ell}}^{\leq \ell}}[x_{j_{\ell}} = \alpha_J] > m^{-0.9|J_{\ell}|}$ . 5:  $\mathscr{F} \leftarrow \mathscr{F} \cup \{(J_{\ell}, \alpha_{J_{\ell}})\}.$ 6:  $A^{\ell} \leftarrow \{ x \in A^{\geq \ell} \mid x_{J_{\ell}} = \alpha_{J_{\ell}} \}.$  $A^{\geq \ell+1} \leftarrow A^{\geq t} - A^{\ell}; \ \ell \leftarrow \ell + 1.$ 7: 8:  $L \leftarrow \ell$ . 9: for all  $\ell < L$  do. ▷ Phase II 10: for all  $\beta \in \{0,1\}^{J_{\ell}}$  do 11:  $B^{\ell,\beta} \leftarrow \{ y \in B \mid Index_m^{J_\ell}(\alpha_{J_\ell}, y) = \beta \}.$ 12: return  $\mathscr{F}$ ,  $(A^{\ell})_{\ell < L}$ ,  $(B^{\ell,\beta})_{\ell < L,\beta \in \{0,1\}^{J_{\ell}}}$ 

*Proof.* For contradiction, assume that for every  $\ell \leq L$  there is some  $\beta^{\ell} \in \{0,1\}^{J_{\ell}}$  such that  $|B_{S}^{\ell,\beta^{\ell}}| < |B_{S}|/m^{|J_{\ell}|}$ . Define  $B_{\pm}$  to be the union of these  $B^{\ell,\beta^{\ell}}$  and  $B_{\neq} = B - B_{\pm}$ .

We first show that we must have  $|B_{\pm}| < |B|/2$ : Consider the number of  $\ell < L$  such that  $|J_{\ell}| = k$ .

Trivially, this is at most  $\binom{n}{k} \leq n^k \leq (m/3)^k$  since  $m \geq n^{1.2}$ , but this part of the proof doesn't actually need this constraint on m. The latter follows because, (By definition, since the set of inputs in  $A^{\ell}$  are a violation of min-entropy rate 0.9 for  $A^{\geq \ell}$  on the set  $J_{\ell}$ , for  $|J_{\ell}| = k$  we have  $|A^{\ell}| > |A^{\geq \ell}|/m^{0.9|J_{\ell}|} = |A^{\geq \ell}|/m^{0.9k} \geq |A|/(2m^{0.9k})$  and the sets  $A^{\ell}$  are disjoint. Therefore, we have at most  $2m^{0.9k} \leq (m/3)^k$  such sets since  $m^{0.1}/2 \geq 3$  for sufficiently large m.)

By our assumption that  $|B^{\ell,\beta^{\ell}}| < |B|/m^{|J_{\ell}|}$  for each  $\ell$ , we have

$$|B_{=}| < \sum_{k=1}^{n} (m/3)^{k} |B|/m^{k} = \sum_{k=1}^{n} |B|/3^{k} < |B|/2$$

Therefore  $|B_{\neq}| > |B|/2$ . Now define  $A^{<L} = \bigcup_{\ell < L} A^{\ell}$ . By the condition of Step 3, we have  $|A^{<L}| > |A|/2$ , so  $|A^{<L} \times B_{\neq}| > |A \times B|/4$ . Therefore  $A^{<L} \times B_{\neq}|$ , which is contained in  $A' \times B'$ , has  $|A^{<L} \times B_{\neq}| > |A' \times B'|/2^{c+2}$ . In particular the Full Range Lemma says that there is some  $x_{J}^{*} \in A^{<L}$  such that every  $\beta \in \{0, 1\}^{J}$  is a possible output in  $Index_{m}^{J}(\{x_{J}^{*}\} \times B_{\neq})$ . However,  $x_{J}^{*} \in A^{\ell}$  for some  $\ell$  which means that  $Index_{m}^{J}(\{x_{J}^{*}\} \times B_{\neq})$  does not contain  $\beta^{\ell}$ , which is a contradiction.  $\Box$ 

**Lemma 13.9** (Simulation Invariant). At the *t*-th step of the simulation for  $t \le n \log m$ , the rectangle  $A \times B$  is  $\rho$ -structured for some  $\rho$  whose unset bits are S and

#### Algorithm 10 Actual Deterministic min-entropy based simulation for Index lifting.

1: Initialize: 2: For every node *u* of *P*, let  $R_u = A_u \times B_u$  be the associated rectangle. 3:  $S \leftarrow [n]$ 4:  $A \leftarrow [m]^n$ ;  $B \leftarrow (\{0,1\}^m)^n$ 5:  $v \leftarrow \text{root of } P$ 6: Create *T* be a tree with one root node v'7: **procedure** BUILD-TREE-INDEX(A, B, S, v, v') while  $S \neq \emptyset$  and v is not a leaf of P do 8:  $\rho \leftarrow$  label of path from root to v' in T 9: if *A* has min-entropy rate  $\geq 0.9$  then 10: Let  $v_0$  and  $v_1$  be the children of v in P.  $\triangleright$  Simulate another step of *P*. 11: Choose  $b \in \{0, 1\}$  for which  $|(A \times B) \cap R_{y_h}| \ge |A \times B|/2$ . 12:13:  $v \leftarrow v_b$  $A \leftarrow A \cap A_{\nu_h}$  and  $B \leftarrow B \cap B_{\nu_h}$ 14:  $\triangleright$  *A* has min-entropy rate < 0.9 else 15:  $(\mathscr{F}, (A^{\ell})_{\ell < L}, (B^{\ell,\beta})_{\ell < L,\beta \in \{0,1\}^{J_{\ell}}}) \leftarrow \text{DENSITY-RESTORING-PARTITION}(A, B, S)$ 16: Let  $\ell$  satisfy  $|B^{\ell,\beta}| \ge |B|/m^{J_{\ell}}$  for all  $\beta \in \{0,1\}^{J_{\ell}}$ 17: ▷ Exists by Lemma 13.8  $A \leftarrow A^{\ell} \subseteq \{x \in A \mid x_{J_{\ell}} = \alpha_{J_{\ell}}\}$ 18: Extend tree *T* from v' by adding queries to all variables  $z_j$  for  $j \in J$ . 19: for all assignments  $\beta \in \{0, 1\}^{J_{\ell}}$  do 20:  $v'_{\beta} \leftarrow$  the leaf of *T* following  $\beta$  from  $\nu'$ . 21: Let  $\gamma \in (\{0,1\}^m)^{J_\ell}$  maximize  $|\{y \in B^{\ell,\beta} \mid y_{J_\ell} = \gamma\}|$ 22:  $B^{\beta} \leftarrow \{ y \in B^{\ell,\beta} \mid y_{J_{\ell}} = \gamma \}$ 23: BUILD-TREE-INDEX( $A, B^{\beta}, S - J_{\ell}, \nu, \nu'_{\beta}$ ) 24: Make v' a leaf labelled by the label of leaf v (or the value of  $f(\rho)$  or some element of 25:

 $R(\rho)$  if  $S = \emptyset$ ).

- $A_S$  has deficiency at most  $2t 0.1(n |S|) \log m$ ,
- $|B_{S}| \ge 2^{m|S|-t-(n-|S|)\log m}$ , and
- $A \times B \subseteq R_v$  where  $R_v$  is the rectangle associated with node v in P.

*Proof.* For the first part observe that for every simulate step,  $A_S$  can increase deficiency at most 1. This might require an additional density restoration which would replace S by  $S - J_{\ell}$  for some  $\ell$  but by Lemma 13.7 would reduce the deficiency by  $0.1|J_{\ell}|\log m - 1$ . Together, these yield the claimed bound.

For the second part observe that every simulate step can reduce the size of  $B_S$  by at most 1 and Lemma 13.8 implies that each  $|B^{\ell,\beta}| \ge |B|/m^{J_{\ell}} = |B| \cdot 2^{-|J_{\ell}|\log m}$  and hence  $|B^{\ell}_{S-J_{\ell}}| \ge |B|_S/m^{J_{\ell}}$  since we defined  $B^{\ell}$  using the assignment  $\gamma \in (\{0,1\}^m)^{J_{\ell}}$  giving the largest projection of  $B^{\ell,\beta}$  on  $S-J_{\ell}$ .

Since  $t \le n \log m$  we also have  $|B_S| \ge 2^{m|S|-2n \log m}$  as required to complete the proof that  $A \times B$  is  $\rho$ -structured, since the other properties have already been ensured by construction.

The last part follows from the simulation steps.

We now give the proof of the simulation theorem assuming the Full Range Lemma.

*Proof of Theorem 13.4 for*  $m \ge n^{1.2}$ . Let *P* be a protocol for  $R \circ Index_m^n$  of complexity C(P). By the Simulation Invariant Lemma, the deficiency of  $A_S$  when the simulation reaches a leaf of the decision tree is precisely

$$2t - 0.1(n - |S|)\log m \le 2C(P) - 0.1(n - |S|)\log m \ge 0$$

and the depth of the leaf of the decision tree is n - |S|. Therefore, the height of the decision tree is at most  $20C(P)/\log m$ .

Consider a leaf v' of T: If  $S = \emptyset$  then  $\rho$  is total and the output value is contained in  $R(\rho)$  by definition. If v is a leaf of P and  $A \times B \subseteq R_v$ . We need to argue that the label of v is a possible output of R(z) for all  $z \in \{0, 1\}^n$  consistent with  $\rho$ . Since  $A \times B \subseteq R_v$  is  $\rho$ -structured, and satisfies the conditions of the Full Range Lemma,  $Index_m^n(A \times B)$  contains all  $z \in \{0, 1\}^n$  consistent with  $\rho$  and hence the correctness of the label of v on  $A \times B$  implies that it is contained in R(z) for all z consistent with  $\rho$  and hence is correct in T.

A slight variant of the argument can prove the following:

**Theorem 13.10.** Let  $m \ge 3n \log m$ . For every search problem R defined on  $\{0, 1\}^n$  we have

$$C^{cc}(R \circ Index_m^n) = \Omega(C^{dt}(R)).$$

It only remains to prove the Full Range Lemma.

#### Proof of the Full Range Lemma

This is based on a newly improved Sunflower Lemma.

**Definition 13.11.** A collection of *k*-subsets of  $S_1, \ldots, S_p \subseteq \mathcal{U}$  is a *p*-sunflower iff there is a  $C \subseteq [n]$ , the core such that for every  $i \neq j$ ,  $S_i \cap S_j = C$ . For any *i* the sets  $S_i \setminus C$  are called *petals*. Note that a collection of *p* disjoint sets is a *r*-sunflower with an empty core.

A Sunflower Lemma gives an upper bound t on the size of a collection of k-sets needs to be in the worst case before it is guaranteed to contain some p-sunflower.

The original Sunflower Lemma of Erdös-Rado showed that this bound is at most  $(p-1)^k k!$  which is  $r^k$  for  $r \sim pk$ . Note that this is independent of the size of  $\mathcal{U}$ .

It is conjectured that the bound is  $r^k$  for  $r \sim p$ . There was a recent breakthrough by Alweiss, Lovett, Wu, and Zhang that reduced the dependence on k, with  $r \sim p^3 \log k \log \log k$ . Then Anup Rao improved this to  $r \sim p \log(pk)$  which is better than the Erdös-Rado bound for a much wider range of p and Bell, Chueluecha and Warnke improved the bound to  $r \sim p \log k$ . See the survey article by Anup Rao posted in the Bibliography.

#### Sketch of Sunflower Lemma argument

**Definition 13.12.** A family of *k*-sets in  $\mathscr{U}$  is *r*-spread iff for every subset  $Z \subseteq \mathscr{U}$ , at most a  $1/r^{|Z|}$  fraction of sets contains *Z*.

In proving an upper bound of the form  $r^k$  for containing k-Sunflowers it suffices to consider r-spread sets by the following reasoning: Suppose the family of sets of size  $r^k$  is not r-spread. Then there is a  $Z \subseteq \mathscr{U}$  such that at least  $r^{k-|Z|}$  sets all contain Z. Choose a maximal such set Z. We apply the inductive hypothesis to find a sub-family of sets with Z removed, which is a family of k' = k - |Z| sets. Observe that since Z was maximal, this family of at least  $r^{k'}$  k'-sets is r-spread. We apply the Sunflower Lemma for r-spread families to get a p-sunflower in this family and add Z back to every set to get the p-sunflower in the original family.

The following is a trivial form with r = pk, which is slightly worse than the original Erdös-Rado version; the new proofs are much more sophisticated but also yield families of p disjoint sets for much smaller r.

**Proposition 13.13.** Suppose that a family of  $\geq r^k$  k-sets is r-spread for r = pk. Then the family contains at least p disjoint sets.

*Proof.* Let *t* be the size of a maximal sub-collection of disjoint sets in the family (which together include *kt* elements). Suppose that t < p. Then some element  $z \in \mathcal{U}$  must be contained in at least a 1/(tk) > 1/(pk) = 1/r fraction of sets in the family which contradicts the assumption of *r*-spreadness with  $|Z| = \{z\}$ .

We now focus on the case that  $\mathcal{U} = [nm]$  consisting of *n* blocks, each of size *m*. We consider the case that k = n and the sets in the family have exactly 1 element per block; that is, the sets can be viewed as elements of  $[m]^n$ .

What does it mean for the family (subset of  $[m]^n$ ) to be *r*-spread when we express the condition in terms of elements of  $[m]^n$ ? It means that for every  $J \subseteq [n]$  and every partial assignment  $\alpha_J \in [m]^J$ , at most a 1/r|J| fraction of elements in the family are consistent with  $\alpha_J$ . Choosing  $r = m^{0.9}$ , we see that this is *precisely* the condition for the subset of  $[m]^n$  to have min-entropy rate at least 0.9!

The following is Lemma 4 in Anup Rao's 2020 Coding for Sunflowers paper (specialized for  $\gamma = 1/2$ ).

**Lemma 13.14.** There is a K > 0 such that the following holds. Let  $\varepsilon > 0$ . Suppose that a family of k-sets in  $\mathscr{U}$  is r-spread for  $r \ge K \log(k/\varepsilon)$  and has size at least  $r^k$ . Then a uniformly random subset W of  $\mathscr{U}$  contains an element of the family with probability at least  $1 - \varepsilon$ .

In our context, we will can think of  $\mathcal{U} = [nm]$  and k = n. A uniformly random subset W corresponds to a random vector in  $y \in (\{0, 1\}^m)^n$ . The family of k-sets that is r-spread for  $r = m^{0.9}$  with one element per block will be a set A of vectors in  $[m]^n$  that has min-entropy rate at least 0.9, and the event that the set W contains an element of the family is the same as saying that  $\exists x \in A \, Index_m^n(x, y) = 1^n$ . Lemma 13.14 says that if  $m^{0.9} \ge K \log(n/\varepsilon)$  then

$$\Pr[\exists x \in A. Index_m^n(x, y) = 1^n] \ge 1 - \varepsilon.$$

It is pretty clear in this statement that  $1^n$  is not special and by symmetry the same statement is true for every string  $\beta \in \{0, 1\}^n$ . However, this is would be wasteful and not enough for the Full Range Lemma, since it would allow the element  $x \in A$  to depend on  $\beta$  and we require something more subtle.

*Proof of the Full Range Lemma.* Let c > 0 be a constant and  $m \ge n^{1.2}$ . Let  $A' \times B'$  be a  $\rho$ -structured rectangle with  $unset(\rho) = S$  and  $A \times B \subseteq A' \times B'$  satisfies  $|A \times B| \ge |A' \times B'|/c$ . Let  $J \subseteq S$ .

We want to prove that there is an  $x_J^* \in A_J$  such that for every  $\beta \in \{0,1\}^J$  we have  $\beta \in Index_m^J(\{x_J^*\} \times B_J)$ .

We consider the negation of this Full Range property, which is that for every  $x \in A_J$  there is some  $\beta_x \in \{0,1\}^J$  such that  $\beta_x \notin Index_m^J(\{x\} \times B_J)$ . We prove that the set *E* of all strings  $y \in (\{0,1\}^m)^J$  such that  $Index_m^J(x,y) \neq \beta_x$  for every  $x \in A_J$  is much smaller than  $B_J$ , which ensures that the negation of the Full Range property is false.

More precisely, we will use Lemma 13.14 with k = |J| to show that for y chosen uniformly at random from  $\{0, 1\}^m$ , the probability that  $y \in E$  is at most  $\varepsilon = 2^{-3n \log m}$ , a much smaller fraction of such strings than the set  $B_J$ , which satisfies  $|B_J| \ge |B'_J|/2^c \ge 2^{|J|m-2n \log m-c}$ , since  $B'_S$ has deficiency at most  $2n \log m + c$ .

Since  $A'_{S}$  has min-entropy rate 0.9 and  $A \subseteq A'$  has  $|A| \ge |A'|/2^{c}$ , for every  $J' \subseteq J$ ,  $A_{J'}$  has min-entropy at least  $0.9|J|\log m - c$  which is min-entropy rate at least  $0.9-c/\log m$ . It follows that the family of |J|-sets in  $\mathcal{U} = [|J|m]$  corresponding to  $A_{J}$  is *r*-spread for  $r = m^{0.9}/2^{c}$  and contains at least  $r^{J}J$  sets. For sufficiently large n,

$$K \log(|J|/\varepsilon) \le K \log(n2^{3n\log m}) \le 6n \log m \le m^{0.9}/2^c = r$$

since  $m \ge n^{1.2}$ , so the family of sets in  $\mathscr{U}$  corresponding to  $A_J$  meets the pre-conditions of Lemma 13.14. However, it is not obvious how the conclusion of Lemma 13.14 gives the claimed bound on the size of *E*.

For each  $x \in A_J$  we define a |J|-term  $D_x$  in the bits of y indexed by x, which is true iff the vector of bits indexed by x has values  $\beta_x$ . By definition, the |J|-DNF formula  $D = \bigvee_{x \in A_J} D_x$  will be true on input y iff  $y \notin E$ .

To prove the bound, we consider the values of  $\beta_x$  that minimize the probability of satisfying D (which maximizes the probability of E). Each term is individually satisfied with precisely the same probability. If two terms  $D_x$  and  $D_{x'}$  are inconsistent with each other then they correspond to disjoint assignments which means that  $D_x \vee D_{x'}$  has more satisfying assignments than if  $D_x$  and  $D_{x'}$  are consistent. Therefore, the probability of satisfying D is minimized when all the  $D_x$  terms are consistent with a single  $\gamma \in (\{0, 1\}^n)^J$ .

By symmetry, the probability of satisfying such a D does not depend on the value of  $\gamma$  (it only depends on which bits the elements  $x \in A_J$  index), so we can assume wlog that all of the  $\beta_x$  values are  $1^{|J|}$ . In particular, this means that y satisfies D iff for some  $x \in A_J$  all bits in y indexed by x have value 1. This is precisely the condition that for some  $x \in A_J$ , the subset of  $\mathcal{U} = [|J|m]$  indexed by x is contained in the subset W of  $\mathcal{U}$  for which y is the characteristic function, which is precisely the event for which Lemma 13.14 yields a lower bound of  $1 - \varepsilon$ . Therefore the probability that y satisfies D is at least  $1 - \varepsilon$  and hence E contains at most a  $\varepsilon = 2^{-3n \log m}$  fraction of elements y as required and hence the Full Range Lemma follows.  $\Box$ 

## Limits of sunflower arguments

For the Full Range Lemma we can make it work for  $m \ge n^{1+\varepsilon}$  by replacing 0.9 by  $1 - \delta$  for  $\delta = \varepsilon/2$ . If we only require  $\Omega(1)$  simulation step per query step we can replace the min-entropy required per block of A to  $\log m - C$  for some large constant C (so that overall min-entropy of  $A_s$  goes up by a large constant per query). With this value it suffices for the Full Range Lemma to have  $r = m/2^C > 6Kn \log m$  which holds when m is at least  $C'n \log n$  for some constant C'.

# 14 Randomized Communication Complexity and Lifting

In communication complexity, lower bounds randomized strategies play an even larger role for applications than lower bounds for deterministic ones.

**Definition 14.1.** A *randomized decision tree* is a probability distribution over deterministic decision trees. Its cost is the maximum height over all (deterministic) decision treed in the support of the distribution.

A randomized communication protocol (with shared randomness) is a probability distribution over deterministic protocols. The communication cost of a randomized protocol is the maximum over all deterministic protocols in the support of the distribution. We say that a randomized decision tree (randomized communication protocol) *computes a function f with error at most*  $\varepsilon > 0$  iff for each input (resp. pair of inputs) to *f*, the probability that a tree (resp. protocol) chosen from the distribution produces the correct answer on that input with probability at least  $1 - \varepsilon$ .

We write  $C_{\varepsilon}^{dt}(f)$  (resp.  $C_{\varepsilon}^{cc}(f)$ ), respectively for the minimum cost of any randomized decision tree (randomized protocol) that computes f with error at most  $\varepsilon$ .

Probably the most important and widely applicable theorem in communication complexity is the following. Though there have been several improved and simpler proofs, none is especially easy.

**Theorem 14.2** (Kalyanasundaram-Schnitger). For any  $\varepsilon > 0$ ,  $C^{cc}(DISJ_n)$  is  $\Omega(n)$ .

The  $DISJ_n$  function is the composition  $OR_n \circ AND_2^n$ . One of the big goals of lifting would be to prove a general result that gives this lower bound for disjointness as a special.

GöÖs, Pitassi, and Watson proved the following lifting theorem for randomized computation.

**Theorem 14.3** (Göös-Pitassi-Watson). For c > 0 sufficiently large,  $\varepsilon > 0$ . and any Boolean relation R on  $\{0,1\}^n$ , if  $n \ge m^c$  then  $C_{\varepsilon}^{cc}(R \circ Index_m^n)$  is  $\Omega(C_{\varepsilon}^{dt}(R)\log m)$ .

The basic idea involving simulation is similar to that of the deterministic case - in fact the density-restoring partition method was first set up for this purpose. Since the randomized protocol is a distribution over deterministic protocols, the random choice for the decision tree first chooses one of these deterministic protocols but now does a further random simulation of the branches of that deterministic protocol.

A key difference with the deterministic case is that in the simulation steps, the algorithm cannot simply choose the larger set  $A \times B$  of consistent inputs. Instead, the algorithm must choose the bit to communicate according to something close to the probability that the original randomized algorithm sends 0 or 1. The algorithm also can't simply fix one of the choices in the density-restoring partition; it must generate distributions that are close to what the randomized algorithm does. To this end, it doesn't stop in the decomposition at |A|/2 and only finishes when there are no blocks with too little min-entropy.

Instead of the Full Range Lemma, we need the following much stronger property of  $Index_m$ :

**Lemma 14.4** (Uniform Marginals Lemma). Let  $A \times B$  be  $\rho$ -structured and fix any total assignment  $\sigma \in \{0, 1\}$  that is consistent with  $\rho$ . Then the set of  $(A \times B)_{\gamma}$  of inputs in  $A \times B$  such that  $Index_m^n(x, y) = \gamma$  is non-empty, and the projections onto A and B of the uniform distribution on  $(A \times B)_{\gamma}$  are within  $1/n^2$  of the uniform distribution on A and on B, respectively.

The Uniform Marginals Lemma lets the simulation choose the bits for Alice and Bob to send based on the probabilities within A and B separately and ignore the impact of the constraints of matching the queries that the decision tree makes, except when fixing bits of Bob's input to match Alice's input pointers that are fixed during density restoration. More precisely, when Alice sends a bit that splits A into  $A^0$  and  $A^1$ , the simulation chooses to send each value of b with probability  $|A^b|/|A|$ ; the simulation behaves similarly when Bob sends a bit. When running the density-restoring partition the algorithm chooses the set  $A^i$  that fixes some  $x_J$  to  $\alpha_J$ , with probability proportional to its size and then fixes *B* to match the  $\beta \in \{0, 1\}^J$  given by the query answers in the decision tree.

## **Randomized Lifting with Inner Product**

Chattopadhyay, Filmus, Koroth, Meir, and Pitassi extended the lower bound method for randomized algorithms to any "low discrepancy" gadget such as  $IP_d$  for  $d = \Theta(\log n)$ .

**Theorem 14.5.** For c > 0 sufficiently large,  $\varepsilon > 0$ . and any Boolean relation R on  $\{0,1\}^n$ , if  $d \ge c \log n$  then  $C_{\varepsilon}^{cc}(R \circ IP_d^n)$  is  $\Omega(C_{\varepsilon}^{dt}(R)d)$ .

The proof of this theorem is much more involved because we no longer have the asymmetry with Bob's input string being much larger than Alice's input so that small deficiency suffices. In this case one must keep track of the min-entropy rate for both Alice and Bob. Because of this, there needs to be some back-and-forth between restoring Alice's min-entropy rate and restoring Bob's, since fixing Alice's may ruin Bob's and vice versa. This requires a much stronger condition than  $A \times B$  being  $\rho$ -structured, and much more careful assessment of when coordinates become risky and need to be queried. Alice

## **15 DAG-protocols**

**Definition 15.1.** A subset  $C \subseteq \{0,1\}^n$  is a *cube* iff there is a partial assignment  $\rho \in \{0,1,*\}^n$  such that

 $C = \{x \in \{0, 1\}^n \mid x_i = \rho(i) \text{ for all } i \text{ set by } \rho\}.$ 

A *cube DAG* for a function f or relation R on  $\{0, 1\}^n$  is a directed acyclic graph G with out-degree 2, with each node u labelled by a cube  $C_u \subseteq \{0, 1\}^n$  subject to the following conditions:

- The root is labelled by  $\{0,1\}^n$
- For every node *u* with children *v* and *w* the cube  $C_u \subseteq C_v \cup C_w$ .
- Each sink *w* of *G* also has an output label  $\ell$  that is an allowable output of *f* (or *R*) for every  $x \in C_w$ .

A cube DAG is *reduced* iff the cube  $C_u$  for each node u is not contained in the cubes  $C_v$  or  $C_w$  for its children v and w. The *width* of a cube DAG is the maximum number of fixed positions in any cube in the DAG.

Note that any decision tree is a special case of a cube DAG where the cube associated with node v is the set of inputs consistent with the partial assignment reaching node v. In that case each  $C_u$  is the disjoint union of  $C_v$  and  $C_w$ .

In general, since  $C_u$ ,  $C_v$ , and  $C_w$  are all cubes, if a cube DAG is reduced then there must be precisely one bit where the partial assignments defining  $C_v$  and  $C_w$  take opposite signs.

If *F* is an unsatisfiable CNF formula with clauses  $D_1, \ldots, D_m$ , any Resolution refutation of *F* yields a cube DAG for *Search*<sub>F</sub> with the same DAG structure as follows: Let *G* be the DAG of the Resolution refutation with

- The root is labelled by the empty clause
- Each internal node *u* is labelled by a clause  $D_u$  that is the resolvent of the clauses  $D_v$  and  $D_w$  labelling its children *v* and *w* on some variable  $x_i$ .
- Each leaf node *w* is labelled by a clause *D<sub>i</sub>* of *F*.

We define cube  $C_u$  on the same structure as the set of inputs that falsify clause  $D_u$  (it is a cube given by the partial assignment associated with the conjunction  $\overline{C_u}$  and every leaf of the refutation that is labelled by input clause  $D_j$ , is labelled by output j in the cube DAG. The soundness of Resolution inference implies that  $C_u \subseteq C_v \cup C_w$ .

Conversely, any cube DAG for  $Search_F$  yields a Resolution refutation of F where we are allowed to freely add a *weakening rule* at the leaves that can add extra literals to an input clause. The fact that it is a Resolution refutation follows from the one bit flipped property between the child cubes of cube DAGs.

We therefore obtain the following:

**Proposition 15.2.** The minimum Resolution refutation size of an unsatisfiable CNF formula F is precisely the size of the smallest cube DAG for Search<sub>F</sub>. Moreover, the Resolution width of F is the width of the narrowest cube DAG solving Search<sub>F</sub>.

**Definition 15.3.** Given space  $X \times Y$  a *rectangle DAG* (a.k.a. a 2-party DAG protocol) for a function f or relation R on  $X \times Y$  is a directed acyclic graph G with out-degree 2, with each node u labelled by a rectangle  $R_u \subseteq X \times Y$  subject to the following conditions:

- The root is labelled by  $X \times Y$
- For every node *u* with children *v* and *w* the cube  $R_u \subseteq R_v \cup R_w$ .
- Each sink *w* of *G* also has an output label *l* that is an allowable output of *f* (or *R*) for every (*x*, *y*) ∈ *R<sub>w</sub>*.

A rectangle DAG is *reduced* iff the cube  $C_u$  for each node u is not contained in the cubes  $C_v$  or  $C_w$  for its children v and w; we assume that it is reduced w.l.o.g.

A 2-party communication protocol is a rectangle DAG (for *G* a tree) where  $R_v$  and  $R_w$  partition  $R_v$ .

Write  $R_u = A_u \times B_u$ ,  $R_v = A_v \times B_v$  and  $R_w = A_w \times B_w$ . Since  $R_u \subseteq R_v \cup R_w$  and all three are rectangles, We must have either

- $A_u \subseteq A_v \cup A_w$  and  $B_u \subseteq B_v, B_w$  ("Alice speaks"), or
- $A_u \subseteq A_v, A_w$  and  $B_u \subseteq B_v \cup B_w$  ("Bob speaks").

In DAG protocols, players can "forget" some of the past and only keep what they need to remember.

Rectangle DAGs were originally introduced by Razborov in order to extend Karchmer-Wigderson games to analyze *circuit* size and not just formula size and depth.

**Lemma 15.4.** The circuit complexity over the binary  $\land$ ,  $\lor$  and  $\neg$  basis for Boolean function f is the size of the smallest rectangle (2-party DAG protocol) for  $KM_f$  on  $f^{-1}(1) \times f^{-1}(0)$ .

The monotone circuit complexity over the  $\land, \lor$  basis for monotone Boolean function f is the size of the smallest rectangle DAG (2-party DAG protocol) for mKM<sub>f</sub> on  $f^{-1}(1) \times f^{-1}(0)$ .

*Proof.* The structure of the rectangle DAG (and that of the circuit match exactly. The correspondences are exactly the same as in the regular Karchmer-Widgerson games with ordinary protocols.  $\Box$ 

**Definition 15.5.** A set  $T \subseteq X \times Y$  is a *triangle* iff there are functions  $a_T : X \to \mathbb{R}$  and  $b_t : Y \to \mathbb{R}$  such that  $T = \{(x, y) \in X \times Y \mid a_T(x) > b_T(y)\}.$ 

The motivation for the term triangle is that we can sort the elements of *X* by decreasing  $a_T$  values and sort the elements of *y* by increasing  $b_T$  value and set *T* looks like a stepped triangle inside  $X \times Y$ : The entries of *T* are shifted to the upper left in the array, with each row have a number of entries at most that of the previous row.

**Definition 15.6.** We can define a *triangle DAGs* that computes a Boolean function f or relation R on  $X \times Y$  just as we have done for Rectangle DAGs. Each node u of the out-degree 2 DAG is labelled by a triangle  $T_u$  such that:

- The root is labelled by the triangle  $X \times Y$
- For node *u* with children  $T_v$  and  $T_w$ , we have  $T_u \subseteq T_v \cup T_w$
- Each sink w of G has a label that is an allowable answer for all inputs in  $T_w$ .

We will see that triangle DAGs are related to a much more powerful class of proofs than Resolution proofs:

**Definition 15.7.** Given a CNF formula *F* we can translate each clause of *F* into an integer linear inequality: For example, clause  $x_1 \lor \overline{x}_2 \lor x_3$  becomes the linear inequality  $x_1 + 1 - x_2 + x_3 \ge 1$  or, in standard form  $x_1 - x_2 + x_3 \ge 1$ .

Given an unsatisfiable CNF formula F, a (semantic) Cutting Planes refutation of F is a sequence of inequalities for the form  $a_1x_1 + \cdots + a_nx_n \ge D$  for  $a_1, \ldots, a_n, D$  all integers, ending in the inequality  $0 \ge 1$ , each of which is either a translation of an input clause of F or is a logical consequence of two prior inequalities. The sequence of inferences yields a refutation DAG with root  $0 \ge 1$ .

Consider an arbitrary partition of the variables of an unsatisfiable CNF *F* into two groups: *x* variables and *y* variables, so we can write each line  $\ell$  of the Cutting Planes refutation as  $a_{\ell}(x)+b_{\ell}(y) \ge D$ . We can associate a triangle *T* on *X*×*Y* with the set of assignments that falsify line  $\ell$  by setting  $a_T(x) = D - a_{\ell}(x)$  and  $b_T(y) = b_{\ell}(y)$ . Then  $a_T(x) > b_T(y)$  iff  $D - a_{\ell}(x) > b_{\ell}(y)$  iff  $a_{\ell}(x) + b_{\ell}(y) < D$ .

Following the same ideas as the previous arguments, we obtain the following:

**Lemma 15.8.** For every partition of the assignments of the variables of unsatisfiable CNF formula F into disjoint parts X and Y, the size of a (semantic) Cutting Planes refutation of is at least the size of the smallest triangle DAG for Search<sub>F</sub> on  $X \times Y$ .

*Proof.* The triangle  $T_u$  on  $X \times Y$  labelling each node u in the refutation will be the set of inputs falsifying the inequality at u, as described above. The fact that  $T_u \subseteq T_v \cup T_w$  at a node u with children u and v the the requires only the soundness of Cutting Planes inference, which is the "easy" direction in the previous arguments. The rest is as before.

**Definition 15.9.** A *monotone real gate* is any function  $g : \mathbb{R} \times \mathbb{R} \to \mathbb{R}$  such that for any real inputs (x, y) and (x', y') with  $x \ge x'$  and  $y \ge y'$  we have  $g(x, y) \ge g(x', y')$ . A *monotone real circuit* is a circuit composed of (two-input) monotone real gates, where the (monotone) function computed at each node is the composition of the gate function with the functions at its children.

Monotone real circuits carry real values along their wires, but we will be interested in how they can compute monotone Boolean functions that start at end with Boolean values. **Theorem 15.10.** The size of the smallest monotone real circuit computing a monotone Boolean function f is the size of the smallest triangle DAG computing  $mKW_f$  on  $f^{-1}(1) \times f^{-1}(0)$ .

*Proof.* There are two directions to the argument as before.

Suppose that we have a monotone real circuit. We normally think of a circuit as directed towards the output but we reverse the edges. For each gate u let  $f_u$  be the function computed at u. Beginning at the root we maintain the property that at each node u, the triangle  $T_u$  consists of the set of (x, y) such that  $f_u(x) > f_u(y)$ , which is a triangle by definition. This is clearly true at the root. Each sink corresponds to some input  $z_i$  to f so we give it output label i. We now have to show that  $T_u \subseteq T_v \cup T_w$  where v and w are the children of (input gates to) u. We have  $f_u(z) = g_u(f_v(z), f_w(z))$  where  $g_u$  is a monotone real gate. Let  $(x, y) \in T_u$ . Then  $f_u(x) > f_u(y)$  by definition. Therefore  $g_u(f_v(x), f_w(x)) > g_u(f_v(y), f_w(y))$ . Since  $g_u$  is a monotone function we must have  $f_v(x) > f_v(y)$  or  $f_w(x) > f_w(y)$  (or both). This is equivalent to  $(x, y) \in T_v$  or  $(x, y) \in T_w$ .

For the other direction, suppose that we have a triangle DAG solving  $mKW_f$  where each node u has  $T_u$  is the set of inputs  $(x, y) \in X \times Y$  such that  $a_u(x) > b_u(y)$ . We build the circuit bottom up maintaining the invariant that for the function  $f_u$  computed at u,  $f_u(x) > f_v(y)$ for every  $(x, y) \in T_u$ . We will maintain a stronger invariant, namely that for every node u,  $f_u(x) \ge a_u(x)$  and  $b_u(y) \ge f_u(y)$  for all  $x \in f^{-1}(1)$  and  $y \in f^{-1}(0)$ . For  $(x, y) \in T_u$  we have  $a_u(x) > b_u(y)$  and hence  $f_u(x) > f_v(y)$  holds for all  $(x, y) \in T_u$ .

Each sink *w* of the DAG is labelled by some  $i \in [n]$ . Since *i* is a correct answer to  $mKW_f$  $T_w$  is contained in the set  $\{x \in f^{-1}(1) : x_i = 1\} \times \{y \in f^{-1}(0) : y_i = 0\}$ , which is a triangle. We can replace each sink triangle with this potentially larger triangle and assume without loss

of generality that  $a_w(z) = b_w(z) = \begin{cases} 1 & z_i = 1 \\ 0 & z_i = 0 \end{cases}$ , since  $a_w(x) > b_w(y)$  implies that  $x_i = 1$  and

 $y_i = 0$ . The function  $f_w$  will be  $f_w(z) = z_i$  which satisfies the inductive conditions.

Now suppose that node *u* has children *v* and *w* and  $T_u \subseteq T_v \cup T_w$ .

We define the gate  $g_u$  based on the functions  $a_u, a_v, a_w$  as

$$g_u(\alpha,\beta) = \max_{s \in f^{-1}(1)} \{a_u(s) \mid a_v(s) \le \alpha \text{ and } a_w(s) \le \beta\}.$$

Clearly  $g_u$  is a monotone real-valued function in  $\alpha$  and  $\beta$  since increasing  $\alpha$  or  $\beta$  only allows more values to be considered for the max. Then  $f_u(z) = g_u(f_v(z), f_w(z))$ .

Suppose that  $x \in f^{-1}(1)$  which has

$$f_u(x) = \max_{s \in f^{-1}(1)} \{ a_u(s) \mid a_v(s) \le f_v(x) \text{ and } a_w(s) \le f_w(x) \}.$$
(4)

By the inductive hypothesis applied to v and w,  $a_v(x) \le f_v(x)$  and  $a_w(x) \le f_w(x)$ . Therefore both conditions in (4) hold for s = x and we have  $a_u(x)$  as one of the candidate values in the max for  $f_u(x)$ , so we obtain  $f_u(x) \ge a_u(x)$ . Similarly for  $y \in f^{-1}(0)$  we have

$$f_{u}(y) = \max_{z' \in f^{-1}(1)} \{ a_{u}(s) \mid a_{v}(s) \le f_{v}(y) \text{ and } a_{w}(s) \le f_{w}(y) \}.$$
(5)

Suppose that  $f_u(y) > b_u(y)$ . Then there must be some  $s \in f^{-1}(1)$  with  $a_u(s) > b_u(y)$  such that  $a_v(s) \le f_v(y)$  and  $a_w(s) \le f_w(y)$ . The first condition implies that the pair (s, y) must be in  $T_u$ by definition so  $(s, y) \in T_v$  or  $(s, y) \in T_w$ . W.l.o.g. suppose that  $(s, y) \in T_v$ . Then  $a_v(s) > b_v(y)$ . By the inductive hypothesis for v since  $y \in f^{-1}(0)$  we must have  $b_y(y) \ge f_y(y)$ , which implies that  $a_{\nu}(s) > f_{\nu}(y)$  contradicting the requirement that  $a_{\nu}(s) \leq f_{\nu}(y)$  in the definition of  $f_{\mu}$ . It follows that  $f_u(y) \le b_u(y)$  as required.

When we reach the root *r* we obtain that every  $(x, y) \in f^{-1}(1) \times f^{-1}(0), f_r(x) \ge a_r(x) > a_r(x)$  $b_r(y) \ge f_r(y)$ . This means that there is some threshold  $\tau$  such that  $f_r(x) > \tau$  if  $x \in f^{-1}(1)$ and  $f_r(y) < \tau$  if  $y \in f^{-1}(0)$ . For the final circuit we replace the output gate  $g_r$  by  $g'_r = \operatorname{th}_{\tau} \circ g_r$ where th<sub> $\tau$ </sub>(*a*) =  $\begin{cases}
1 & a \ge \tau \\
0 & a < \tau
\end{cases}$  *g*'<sub>*r*</sub> is monotone since it is the composition of monotone functions

and the function computed at the output gate is now f.

## Lifted CNF formulas with Index

Given an unsatisfiable k-CNF formula F in variables  $z_1, \ldots, z_n$ , there is a natural unsatisfiable CNF formula  $F \circ Index_m^n$  in 2nm variables, nm x variables and nm y variables. as follows: Add clauses  $x_{i1} \lor \cdots \lor x_{im}$  and  $\overline{x}_{ij} \lor \overline{x}_{ij'}$  for each  $i \neq k$ . Now replace each *k*-clause

$$(z_{i_1}^{b_1} \vee \cdots \vee z_{i_k}^{b_k})$$

where  $z_i^0 = z_i$  and  $z_i^1 = \overline{Z}_i$ , by the  $m^k$  2*k*-clauses of the form

$$(\overline{x}_{i_1j_1} \lor \cdots \lor \overline{x}_{i_kj_k} \lor y_{i_1j_1}^{b_1} \lor \cdots \lor y_{i_kj_k}^{b_k})$$

which say that for each possible setting of pointers, if the the pointers are set that way then the clause in the *y* vectors pointed to must hold.

#### Lifting DAG protocols

Garg, Göös, Kamath and Sokolov proved a powrful lifting theorem for DAG protocols that lets one go from cube DAGs to rectangle DAGs and triangle DAGs using density-restoring partitions. There is an improved and simplified version of this proof in the Lifting with Sunflowers paper.

**Theorem 15.11.** For every  $\varepsilon > 0$ , there are constants c, c' > 0 such that for  $m = n^{1+\varepsilon}$  and any relation R on  $\{0,1\}^n$  that requires cube DAG width at least w

- $R \circ Index_m^n$  requires rectangle DAG size at least  $n^{cw}$ , and
- $R \circ Index_m^n$  requires triangle DAG size at least  $n^{c'w}$ .

Corollary 15.12. If F is an unsatisfiable k-CNF formula in n variables requiring Resolution width w, then for  $m = n^{\varepsilon}$ ,  $F \circ Index_m^n$  is an unsatisfiable 2k-CNF formula requiring size  $n^{\Omega(w)}$  Cutting Planes refutations. Further  $f_{F,m}$  requires monotone real circuit size  $n^{\Omega(w)}$ .

Given the large number of unsatisfiable *k*-CNF formulas with  $\Omega(n)$  Resolution width lower bounds, this gives many lower bounds of the form  $2^{n^{\delta}}$  on Cutting Planes proof size and on the size of monotone real circuits.

A particularly simple explicit example is the following: Define the *3XOR-SAT* problem on at most  $8n^3/3$  bits as follows: The input has one bit for each of the  $16\binom{n}{3} \le 8n^3/3$  possible parity constraints of size at most 3, which defines a formula given by the constraints where there is a 1. The output is 1 iff the formula is *unsatisfiable*. This is clearly monotone since additional constraints can only make things more unsatisfiable

**Corollary 15.13.** 3XOR-SAT requires monotone (real) circuits of size at least  $2^{n^{\delta}}$ .

In the rectangle DAG case the ideas of the proof need to change by a lot since it is critical that the decision DAG being build does NOT remember the entire history and only retains the O(w) bits defining the cube associated with the current node. Rather than working with the  $\rho$ -structured rectangle at each step, the algorithm works relative to the actual rectangle  $R_{\nu} = A_{\nu} \times B_{\nu}$  of the protocol itself. It fully partitions  $R_{\nu}$  using the density-restoring ideas as before (not stopping at 1/2) but with a twist, and shows that if that rectangle is large enough  $2^{-2w\log m}$  then one can partition it rectangles defined by various  $\rho$  of size O(w) and high minentropy (but not necessarily of low deficiency) with some nice consistency properties. It would at first seem that this would add the new  $\rho$  to the existing one, but these consistency properties all one to drop a similar number of coordinates defining the rectangle at the previous step.

The whole argument is all about finding big enough good rectangles as one goes down the protocol DAG. The key to extending this to triangle DAGs is that every triangle contains a fairly large rectangle of about 1/2 the total number of points and the proof proceeds by maintaining rectangles as it navigates the protocol DAG.

**Open Problem 15.14.** *The following are open (in addition to improving m):* 

- Extend the arguments to randomized or nondeterministic DAG lifting. (This is mostly of technical interest.)
- Extend any lifting argument to 3 player communication complexity. (The number on forehead version for DAG lifting would give lower bounds on degree 2 proofs.)
- Extend lifting to DAGs where objects are defined by constraints  $(x, y) \in S$  iff a(x) > b(y)and c(x) > d(y). This would give lower bounds on proof sizes involving conjunctions of linear constraints.