

Computer-Aided Reasoning for Software

# **Model Checking I**

# CSE507

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# Today

## Last lecture

- Symbolic execution and concolic testing

## Today

- Introduction to model checking

## Reminders

- Homework 3 is due next Wednesday at 11pm

# What is model checking?

An automated technique for verifying that a concurrent finite state system satisfies a given temporal property.

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# What is model checking?

An automated technique for verifying that a concurrent finite state system satisfies a given temporal property.

A state of the system (e.g., an initial state).

A temporal logic formula (e.g., a request is *eventually* acknowledged).

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A mathematical model of the system, given as a **Kripke structure** (a finite state machine).

# Why model checking?

**Model checking**

**Classic & bounded verification**

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## Classic & bounded verification

- Deterministic, single-threaded, possibly infinite-state, terminating programs.
- Fully described by their input/output behavior.
- Semi-automatic or bounded-automatic checking of properties in expressive logics (e.g., FOL).

- Libraries and ADT implementations
- Heap-manipulating programs (e.g., OO)
- Tricky deterministic algorithms



# Why model checking?

## Model checking

- *Reactive systems*: concurrent finite-state programs with *ongoing* input/output behavior.
- *Control-intensive* but without a lot of data manipulation.
- Fully automatic checking of properties in less expressive (temporal) logics.

- Microprocessors and device drivers
- Embedded controllers (e.g., cars, planes)
- Protocols (e.g., cache coherence)

## Classic & bounded verification

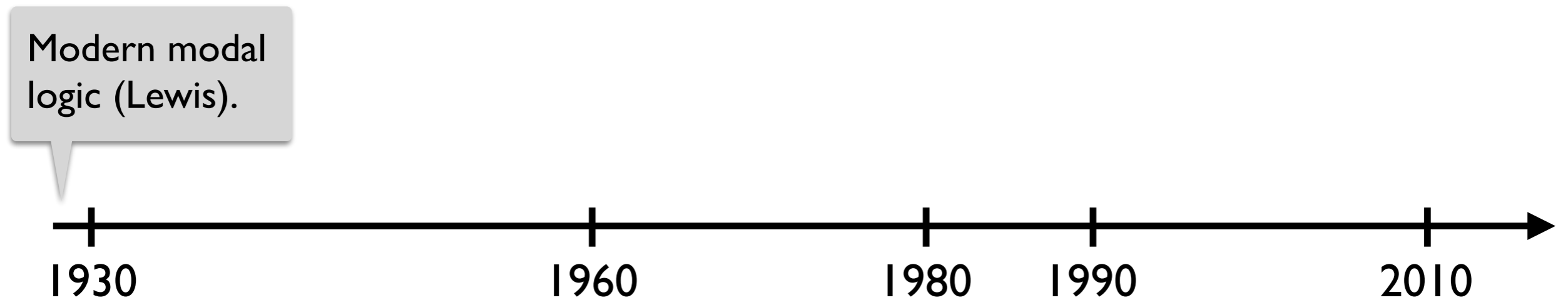
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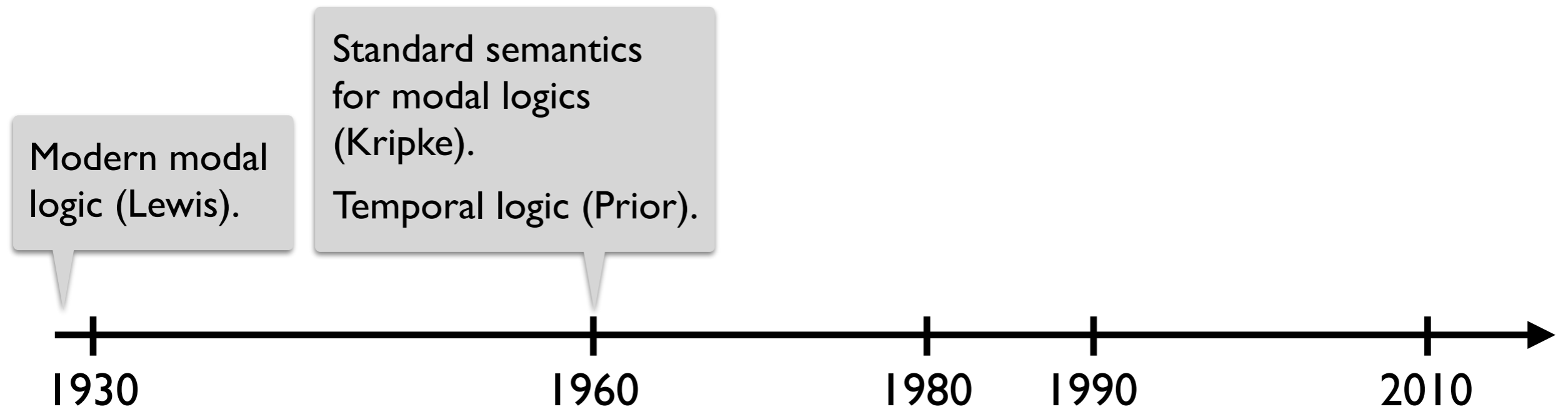
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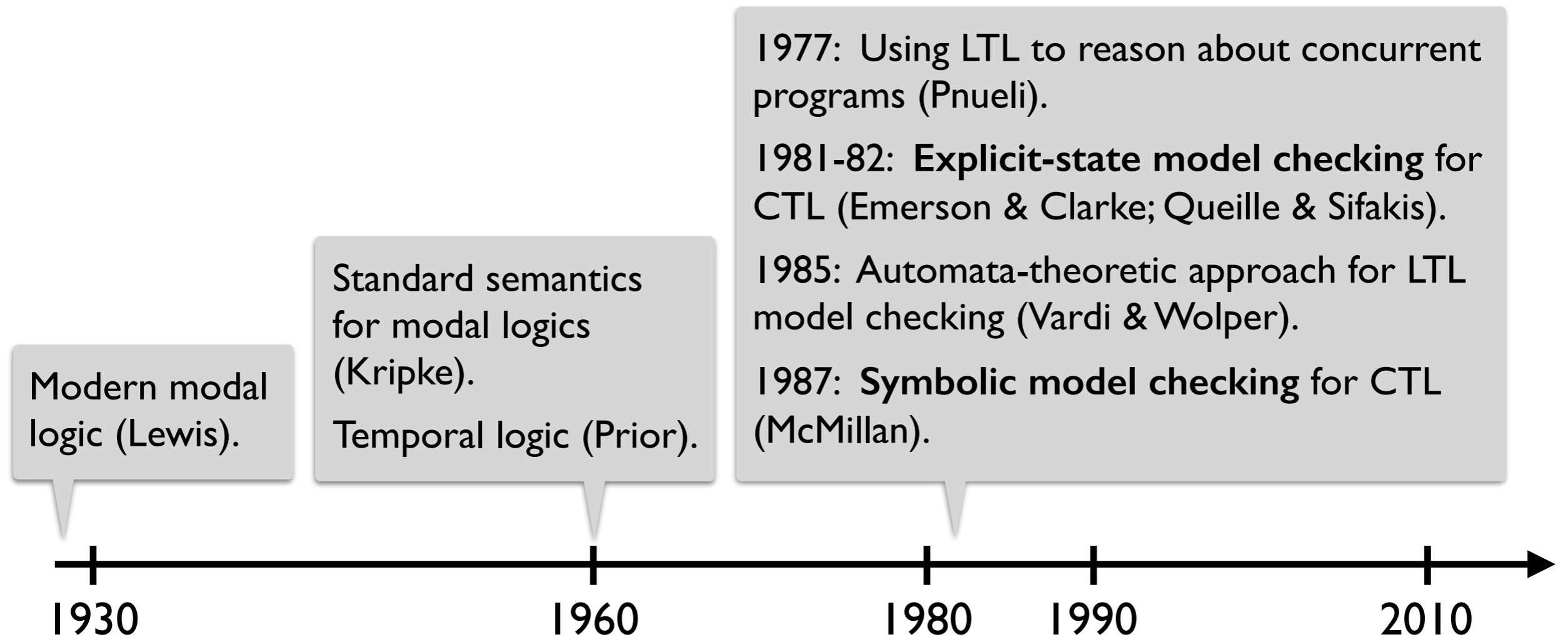
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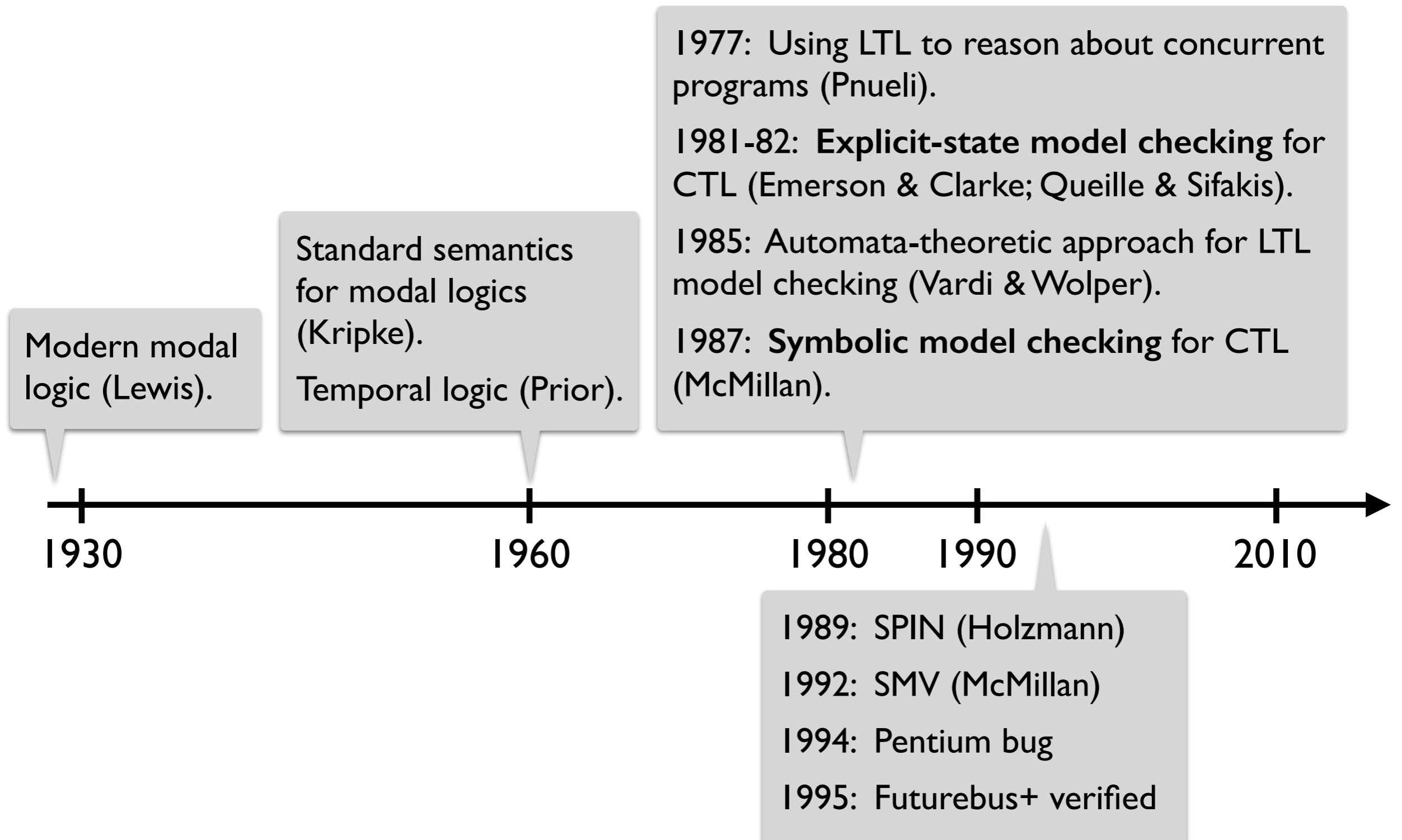
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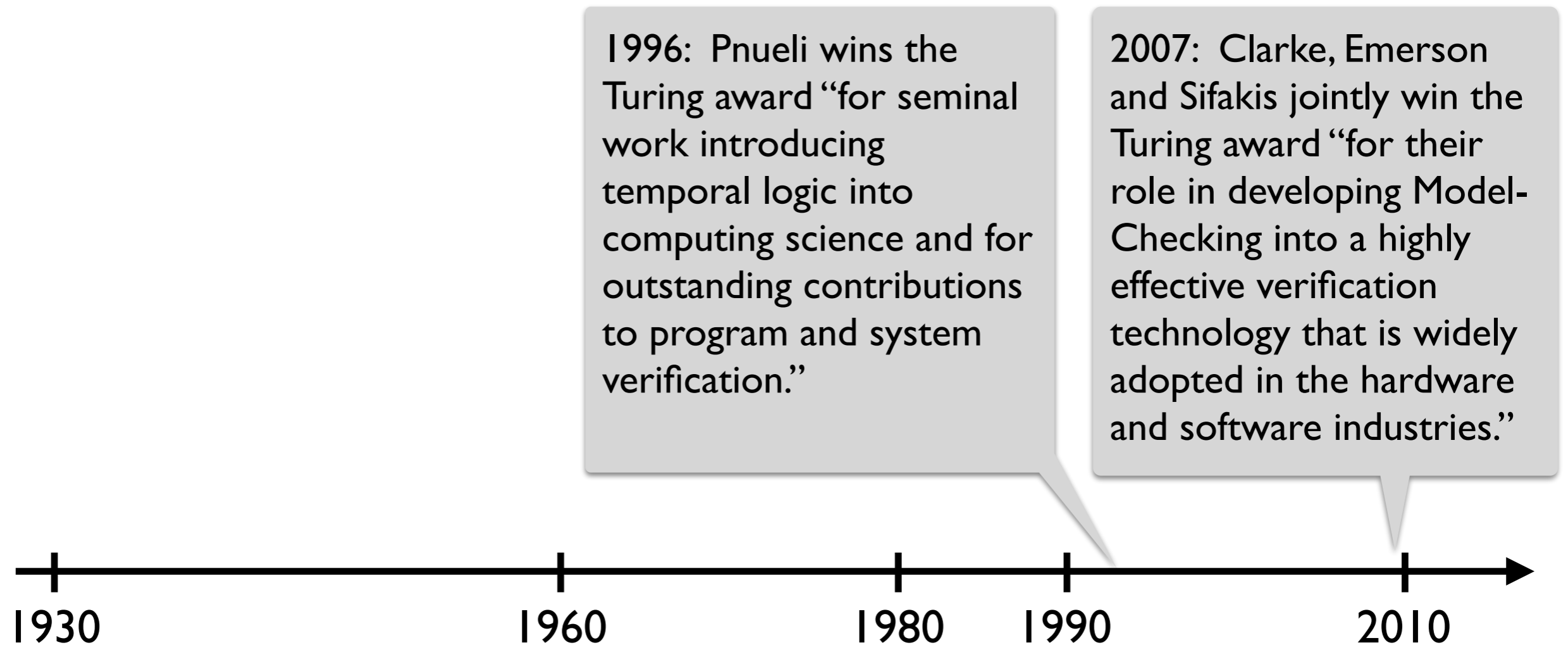
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# Kripke structures



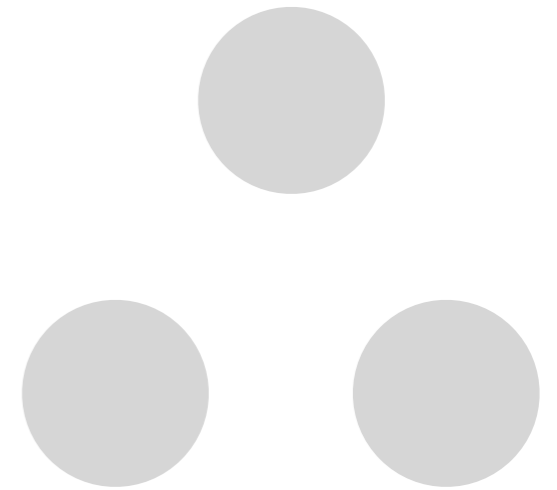
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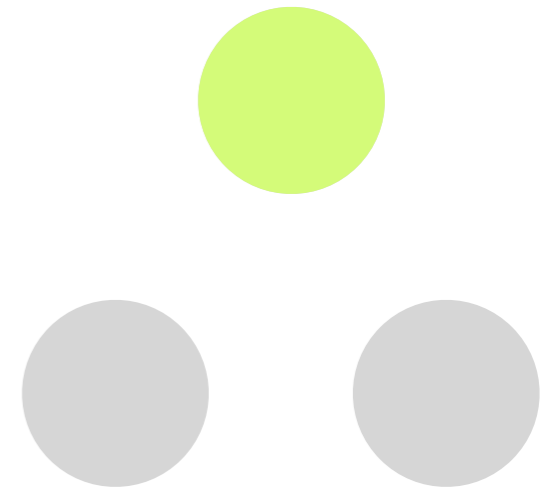
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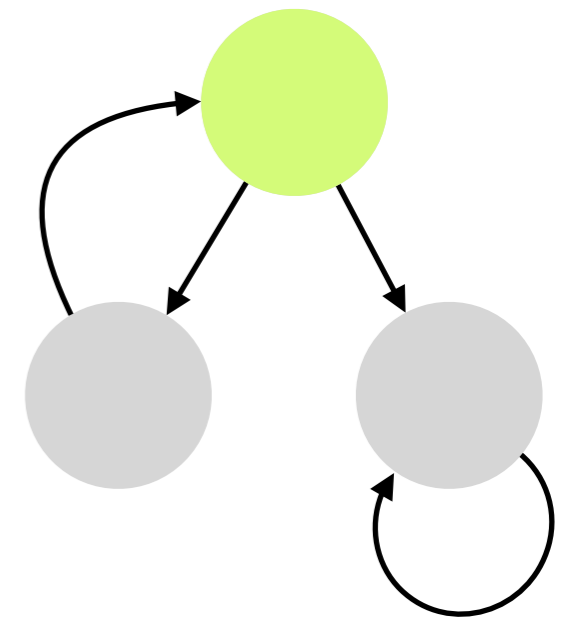
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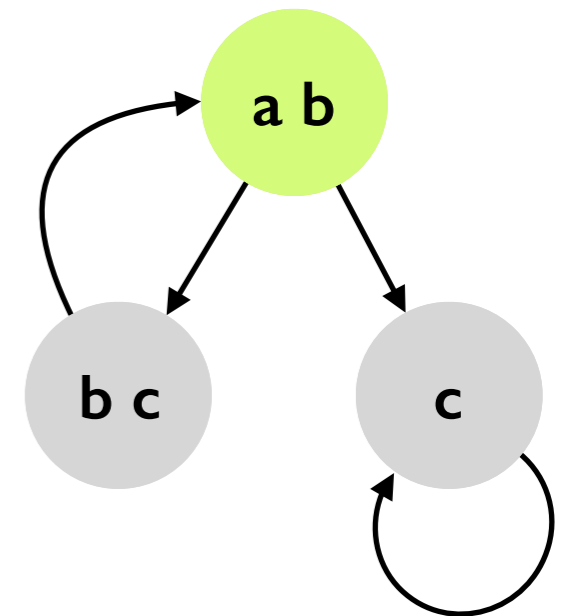
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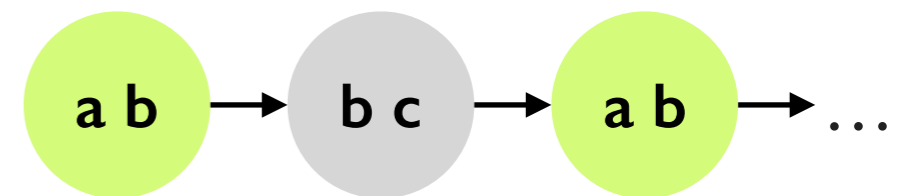
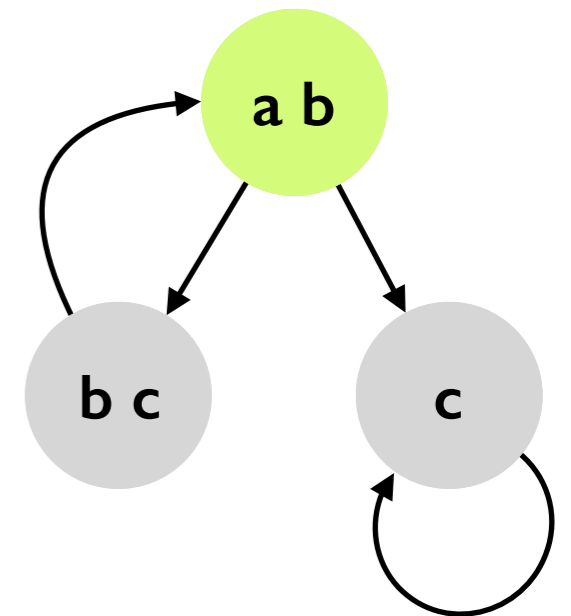


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**A *path* in  $M$  is an infinite sequence of states  $\pi = s_0 s_1 \dots$  such that for all  $i \geq 0$ ,  $(s_i, s_{i+1}) \in R$ .**



# Modeling systems with Kripke structures

```
// x=1, y=1  
x := (x + y) % 2
```

- In a finite-state program, system variables  $V$  range over a *finite domain*  $D$ :  $V = \{x, y\}$  and  $D = \{0, 1\}$ .
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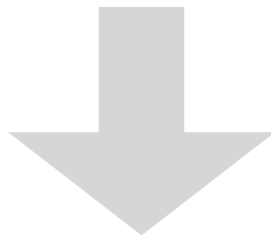
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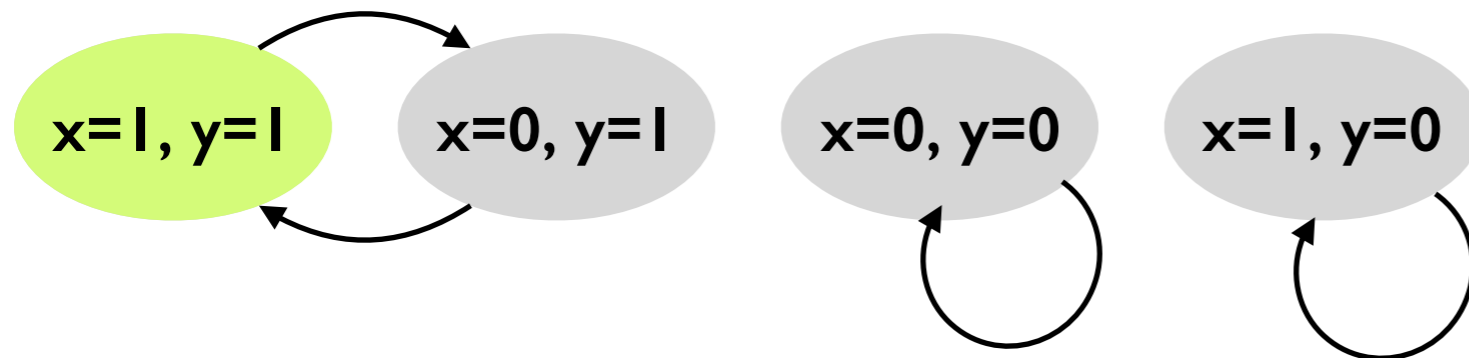
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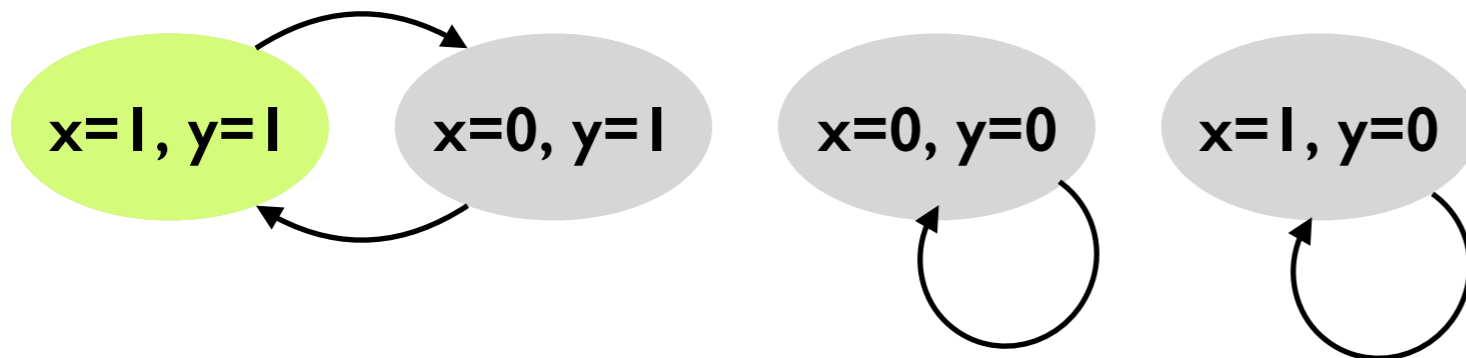


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**State explosion:** Kripke structure usually exponential in the size of the program.

# A Kripke structure for a concurrent program

**P<sub>1</sub>**

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10 while (true) {  
11   wait(turn == 0);  
    // critical section  
12   turn := 1;  
13 }
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Two processes executing concurrently and asynchronously, using the shared variable `turn` to ensure *mutual exclusion*:

They are never in the critical section at the same time.

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State of the program described by the variable `turn` and the *program counters* for the two processes.

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10, 20

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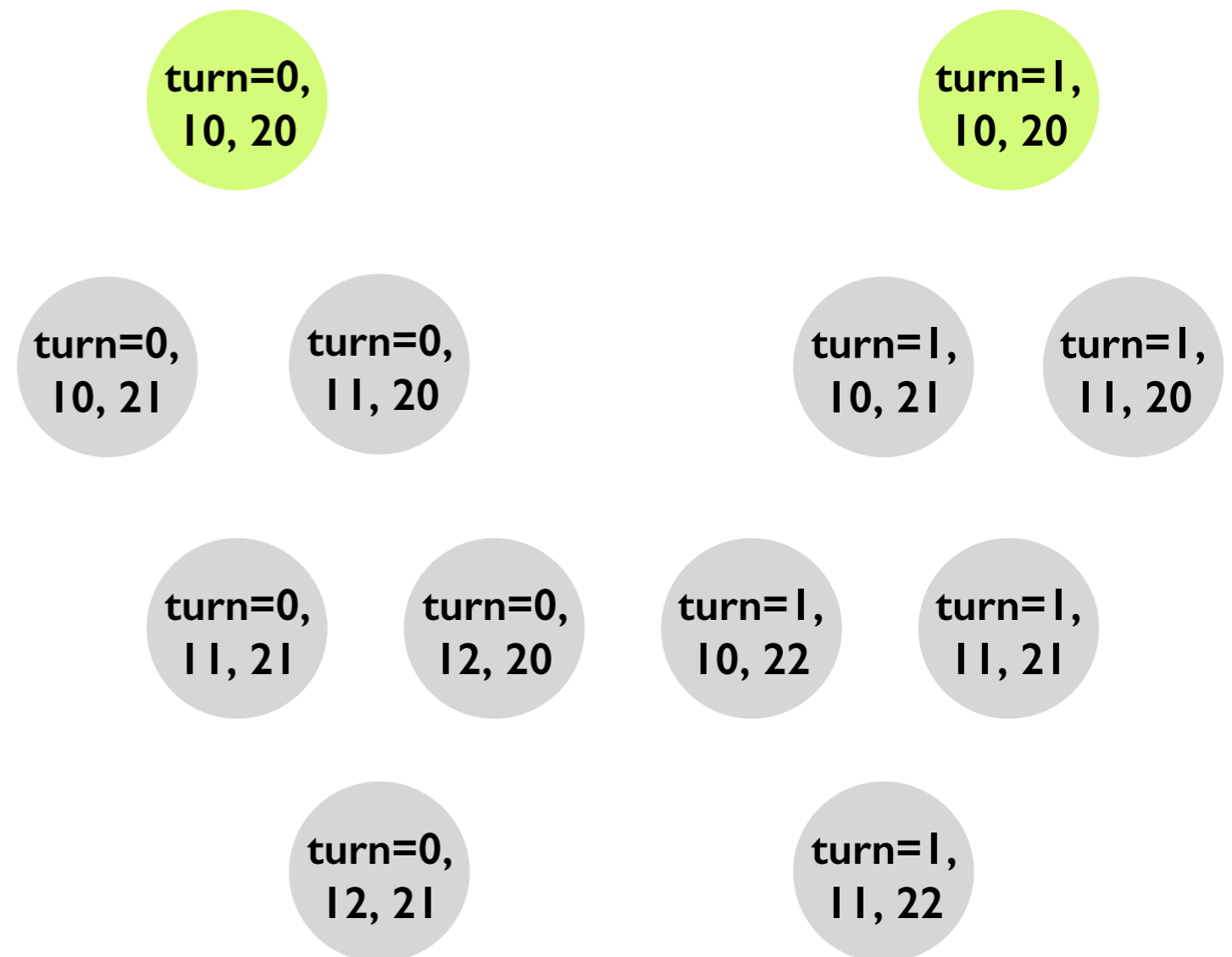
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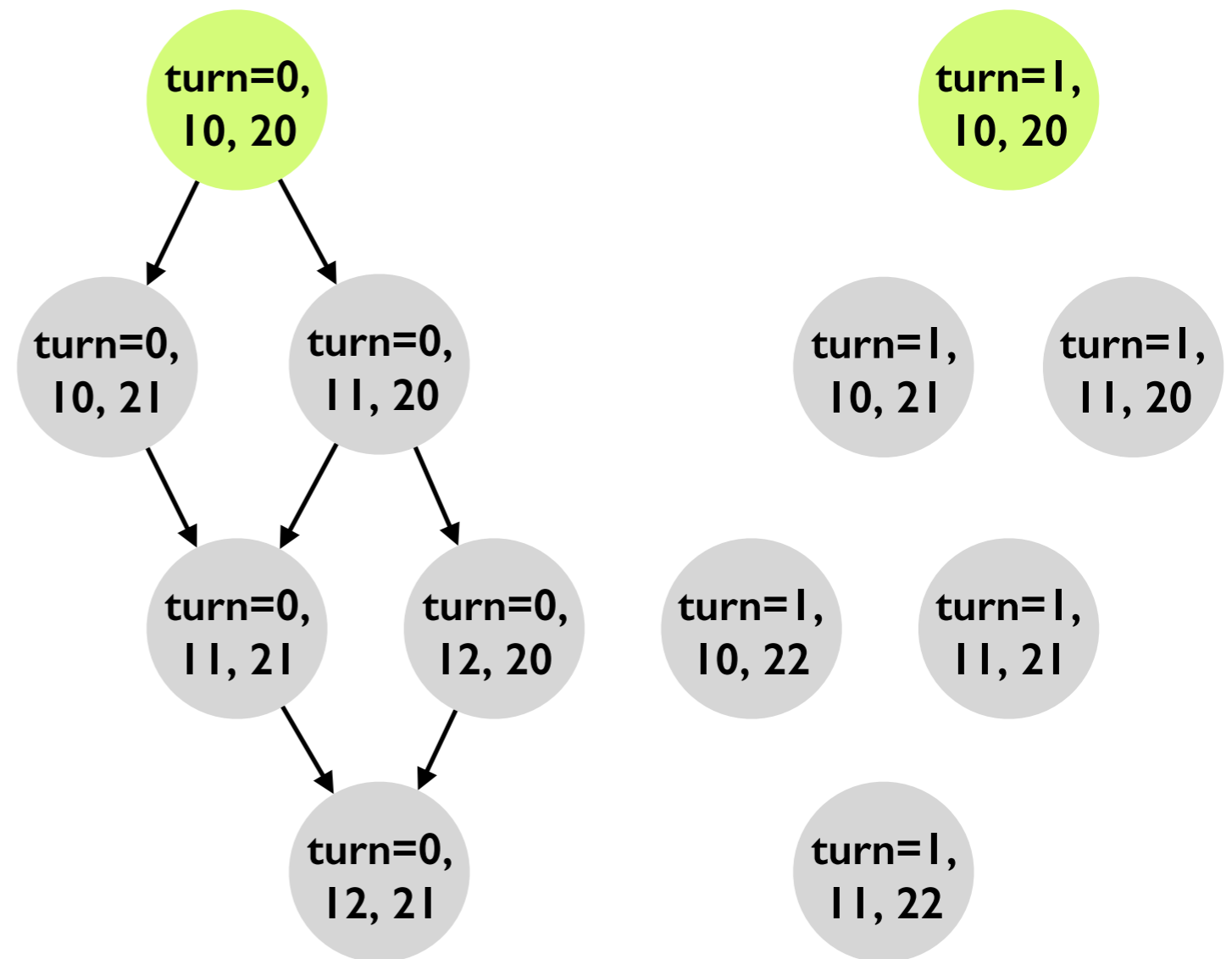
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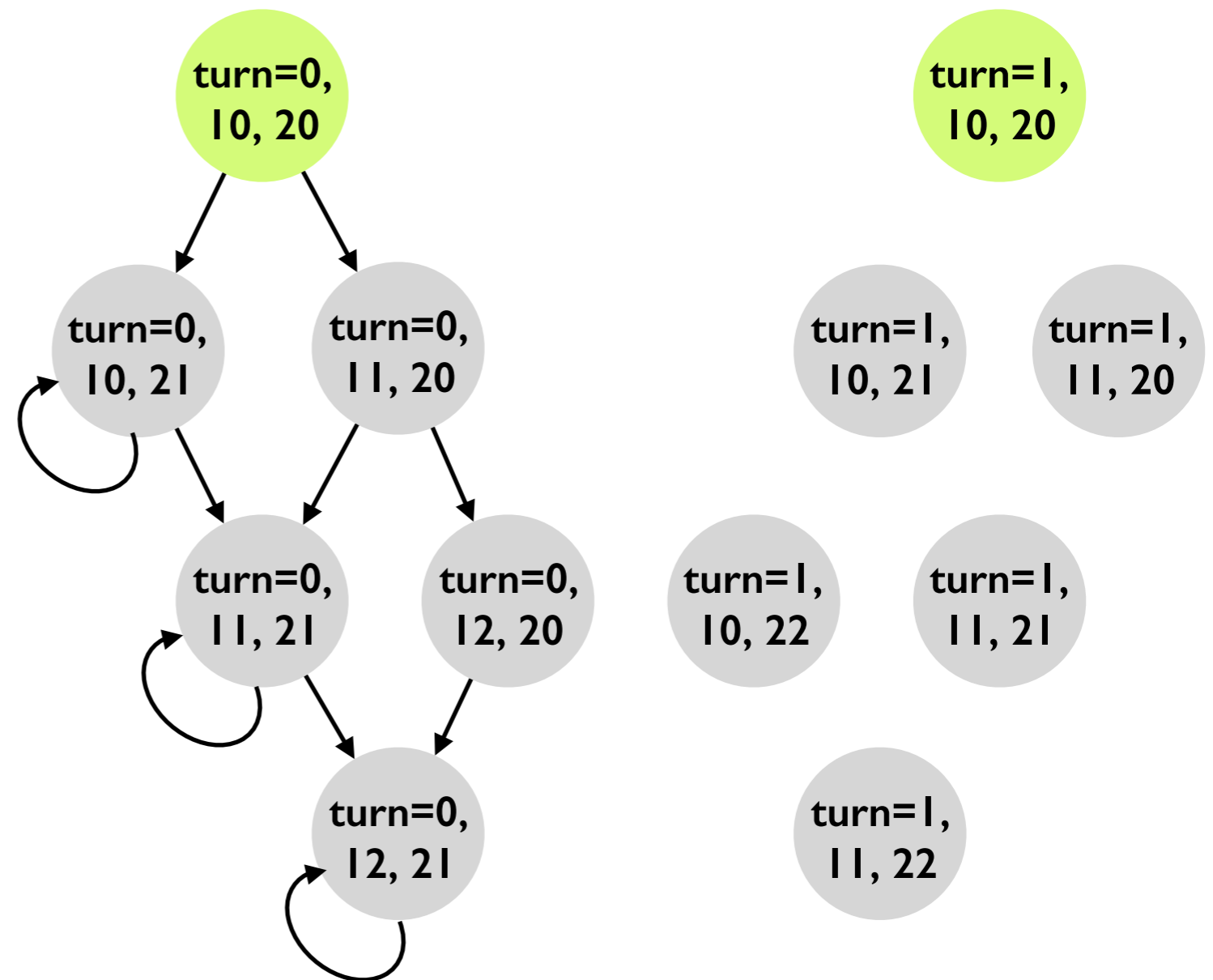
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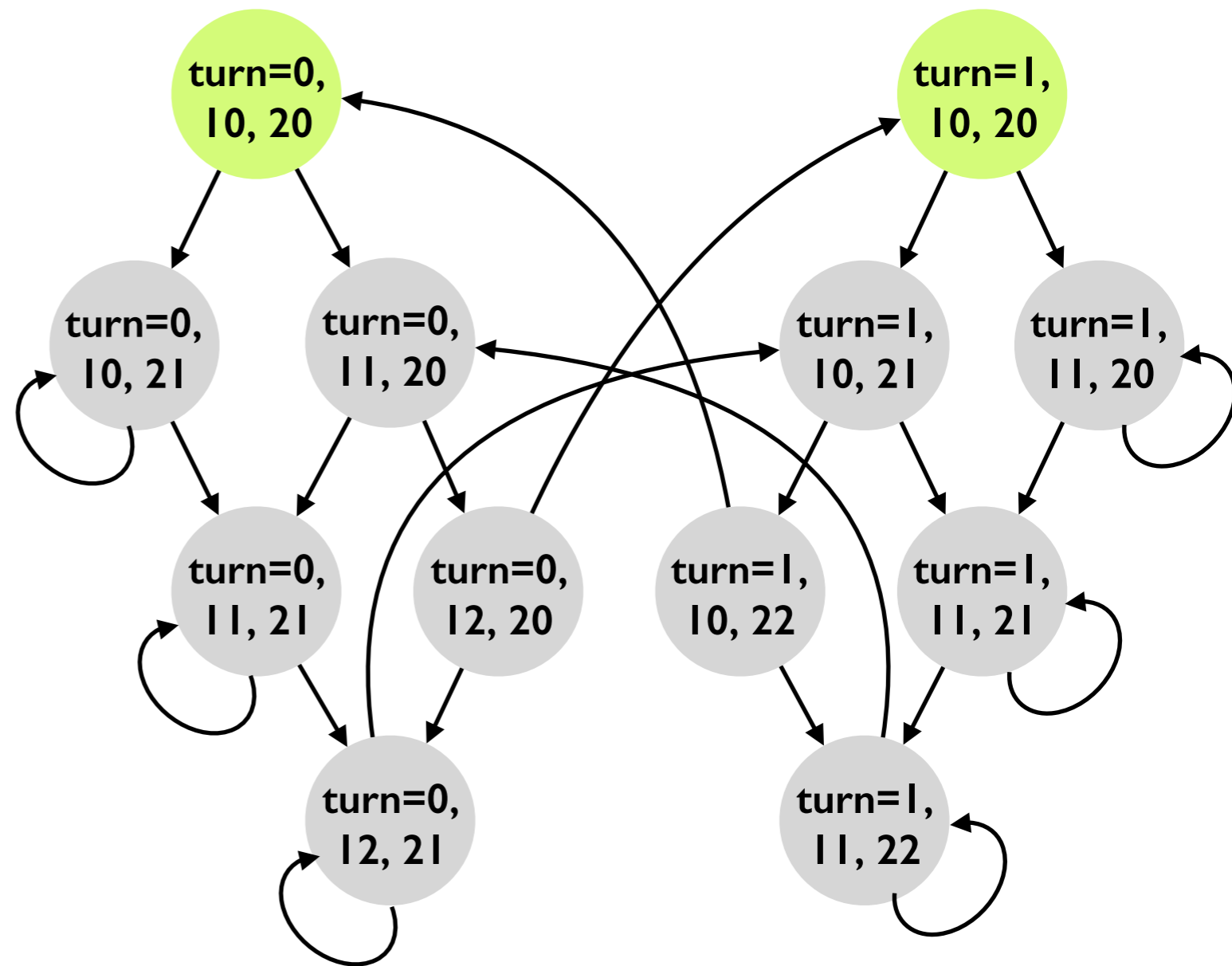
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- “Nothing bad will happen.”
- $\varphi$  is a safety property iff every infinite path  $\pi$  violating  $\varphi$  has a **finite prefix**  $\pi'$  such that every extension of  $\pi'$  violates  $\varphi$ .

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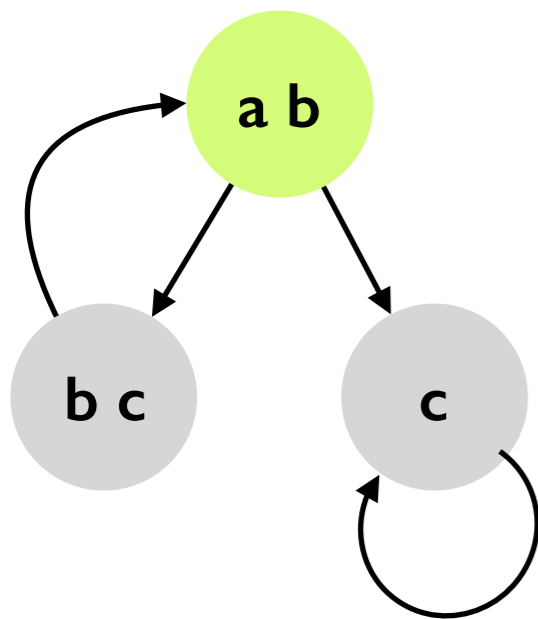
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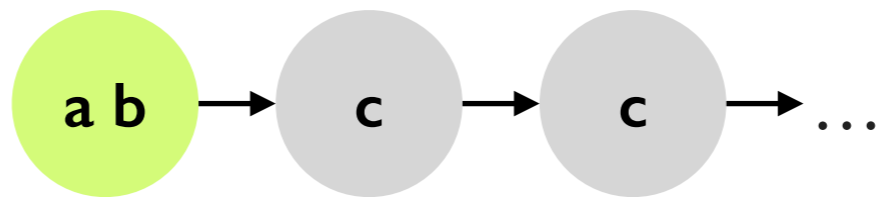
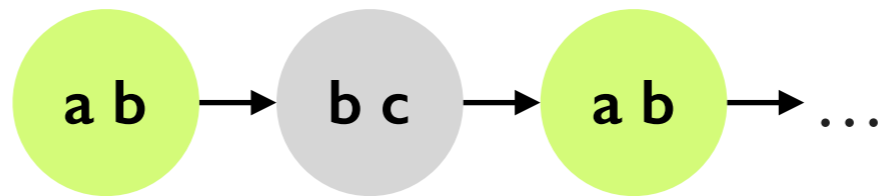
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**Starvation freedom:** whenever  $P_1$  is ready to enter its critical section, it will eventually succeed (provided that the scheduler is *fair* and does not let  $P_2$  stay in its critical section forever).

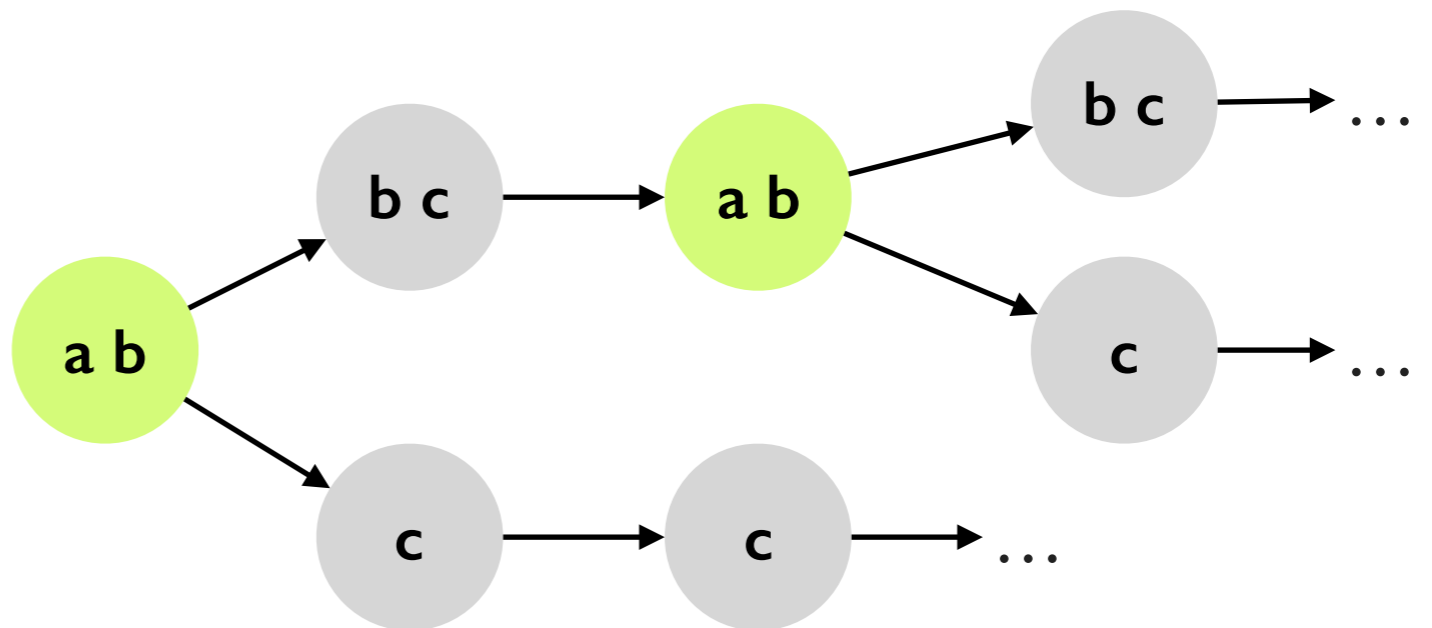
# Expressing properties in temporal logics



Linear time: properties of computation paths



Branching time: properties of computation trees



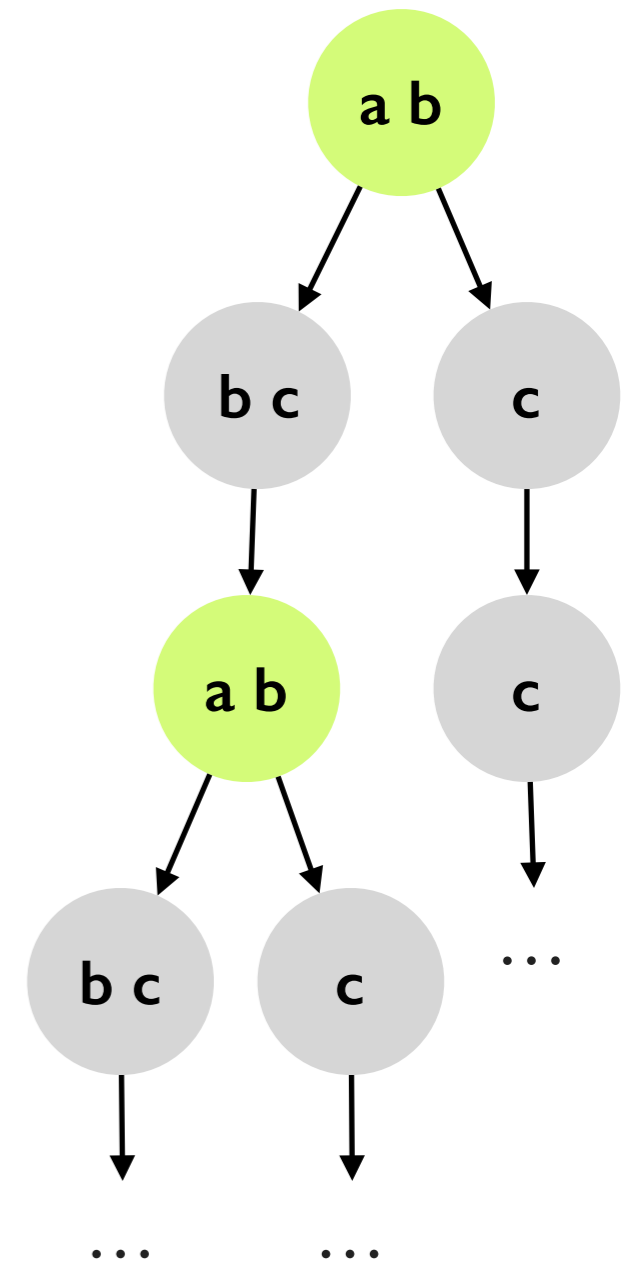
# Computation tree logic CTL\*

**Path quantifiers describe the branching structure of the computation tree:**

- **A** (for all paths)
- **E** (there exists a path)

**Temporal operators describe properties of a path through a tree:**

- **Xp** (p holds “next time”)
- **Fp** (p holds “eventually” or “in the future”)
- **Gp** (p holds “always” or “globally”)
- **p U q** (p holds “until” q holds)



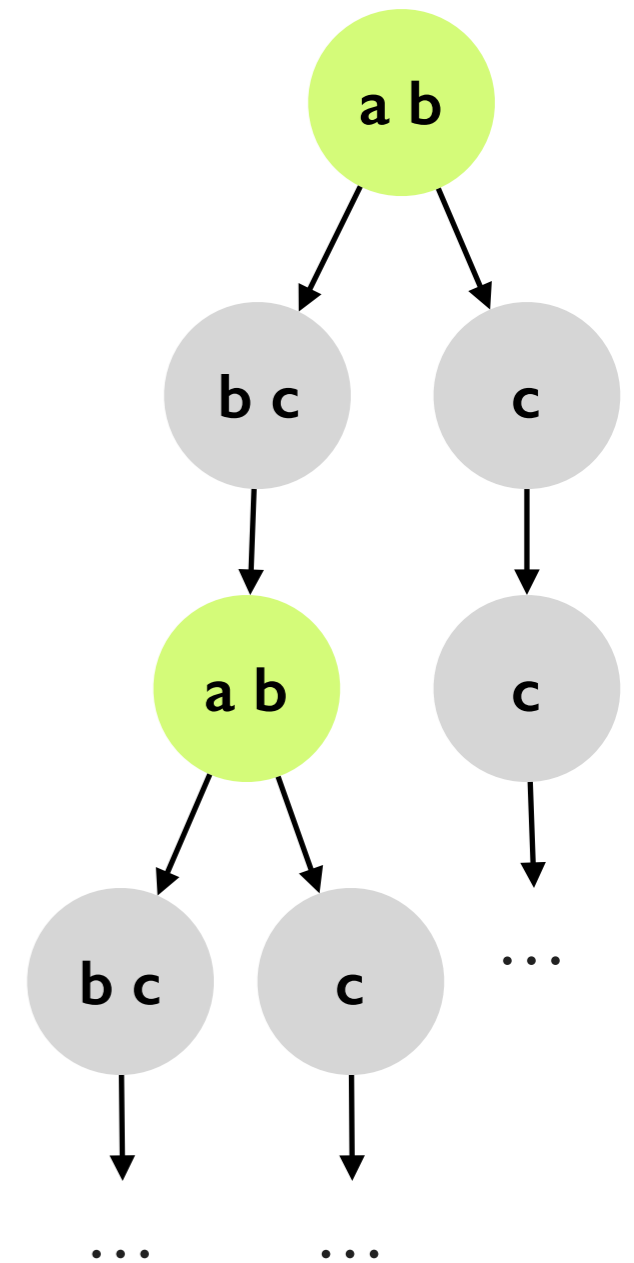
# Syntax of CTL\*

## State formulas

- Atomic propositions:  $a \in AP$
- $\neg f, f \wedge g, f \vee g$ , where  $f$  and  $g$  are state formulas
- $A_p$  and  $E_p$ , where  $p$  is a path formula

## Path formulas

- $f$ , where  $f$  is a state formula
- $\neg p, p \wedge q, p \vee q$ , where  $p$  and  $q$  are path formulas
- $Xp, Fp, Gp, p U q$ , where  $p$  and  $q$  are path formulas



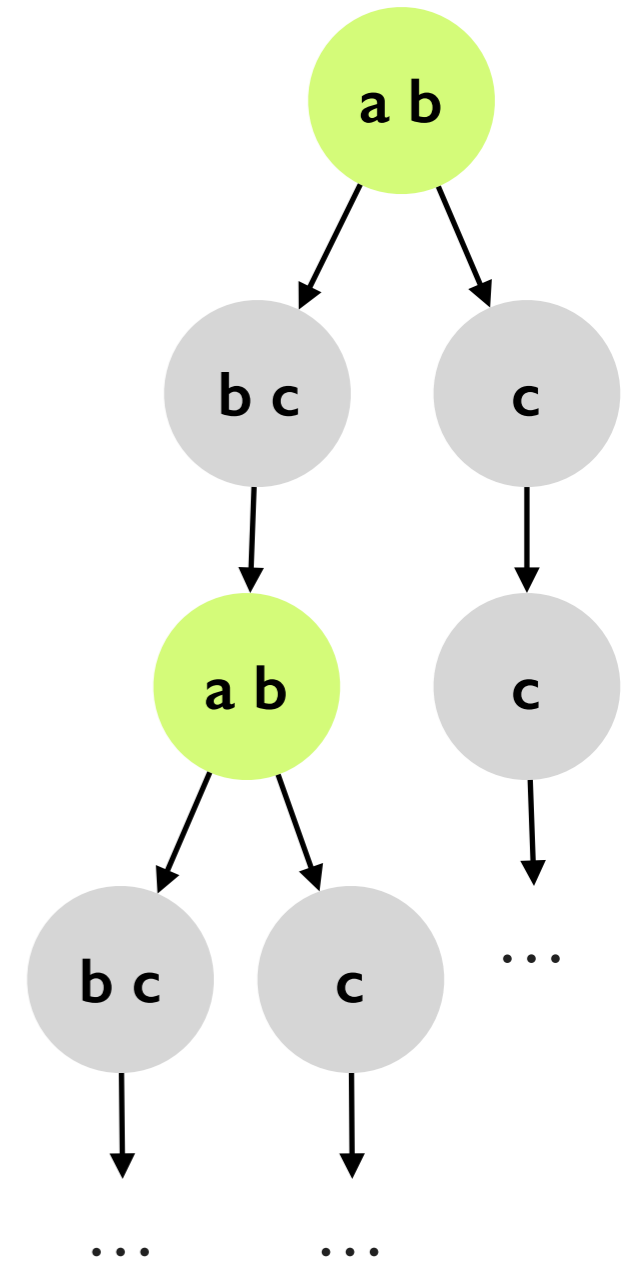
# Semantics of CTL\*

## State formulas

- $M, s \models f$  iff  $f \in L(s)$
- $M, s \models \mathbf{A}p$  iff  $M, \pi \models p$  for all paths  $\pi$  that start at  $s$
- $M, s \models \mathbf{E}p$  iff  $M, \pi \models p$  for some path  $\pi$  that starts at  $s$

## Path formulas ( $\pi^k$ is suffix of $\pi$ starting at $s_k$ )

- $M, \pi \models f$  iff  $M, s \models f$  and  $s$  is the first state of  $\pi$
- $M, \pi \models \mathbf{X}p$  iff  $M, \pi^1 \models p$
- $M, \pi \models \mathbf{F}p$  iff  $M, \pi^k \models p$  for some  $k \geq 0$
- $M, \pi \models \mathbf{G}p$  iff  $M, \pi^k \models p$  for all  $k \geq 0$
- $M, \pi \models p \mathbf{U} q$  iff  $M, \pi^k \models q$  and  $M, \pi^j \models p$  for some  $k \geq 0$  and for all  $0 \leq j < k$



# CTL and Linear Temporal Logic (LTL)

**Computation Tree Logic (CTL)**

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## Computation Tree Logic (CTL)

- Fragment of CTL\* in which each temporal operator is prefixed with a path quantifier.
- **AG(EF p)**: From any state, it is possible to get to a state where p holds.

## Linear Temporal Logic (LTL)



# CTL and Linear Temporal Logic (LTL)

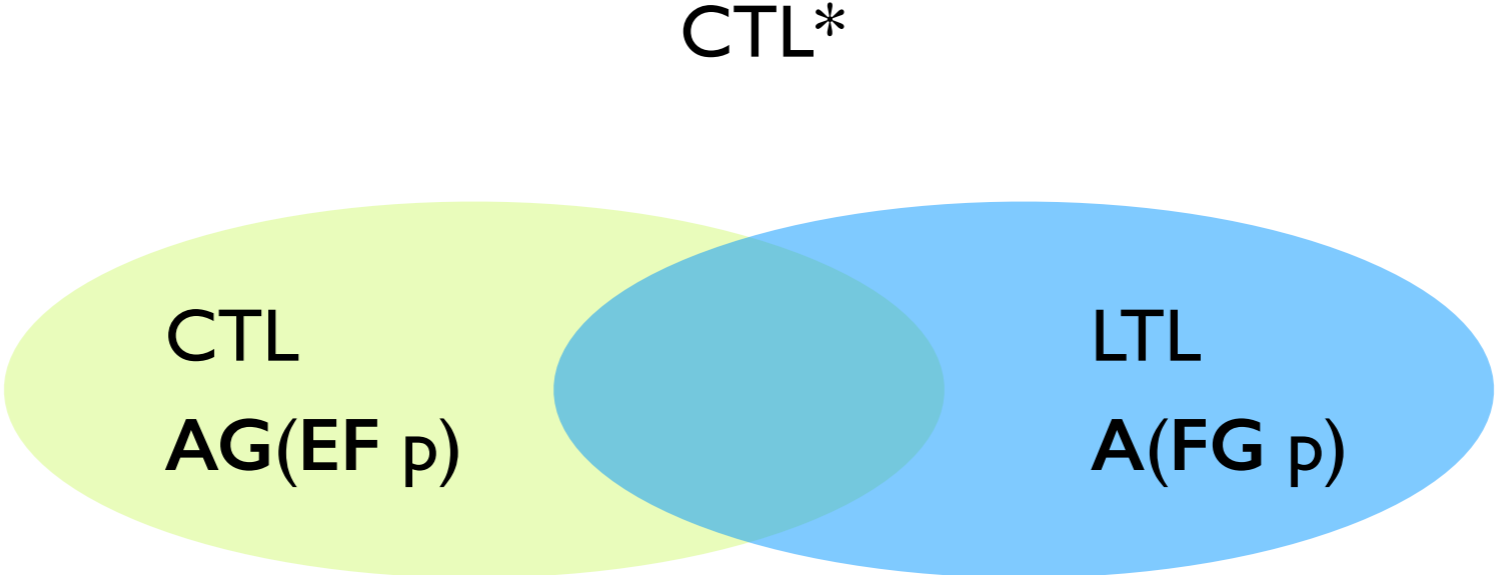
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## Linear Temporal Logic (LTL)

- Fragment of CTL\* with formulas of the form **A**p, where p contains no path quantifiers.
- **A(FG p)**: Along every path, there is some state from which p will hold forever.

# Expressive power of CTL, LTL, and CTL\*



# Fairness

**Cannot be expressed in CTL**

**Can be expressed in LTL**

# Fairness

## Cannot be expressed in CTL

- Handled by changing the semantics to use fair Kripke structures.
- A *fair* Kripke structure  $M = \langle S, S_0, R, L, F \rangle$  includes an additional set of sets of states  $F \subseteq 2^S$ .
- For each  $P \in F$ , a *fair path*  $\pi$  includes some states from  $P$  infinitely often.
- Path quantifiers interpreted only with respect to fair paths.

## Can be expressed in LTL

# Fairness

## Cannot be expressed in CTL

- Handled by changing the semantics to use fair Kripke structures.
- A *fair* Kripke structure  $M = \langle S, S_0, R, L, F \rangle$  includes an additional set of sets of states  $F \subseteq 2^S$ .
- For each  $P \in F$ , a *fair path*  $\pi$  includes some states from  $P$  infinitely often.
- Path quantifiers interpreted only with respect to fair paths.

## Can be expressed in LTL

- Absolute fairness:  $A(\mathbf{GF} p_{\text{exec}})$
- Strong fairness:  
 $A((\mathbf{GF} p_{\text{ready}}) \Rightarrow (\mathbf{GF} p_{\text{ready}} \wedge p_{\text{exec}}))$
- Weak fairness:  
 $A((\mathbf{FG} p_{\text{ready}}) \Rightarrow (\mathbf{GF} p_{\text{ready}} \wedge p_{\text{exec}}))$

# Model checking complexity for CTL, LTL, CTL\*

## Polynomial Time for CTL

- Best known algorithm:  $O(|M| * |f|)$

## PSPACE-complete for LTL

- Best known algorithm:  $O(|M| * 2^{|f|})$

## PSPACE-complete for CTL\*

- Best known algorithm:  $O(|M| * 2^{|f|})$



$M, s \models f$

# Model checking techniques for CTL and LTL

## CTL

- Graph-theoretic explicit-state model checking (EMC)
- Symbolic model checking with Ordered Binary Decision Diagrams (SMV, NuSMV)
- Bounded model checking based on SAT (NuSMV)

## LTL

- Automata-theoretic model checking:
  - Explicit-state (SPIN) or
  - Symbolic (NuSMV)

# Summary

## Today

- Basics of model checking:
  - Kripke structures
  - Temporal logics (CTL, LTL, CTL\*)
  - Model checking techniques

## Next lecture

- Software model checking