CSE 501: Compiler Construction (Winter '96)

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- office hours: TuTh 11:00-12:00 (Sieg 309)

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Goals:

- Understand how languages get implemented efficiently
- Understand what can and can't be done, with how much effort
- Understand state of current research in efficient language implementation

Prerequisites:

- CSE 401 or equivalent
- CSE 505 or equivalent

Readings:

- Dragon book
- important papers from the literature

Grading:

- Midterm: 25%
- Final: 25%
- Homework: 20%
- Project: 30%

Course Outline

Week	Topic
1/1	***Holiday*** Intro; structure of compilers; standard optimizations Intermediate representations: ASTs, CFGs, BBs, DAGs, RTLs Control flow, data flow; dependence Iterative global data flow analysis Reaching definitions, reaching constants
1/8	Common subexpression elimination Lattice-theoretic data flow analysis framework Integer range analysis Data flow analysis generators
1/15	***Holiday*** Advanced intermediate representations: def/use chains; SSA form; VDG form Loop-invariant code motion
1/22	Alias and pointer analysis Inlining Interprocedural analysis
1/29	Partial evaluation; procedure specialization Dependence analysis; automatic parallelization
2/5	Global register allocation
2/12	Calling conventions; callee-save vs. caller-save Link-time register allocation Instruction scheduling
2/19	***Holiday*** Garbage collection
2/26	Implementing functional languages
3/5	Implementing object-oriented languages

The Project

Description

As part of this class you will complete an implementation project as part of a team. The main goal of the course project is to reinforce the lectures and readings with practical experience designing, implementing, and assessing compiler analyses and optimizations.

You will form your own teams of 2-4 people and each team will negotiate a suitable course project with me. Each project should include some sort of analysis component (something the compiler needs to extract from the program) and a transformation component (an improvement the compiler makes to the program based on the extracted information). Each project will include a written report describing the analyses performed, the transformations made, the generality of the optimizations, and the bottom-line improvement in program performance made by the optimizations. The CSE 501 project can be joint with the CSE 548 course project, if appropriate. Some sample projects are listed on the next page.

In order to focus on the interesting aspects of optimizations and minimize less interesting grunt work, the project will be done in the context of the Vortex optimizing compiler, written in Cecil. [Cecil is an object-oriented language we've developed here at UW. We will give tutorials on Cecil and the Vortex infrastructure during the first few weeks of the quarter.] The Vortex compiler includes a lot of support for performing optimizations, including an intermediate representation, a framework for iterative data flow analysis, and several optimizations such as constant propagation, common subexpression elimination, dead assignment elimination, and global regular allocation, and back-ends generating C and SPARC assembly code. Vortex compiles Cecil, C/C++, and Modula-3 (mostly); the project can focus on any or all of these languages, although the representation of and issues in compiling higher-level languages like Cecil are more complicated than those in compiling C/C++.

By leveraging off of the Vortex infrastructure, I hope that your work can focus on your analyses and optimizations, leading to a higher-quality result and better learning with less time cost. I also hope that you will see good examples of how to structure compilers so that they are extensible. Note, however, that the Vortex infrastructure is a research product still under development, and as such is likely to have bugs and some amount of ugly code. Please bear with us and help us to improve the infrastructure.

Time Schedule

Over the course of January, you should have formed project teams and negotiated with me about the details of the project. By **February 5**, at the start of class, you should turn in a formal project proposal, which is essentially a detailed outline of your final report, including a description of your analysis & transformations and a plan for what measurements you expect to take. By **February 26**, you should turn in another draft of this report, including preliminary results, and make your current implementation available to us. The final version of the project report and your implementation is due **March 4**. The final report can be no longer than 5 pages per team member.

Some Sample Projects

- Design and implement some standard optimization(s), such as loop-invariant code motion, induction variable elimination, or partial redundancy elimination.
- Develop a framework for interprocedural analyses analogous to the existing intraprocedural iterative data flow analysis framework.
- Design and implement some interprocedural optimization, such as interprocedural constant propagation. This may involve designing and implementing a call graph data structure, or it may not, depending on what the Vortex infrastructure looks like at the time the project begins.
- Implement an alternative global (or interprocedural) register allocation algorithm, and compare it to the one already implemented. This might lead to a cool quals project.
- Implement an effective instruction scheduler, for some modern pipelined architecture(s). Again, a comparative study of scheduling algorithms might make a cool paper.
- Design and implement some sort of pointer analysis and one or more optimizations using the results of the analysis.
- Implement support for dynamic compilation. E.g., the compiler reasons about variables whose values will be constant at run-time, performs optimizations under the assumption that the variable is an (unknown) constant, and generates simple directives to patch in the real constants at run-time. Some papers have been written describing the design of this algorithm.
- Adapt Jeff Dean's inlining database to support effective automatic inlining decisions.
- Design and implement a cooler intermediate representation, such as SSA form, sparse data flow evaluation graphs, or VDGs, and use it to do some simple optimization(s).