CSE 471 Winter 2000

## Computer Design and Organization

## Problem Set #1

Due: Wednesday January 12

- 1. By the end of the first week, you should have (re)read thoroughly Chapter 6.
- 2. Read the on-line manual for Verilog
- 3. Exercise 6.5 in H&P
- 4. Exercise 6.10 in H&P