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00 QUADRUPLE 2-INPUT POSITIVE-NAND GATES
positive logic:
\[ Y = A \cdot B \]

01 QUADRUPLE 2-INPUT POSITIVE-NOR GATES
positive logic:
\[ Y = A \cdot \overline{B} \]

02 QUADRUPLE 2-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTS
positive logic:
\[ Y = A \cdot B \]

03 QUADRUPLE 2-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTS
positive logic:
\[ Y = A \cdot \overline{B} \]

04 HEX INVERTERS
positive logic:
\[ Y = \overline{A} \]

05 HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS
positive logic:
\[ Y = \overline{A} \]

06 HEX INVERTER BUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS
positive logic:
\[ Y = \overline{A} \]
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07  HEX BUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS
positive logic:
Y = A

08  QUADRUPLE 2-INPUT POSITIVE-AND GATES
positive logic:
Y = A • B

09  QUADRUPLE 3-INPUT POSITIVE-AND GATES
with open-collector outputs
positive logic:
Y = A • B

10  TRIPLE 3-INPUT POSITIVE-NAND GATES
positive logic:
Y = A • B • C

11  TRIPLE 3-INPUT POSITIVE-AND GATES
positive logic:
Y = A • B • C

12  QUADRUPLE 2-INPUT POSITIVE-AND GATES
positive logic:
Y = A • B

13  TRIPLE 3-INPUT POSITIVE-AND GATES
positive logic:
Y = A • B • C

14  HEX SCHMITT-TRIGGER INVERTERS
positive logic:
Y = A

15  HEX INVERTER BUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS
positive logic:
Y = A

16  HEX BUFFER/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS
positive logic:
Y = A

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19. HEX SCHMITT-TRIGGER INVERTERS
   positive logic:
   \[ Y = \overline{A} \]
   
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20. DUAL 4-INPUT POSITIVE-NAND GATES
   positive logic:
   \[ Y = \overline{A \cdot B \cdot C \cdot D} \]
   
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21. DUAL 4-INPUT POSITIVE-AND GATES
   positive logic:
   \[ Y = A \cdot B \cdot C \cdot D \]
   
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25. DUAL 4-INPUT POSITIVE-NOR GATES WITH STROBE
   positive logic:
   \[ Y = \overline{A + B + C + D} \]
   
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26. QUADRUPLE 2-INPUT HIGH-VOLTAGE INTERFACE POSITIVE-NAND GATES
   positive logic:
   \[ Y = \overline{A \cdot B} \]
   
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27. TRIPLE 3-INPUT POSITIVE-NOR GATES
   positive logic:
   \[ Y = A + B + C \]
   
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30. 8-INPUT POSITIVE-NAND GATES
   positive logic:
   \[ Y = A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H \]
   
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32 QUADRUPLE 2-INPUT POSITIVE OR GATES
positive logic:
Y = A + B

33 QUADRUPLE 2-INPUT POSITIVE-NOR BUFFERS
WITH OPEN-COLLECTOR OUTPUTS
positive logic:
Y = \overline{A + B}

35 HEX NONINVERTERS
WITH OPEN-COLLECTOR OUTPUTS
positive logic:
Y = A

37 QUADRUPLE 2-INPUT POSITIVE-NAND BUFFERS
WITH OPEN-COLLECTOR OUTPUTS
positive logic:
Y = \overline{A \cdot B}

38 QUADRUPLE 2-INPUT POSITIVE-NAND BUFFERS
positive logic:
Y = \overline{A \cdot B}

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74
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85 4-BIT MAGNITUDE COMPARATORS

93 4-BIT BINARY COUNTERS

86 QUADRUPLE 2-INPUT EXCLUSIVE-OR GATES
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97 SYNCHRONOUS 6-BIT BINARY RATE MULTIPLIER

90 DECADE COUNTER

107 DUAL J-K FLIP-FLOPS WITH CLEAR

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109 DUAL J-K POSITIVE-EDGE-TRIGGERED
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positive logic:
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265 QUAD COMPLEMENTARY-OUTPUT ELEMENTS
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266 QUAD 2-INPUT EXCLUSIVE-NOR GATES WITH OPEN-COLLECTOR OUTPUTS
positive logic:
\[ Y = A \oplus B \]

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