Modulation and Demodulation
Channel sharing

- Suppose we have TWO CARRIERS that are orthogonal to one another... then we can separate the effects of these two carriers...

- Whoa....
Vectors and modulation

S’pose \( \mathbf{m} \) and \( \mathbf{n} \) are orthogonal unit vectors. Then inner products (dot products) are
\[
\langle \mathbf{m}, \mathbf{m} \rangle = 1 \quad \langle \mathbf{n}, \mathbf{n} \rangle = 1 \\
\langle \mathbf{m}, \mathbf{n} \rangle = \langle \mathbf{n}, \mathbf{m} \rangle = 0
\]

Can interpret inner product as projection of vector 1 ("\( \mathbf{v}_1 \)"") onto vector 2 ("\( \mathbf{v}_2 \)"")...in other words, inner product of \( \mathbf{v}_1 \) and \( \mathbf{v}_2 \) tells us “how much of vector 1 is there in the direction of vector 2.”

If a channel lets me send 2 orthogonal vectors through it, then I can send two independent messages. Say I need to send two numbers, \( a \) and \( b \)...I can send \( a\mathbf{m}+b\mathbf{n} \) through the channel.
At the receive side I get \( a\mathbf{m}+b\mathbf{n} \)
Now I project onto \( \mathbf{m} \) and onto \( \mathbf{n} \) to get back the numbers:
\[
\langle a\mathbf{m}+b\mathbf{n}, \mathbf{m} \rangle = \langle a\mathbf{m}, \mathbf{m} \rangle + \langle b\mathbf{n}, \mathbf{m} \rangle = a + 0 = a \\
\langle a\mathbf{m}+b\mathbf{n}, \mathbf{n} \rangle = \langle a\mathbf{m}, \mathbf{n} \rangle + \langle b\mathbf{n}, \mathbf{n} \rangle = 0 + b = b
\]
The initial multiplication is modulation; the projection to separate the signals is demodulation. Each channel sharing scheme \( \leftrightarrow \) a set of basis vectors.
In single-channel e-field sensing, the “carrier” we transmit is \( \mathbf{m} \), the sensed value is \( a \), and the noise is \( \mathbf{n} \)
We can measure multiple sense channels simultaneously, sharing 1 RCV electrode, amp, and ADC!

Choice of TX wave forms determines multiplexing method:
- TDMA --- Time division: TXs take turns
- FDMA --- Frequency division: TXs use different frequencies
- CDMA ---- Code division: TXs use different coded waveforms

In all cases, what makes it work is ~orthogonality of the TX waveforms!
Single channel sensing / communication

\[ acc = \langle C, ADC \rangle \]

Where \( C \) is the carrier vector and \( ADC \) is the vector of samples. Let’s write out \( ADC \):

\[ ADC = hC \]

Where \( h \) (hand) is sensed value and \( hC \) means scalar \( h \times \) vector \( C \)

\[ Acc \]
\[ = \langle C, hC \rangle \]
\[ = h \langle C, C \rangle \]
\[ = h \]
\[ if \langle C, C \rangle = 1 \]
Multi-access sensing / communication

Suppose we have two carriers, $C_1$ and $C_2$
And suppose they are orthogonal, so that $\langle C_1, C_2 \rangle = 0$
The received signal is

$$ADC = h_1 C_1 + h_2 C_2$$

Let’s demodulate with $C_1$:

$$\text{acc} = \langle C_1, ADC \rangle$$
$$= \langle C_1, h_1 C_1 + h_2 C_2 \rangle$$
$$= \langle C_1, h_1 C_1 \rangle + \langle C_1, h_2 C_2 \rangle$$
$$= h_1 \langle C_1, C_1 \rangle + h_2 \langle C_1, C_2 \rangle$$
$$= h_1$$

If $\langle C_1, C_1 \rangle = 1$ and $\langle C_1, C_2 \rangle = 0$
Verify that $\langle C^1, C^2 \rangle = 0$

Modulated carriers

Sum of modulated carriers

$\langle C^1, 0.2C^1 + 0.7C^2 \rangle =$

$\langle C^1, 0.2C^1 \rangle + \langle C^1, 0.7C^2 \rangle =$

$0.2 \langle C^1, C^1 \rangle + 0$
FDMA

Abstract view

Horizontal axis: time
Vertical axis: amplitude (arbitrary units)
S’pose we pick random carriers: \( c_1 = 2 \times (\text{rand}(1, 500) > 0.5) - 1; \)

Horizontal axis: time  
Vertical axis: amplitude (arbitrary units)

Note: Random carriers here consist of 500 rand values repeated 10 times each for better display
LFSRs (Linear Feedback Shift Registers)
The right way to generate pseudo-random carriers for CDMA

- A simple pseudo-random number generator
  - Pick a start state, iterate
- Maximum Length LFSR visits all states before repeating
  - Based on primitive polynomial...iterating LFSR equivalent to multiplying by generator for group
  - Can analytically compute auto-correlation
- This form of LFSR is easy to compute in HW (but not as nice in SW)
  - Extra credit: there is another form that is more efficient in SW
- Totally uniform auto-correlation
LFSR TX

8 bit LFSR with taps at 3, 4, 5, 7 (counting from 0). Known to be maximal.

for (k=0; k<3; k++) { // k indexes the 4 LFSRs
    low=0;
    if (lfsr[k] & 8) // tap at bit 3
        low++; // each addition performs XOR on low bit of low
    if (lfsr[k] & 16) // tap at bit 4
        low++;
    if (lfsr[k] & 32) // tap at bit 5
        low++;
    if (lfsr[k] & 128) // tap at bit 7
        low++;
    low&=1; // keep only the low bit
    lfsr[k] <<= 1; // shift register up to make room for new bit
    lfsr[k] &= 255; // only want to use 8 bits (or make sure lfsr is 8 bit var)
    lfsr[k] |= low; // OR new bit in
}
OUTPUT_BIT(TX0, lfsr[0] & 1); // Transmit according to LFSR states
OUTPUT_BIT(TX1, lfsr[1] & 1);
OUTPUT_BIT(TX2, lfsr[2] & 1);
OUTPUT_BIT(TX3, lfsr[3] & 1);
meas=READ_ADC(); // get sample...same sample will be processed in different ways
for(k=0;k<3;k++) {
    if(lfsr[k]&1) // check LFSR state
        accum[k]+=meas; // make sure accum is a 16 bit variable!
    else
        accum[k]-=meas;
}
LFSR state sequence

```matlab
>> lfsr1(1:255)
ans =
    2     4     8    17    35    71   142    28    56   113   226   196   137    18
   37    75   151    46    92   184   112   224   192   129     3     6    12    25
   50   100   201   146    36    73   147    38    77   155    55   110   220   185
  114   228   200   144    32    65   130     5    10   21    43    86   173    91
  182   109   218   181   107   214   172    89   178   101   203   150    44    88
  176   97   195   135    15    31   125   251   246   237   219   183   111
  222   189   122   245   235   215   174    93   186   116   232   209   162
  136   16    33    67   134    13    27    54   108   216   177    99   199
  143   30   60   121   243   231   206   156    57   115   230   204   152
   49    98   197   139    22    45    90   180   105   210   164    72   145
   34    69   138   20   41    82   165    74   149    42    84   169   83
   167    78   157   59   119   238   221   187   118   236   217   179
  103   207   158    61   123   247   239   223   191   126   253   250
   244   233   211   166    76   153   51   102   205   154    53    106
   212   168    81   163    70   140   24    48    96   193   131    7
   14    29    58   117   234   213   170    85   171   87   175    95
  190   124   249   242   229   202   148   40    80   161   66   132    9
   19   39    79   159    63    127   255   254   252   248   240   225
  194   133   11   23    47    94   188   120   241   227   198   141
   26    52   104   208   160   64   128    1
```
## LFSR output

\[
\begin{align*}
\text{ans} = & \begin{bmatrix}
-1 & -1 & -1 & 1 & 1 & 1 & -1 & -1 & -1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 & -1 & -1 & 1 & -1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & -1 & 1 & 1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & -1 & 1 & 1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & -1 & 1 & 1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & -1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \\
1 & 1 & 1 & -1 & -1 & -1 & -1 & -1 & -1 & -1 & 1 & 1 & -1 & 1 \\
-1 & -1 & 1 & 1 & 1 & 1 & -1 & 1 & 1 & 1 & -1 & -1 & 1 & -1 \end{bmatrix}
\end{align*}
\]
CDMA by LFSR

Note: CDMA carriers here consist of 500 pseudorandom values repeated 10 times each for better display.
Autocorrelation of pseudo-random (non-LFSR) sequence of length 255

PR seq
Generated w/ Matlab rand cmd
Autocorrelation (full length 255 seq)
End of lecture
Autocorrelation (length 254 sub-seq)

0 or -2
Autocorrelation (length 253 sub-seq)

1, -1, or -3
Autocorrelation (length 128 sub-seq)
Note: In a HW implementation, if you have XOR gates with as many inputs as you want, then the upper configuration is just as fast as the lower. If you only have 2 input XOR gates, then the lower implementation is faster in HW since the XORs can occur in parallel.

“Fibonacci”
“Standard”
“Many to one”
“External XOR”
LFSR

“Galois”
“One to many”
“Internal XOR”
LFSR

Faster in SW!!
Advantage of Galois LFSR in SW

Faster in SW because XOR can happen word-wise (vs the multiple bit-wise tests that the Fibonacci configuration needs)

```c
#include <stdint.h>
uint16_t lfsr = 0xACE1u;
unsigned int period = 0;
do {
    unsigned lsb = lfsr & 1; /* Get lsb (i.e., the output bit). */
    lfsr >>= 1; /* Shift register */
    if (lsb == 1) /* Only apply toggle mask if output bit is 1. */
        lfsr ^= 0xB400u; /* Apply toggle mask, value has 1 at bits corresponding to taps, 0 elsewhere. */
    ++period;
} while(lfsr != 0xACE1u);
```
#include <stdint.h>
uint16_t lfsr = 0xACE1u;
unsigned period = 0;
do {
    /* taps: 16 14 13 11; char. poly: \(x^{16}+x^{14}+x^{13}+x^{11}+1\) */
    lfsr = (lfsr >> 1) ^ ((-lfsr & 1u) & 0xB400u);
    ++period;
} while(lfsr != 0xACE1u);

NB: The minus above is two’s complement negation…here the result is all zeros or all ones…that is ANDed that with the tap mask…this ends up doing the same job as the conditional from the previous implementation. Once the mask is ready, it is XORed to the LFSR
Some “polynomials” (tap sequences) for Max. Length LFSRs

<table>
<thead>
<tr>
<th>Bits $n$</th>
<th>Feedback polynomial</th>
<th>Period $2^n - 1$</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>$x^2 + x + 1$</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>$x^3 + x^2 + 1$</td>
<td>7</td>
</tr>
<tr>
<td>4</td>
<td>$x^4 + x^3 + 1$</td>
<td>15</td>
</tr>
<tr>
<td>5</td>
<td>$x^5 + x^3 + 1$</td>
<td>31</td>
</tr>
<tr>
<td>6</td>
<td>$x^6 + x^5 + 1$</td>
<td>63</td>
</tr>
<tr>
<td>7</td>
<td>$x^7 + x^6 + 1$</td>
<td>127</td>
</tr>
<tr>
<td>8</td>
<td>$x^8 + x^6 + x^5 + x^4 + 1$</td>
<td>255</td>
</tr>
<tr>
<td>9</td>
<td>$x^9 + x^5 + 1$</td>
<td>511</td>
</tr>
<tr>
<td>10</td>
<td>$x^{10} + x^7 + 1$</td>
<td>1023</td>
</tr>
<tr>
<td>11</td>
<td>$x^{11} + x^9 + 1$</td>
<td>2047</td>
</tr>
<tr>
<td>12</td>
<td>$x^{12} + x^{11} + x^{10} + x^4 + 1$</td>
<td>4095</td>
</tr>
<tr>
<td>13</td>
<td>$x^{13} + x^{12} + x^{11} + x^8 + 1$</td>
<td>8191</td>
</tr>
<tr>
<td>14</td>
<td>$x^{14} + x^{13} + x^{12} + x^2 + 1$</td>
<td>16383</td>
</tr>
<tr>
<td>15</td>
<td>$x^{15} + x^{14} + 1$</td>
<td>32767</td>
</tr>
<tr>
<td>16</td>
<td>$x^{16} + x^{14} + x^{13} + x^{11} + 1$</td>
<td>65535</td>
</tr>
<tr>
<td>17</td>
<td>$x^{17} + x^{14} + 1$</td>
<td>131071</td>
</tr>
<tr>
<td>18</td>
<td>$x^{18} + x^{11} + 1$</td>
<td>262143</td>
</tr>
<tr>
<td>19</td>
<td>$x^{19} + x^{18} + x^{17} + x^{14} + 1$</td>
<td>524287</td>
</tr>
</tbody>
</table>
More on why modulation is useful

- Discussed channel sharing already
- Now: noise immunity
Noise

Why modulated sensing?

- Johnson noise
  - Broadband thermal noise
- Shot noise
  - Individual electrons…not usually a problem
- “1/f” “flicker” “pink” noise
  - Worse at lower frequencies
  - ➔ do better if we can move to higher frequencies
- 60Hz pickup

---

Modulation

What is it?
- In music, changing key
- In old time radio, shifting a signal from one frequency to another
- Ex: voice (10kHz “baseband” sig.) modulated up to 560kHz at radio station
- Baseband voice signal is recovered when radio receiver demodulates
- More generally, modulation schemes allow us to use analog channels to communicate either analog or digital information
  - Amplitude Modulation (AM), Frequency Modulation (FM), Frequency hopping spread spectrum (FHSS), direct sequence spread spectrum (DSSS), etc

What is it good for?
- Sensitive measurements
  - Sensed signal more effectively shares channel with noise → better SNR
- Channel sharing: multiple users can communicate at once
  - Without modulation, there could be only one radio station in a given area
  - One radio can chose one of many channels to tune in (demodulate)
- Faster communication
  - Multiple bits share the channel simultaneously → more bits per sec
  - “Modem” == “Modulator-demodulator”
Q: What determines number of messages we can send through a channel (or extract from a sensor, or from a memory)?

A: The number of inputs we can reliably distinguish when we make a measurement at the output

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Shannon
Other applications of modulation / demodulation or correlation computations
Other applications of modulation / demodulation or correlation computations

These are extremely useful algorithmic techniques that are not commonly taught or are scattered in computer science

- Amplitude-modulated sensing (what we’ve been doing)
  - Also known as synchronous detection
- Ranging (GPS, sonar, laser rangefinders)
- Analog RF Communication (AM radio, FM radio)
- Digital Communication (modem==modulator demodulator)
- Data hiding (digital watermarking / steganography)
- Fiber Fingerprinting (biometrics more generally)
- Pattern recognition (template matching, simple gesture rec)
CDMA in comms: Direct Sequence Spread Spectrum (DSSS)

- Other places where DSSS is used
  - 802.11b, GPS

- Terminology
  - Symbols: data
  - Chips: single carrier value
  - Varying number of chips per symbol varies data rate…when SNR is lower, increase number of chips per symbol to improve robustness and decrease data rate
  - Interference: one channel impacting another
  - Noise (from outside)
Visualizing DSSS

https://www.okob.net/texts/mydocuments/80211physlayer/images/dsss_interf.gif
Practical DSSS radios

- DSSS radio communication systems in practice use the pseudo-random code to modulate a sinusoidal carrier (say 2.4GHz)
- This spreads the energy somewhat around the original carrier, but doesn’t distribute it uniformly over all bands, 0-2.4GHz
- Amount of spreading is determined by chip time (smallest time interval)
Data hiding

FiberFingerprint Identification
Proceedings of the Third Workshop on Automatic Identification, Tarrytown, NY, March 2002
E. Metois, P. Yarin, N. Salzman, J.R. Smith

Key in this application: remove DC component before correlating
Gesture recognition by cross-correlation of sensor data with a template

\[ C = \sum_{i=1}^{n} (A_{xi}T_{xi} + A_{yi}T_{yi} + A_{zi}T_{zi}) \]

“RFIDs and Secret Handshakes: Defending Against Ghost-and-Leech Attacks and Unauthorized Reads with Context-Aware Communications,” A. Czeskis, K. Koscher, J.R. Smith, and T. Kohno
Limitations

- TX and RCV need common time-scale (or length scale)
  - Will not recognize a gesture being performed at a different speed than the template

- Except in sensing (synchronous detection) applications, need to synchronize TX and RX...this is a search that can take time
End of section