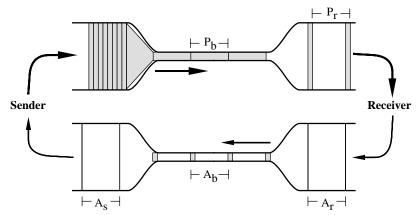
- 1. Peterson and Davie, p 435, number 8.
- 2. Peterson and Davie, p 435, number 9(a). Assume it's a good idea to save bits whereever possible, so find the minimum number of bits that will still provide high performance.
- 3. State the robustness principle. You may find it in RFC 793.
- 4. (A question Tom Anderson used) If two stations on an Ethernet communicate via a sliding window protocol, why might we see a collision on nearly every packet?
- 5. A TCP receiver may send an acknowledgement that advertises a zero-length window. This tells the sender that the window is "closed."
  - (a) Why would a receiver send such an acknowledgement?
  - (b) If the application reads a byte from the buffer, should the kernel send a new acknowledgement advertising a 1-byte window? Why or why not?
- 6. Suppose an application wants to send 100 bytes reliably using TCP. The Maximum Segment Size is 1460 bytes, so these 100 bytes clearly fit within a single packet. In the absence of loss, and over the entire duration of the connection,
  - (a) How many packets will be sent by the sender?
  - (b) How many packets will be sent by the receiver?
  - (c) In multiples of the round trip time of the network between sender and receiver, how long will the connection last?



7. One mechanism for estimating the bottleneck link bandwidth sends a pair of back to back packets into the network, and measures the time between the receipt of acknowledgements. As these packets traverse the bottleneck link, they stretch: taking more time because of the reduced bandwidth. When those packets find a high bandwidth link, they contract in time, but preserve the spacing they acquired because of the bottleneck link. Acknowledgements for these packets also preserve the same spacing.

Because this scheme uses a pair of packets, it is referred to as "packet pair."

Assume that the source sends back to back 1500 byte packets, and that the space between acknowledgements is 1ms.

What is the bandwidth of the bottleneck link?

As an editorial aside, there are two problems to know about this scheme. One is called "ack compression," where the returning acks might have to wait behind queued packets on the return path, which reduces the inter-ack spacing, making the bandwidth of the bottleneck appear larger.

Another is that PC clocks generally tick once every hundredth of a second, and it is somewhat difficult to measure time on a finer granularity (as in this example). While packets traversing a modem can span hundreds of milliseconds, packets traversing a 10 Mbit Ethernet don't even show up.