#### SAFETY, LIVENESS, AND CONSISTENCY

#### **Ellis Michael**

# **HOW DO WE SPECIFY DISTRIBUTED SYSTEMS?**

- **Execution:** Sequence of events (i.e., steps taken by the system), potentially infinite.
- **Property:** A predicate on executions.
- Safety property: Specifies the "bad things" that shouldn't happen in any execution.
- Liveness property: Specifies the "good things" that should happen in every execution.

(See paper for formal definitions.)

#### **THEOREM: EVERY PROPERTY IS EXPRESSIBLE AS THE CONJUNCTION OF A SAFETY PROPERTY AND A LIVENESS PROPERTY.**

#### [Alpern and Schneider. 1987]



#### **THEOREM: EVERY PROPERTY IS EXPRESSIBLE AS THE CONJUNCTION OF A SAFETY PROPERTY AND A LIVENESS PROPERTY.**

Neat automata theory!

#### [Alpern and Schneider. 1987]



#### **SOME PROPERTIES**

#### • The system never deadlocks.

a reply.

Both generals attack simultaneously.

#### • Every client that sends a request eventually gets

## **MORE PROPERTIES: CONSENSUS**

*n* processes, all of which have an input value from some domain. Processes output a value by calling *decide(v)*. Non-faulty processes continue correctly executing protocol steps forever. We usually denote the number of faulty processes f.

- •

• Agreement: No two correct processes decide different values.

Integrity: Every correct process decides at most one value, and if a correct process decides a value v, some process had v as its input.

• **Termination:** Every correct process eventually decides a value.

## **CONSISTENCY IS KEY!**

**Consistency:** the allowed semantics (return values) of a set of operations to a data store or shared object.

Consistency properties specify the **interface**, not the **implementation**. The data might be replicated, cached, disaggregated, etc. "Weird" consistency semantics happen all over the stack!

Anomaly: violation of the consistency semantics

## **TERMINOLOGY: STRENGTH AND WEAKNESS**

- **Strong consistency:** the system behaves as if there's just a single copy of the data (or almost behaves that way).
  - The intuition is that things like caching and sharding are implementation decisions and shouldn't be visible to clients.
- Weak consistency: allows behaviors significantly different from the single store model.
- Eventual consistency: the aberrant behaviors are only temporary.

## WHY THE DIFFERENCE?

#### Performance

- data is replicated
- Availability
  - What if client is offline, or network is not working?
  - Weak/eventual consistency may be only option
- Programmability
  - Weaker models are harder to reason against

#### - Consistency requires synchronization/coordination when

- Often slower to make sure you always return right answer

Registers hold a single value. Here, we consider single-writer registers only supporting write and read.

Semantics defined in terms of the *real-time* beginnings and ends of operations to the object.

- safe: a read not concurrent with any write obtains the previously written value
- regular: safe + a read that overlaps a write obtains either the old or new value
- atomic: safe + reads and writes behave as if • they occur in some definite order





Registers hold a single value. Here, we consider single-writer registers only supporting write and read.

Semantics defined in terms of the *real-time* beginnings and ends of operations to the object.

- safe: a read not concurrent with any write obtains the previously written value
- regular: safe + a read that overlaps a write obtains either the old or new value
- atomic: safe + reads and writes behave as if • they occur in some definite order





#### safe $\Rightarrow$ r<sub>1</sub> $\rightarrow$ a



Registers hold a single value. Here, we consider single-writer registers only supporting write and read.

Semantics defined in terms of the *real-time* beginnings and ends of operations to the object.

- safe: a read not concurrent with any write obtains the previously written value
- regular: safe + a read that overlaps a write obtains either the old or new value
- atomic: safe + reads and writes behave as if • they occur in some definite order



safe  $\Rightarrow$  r<sub>1</sub>  $\rightarrow$  a

**regular**  $\Rightarrow$  r<sub>1</sub>  $\rightarrow$  a  $\land$  (r<sub>2</sub>  $\rightarrow$  a  $\lor$  r<sub>2</sub>  $\rightarrow$  b)  $\land$ 

$$(r_3 \rightarrow a \lor r_3 \rightarrow b)$$



Registers hold a single value. Here, we consider single-writer registers only supporting write and read.

Semantics defined in terms of the *real-time* beginnings and ends of operations to the object.

- safe: a read not concurrent with any write obtains the previously written value
- regular: safe + a read that overlaps a write obtains either the old or new value
- atomic: safe + reads and writes behave as if they occur in some definite order



safe  $\Rightarrow$  r<sub>1</sub>  $\rightarrow$  a

**regular**  $\Rightarrow$  r<sub>1</sub>  $\rightarrow$  a  $\land$  (r<sub>2</sub>  $\rightarrow$  a  $\lor$  r<sub>2</sub>  $\rightarrow$  b)  $\land$  $(r_3 \rightarrow a \lor r_3 \rightarrow b)$ 

atomic  $\Rightarrow$  r<sub>1</sub>  $\rightarrow$  a  $\land$  (r<sub>2</sub>  $\rightarrow$  a  $\lor$  r<sub>2</sub>  $\rightarrow$  b)  $\land$ 

$$(r_3 \rightarrow a \lor r_3 \rightarrow b) \land$$
  
 $(r_2 \rightarrow b \Rightarrow r_3 \rightarrow b)$ 



## **SEQUENTIAL CONSISTENCY**

- Applies to arbitrary shared objects.
- Requires that a history of operations be local ordering at each node.
- Called serializability when applied to transactions

equivalent to a legal sequential history, where a legal sequential history is one that respects the



w(a) $p_{-}$ w(b) $p_2$  $p_3$  $p_4$ 



w(a)pw(b) $p_2$  $p_3$  $p_4$ 



w(a) w(c) $p_{-}$ w(b) $p_2$  $p_3$  $p_4$ 





w(a) $p_{-}$ w(b) $p_2$  $p_3$  $p_4$ 



P2

#### w(a) $r \rightarrow a$ $r \rightarrow a$ $r \rightarrow a$ w(b) $r \rightarrow b$



w(a)pw(b) $p_2$  $p_3$  $p_4$ 



w(a)pw(b) $p_2$ r→a  $p_3$  $p_4$ 



#### LINEARIZABILITY

#### Linearizability = sequential consistency + respects real-time ordering.

#### If $e_1$ ends before $e_2$ begins, then $e_1$ appears before e<sub>2</sub> in the sequential history.

Linearizable data structures behave as if there's a single, correct copy.

#### Atomic registers are linearizable.









## LINEARIZABILITY VS. SEQUENTIAL CONSISTENCY

#### Sequential consistency allows operations to appear out of real-time order. How could that happen in reality?

## LINEARIZABILITY VS. SEQUENTIAL CONSISTENCY

happen in reality?

 The most common way systems are sequentially consistency but not linearizability is that they allow read-only operations to return stale data.

#### Sequential consistency allows operations to appear out of real-time order. How could that



020

a





9







#### Read-only Cache



20

-

Write







#### Read-only Cache



250

~

Write







#### Read-only Cache


### **CAUSAL CONSISTENCY**

 Writes that are not concurrent (i.e., writes related by the happens-before relation) must be seen in that order. Concurrent writes can be seen in different orders on different nodes.

### **CAUSAL CONSISTENCY**

 Writes that are not concurrent (i.e., writes related by the happens-before relation) must be seen in that order. Concurrent writes can be seen in different orders on different nodes.

• Linearizability implies causal consistency.

w(a) $p_1$ w(b) $p_2$ 

 $p_3$ 

 $p_4$ 



w(a) $p_1$ w(b) $p_2$ 

 $p_3$ 

 $p_4$ 

We need to know what causes what (i.e., what messages are sent)!

r→a

r→b

r→b



r→a

w(a) $p_1$ w(b) $p_2$  $p_3$  $p_4$ 



w(a) $p_1$ w(b) $p_2$  $p_3$  $p_4$ 







**Cool Theorem:** Causal consistency\* is the strongest form of consistency that can be provided in an always-available convergent system.

Basically, if you want to process writes even in the presence of network partitions and failures, causal consistency is the best you can do.

[Mahajan et al. UTCSTR-11-22]

\*real-time causal consistency



## WE CAN GET WEAKER!

 FIFO Consistency: writes done by the same process are seen in that order; writes to different processes can be seen in different orders.
 Equivalent to the PRAM model.

 Eventual Consistency ≈ if all writes to an object stop, eventually all processes read the same value. (Not even a safety property! "Eventual consistency is no consistency.")





w(a) $p_1$ w(b) *p*2 *p*<sub>3</sub>

 $p_4$ 



 $p_4$ 

w(a) $p_1$ w(b)  $p_2$  $p_3$ 



Lamport's register semantics, sequential consistency, linearizability, and causal consistency, and FIFO consistency are all *safety properties*.

#### **Thread 1**

#### a = 1 print("b:" + b) print("a:" + a)

### Initially, both a and b are 0.

What are the possible outputs of this program?

#### **Thread 2**

### b = 1

#### **Thread 1**

#### a = 1print("b:" + b) print("a:" + a)

### Initially, both a and b are 0.

What are the possible outputs of this program?

#### **Thread 2**

### b = 1

Depends on memory consistency!



#### **Thread 1**

#### a = 1print("b:" + b) print("a:" + a)

#### Suppose both prints output 0.

#### **Thread 2**

b = 1

#### **Thread 1**



### Suppose both prints output 0.

Then there's a cycle in the happens-before graph. Not sequential!

### **Thread 2** = 1 print("a:" + a)

### **ASIDE: JAVA'S MEMORY MODEL**

- Java is not sequentially consistent!
- the program is data-race free.
- protected by locks (or monitors etc.).

It guarantees sequential consistency only when

 A data-race occurs when two threads access the same memory location concurrently, one of the accesses is a write, and the accesses are not

# **A COMMON (INCORRECT) IDIOM**

class Foo {
 private Bar bar = null;

public void baz() {
 if (bar == null) {
 synchronized(this) {
 if (bar == null) {
 bar = new Bar();
 }
 }
 }
}

bar.doAThing();

### A COMMON (INCORRECT) IDIOM, CORRECTED

class Foo {
 private volatile Bar bar = null;

public void baz() {
 if (bar == null) {
 synchronized(this) {
 if (bar == null) {
 bar = new Bar();
 }
 }
 }
}

bar.doAThing();

volatile = accesses are
sequentially consistent



### A COMMON (INCORRECT) IDIOM, CORRECTED

### class Foo { private volatile Bar bar = null;

bar.doAThing();

Reminder: you don't need to worry about multi-threaded access for the labs!

bar = ne (except not grabbing locks in equals and hashCode)

e = accesses are ally consistent



### HOW TO USE WEAK CONSISTENCY?

default

 Application-level protocols, either using separate communication, or extra always possible)

#### Separate operations with stronger semantics, weak consistency (and high performance) by

# synchronization variables in the data store (not

### MAIN TAKEAWAYS

is to program against (usually).

• The stronger the model, the harder it is to enforce (again, usually).

# • The weaker the consistency model, the harder it