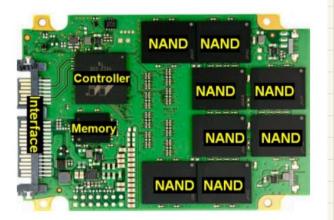
SSD & Filesys Basics 5/8/24

Solid State Prive -> persistent, block addressable, large capacity

NAND Flash packages Connected by Multiple Channels, highly parallel architecture



Units = blocks, pages

0000000 0000000

a block contains hundreds of pages,

page is the wait of read / mide. (4kB)

no moling parts

SSD Operactions

-> Had a page (443): fast access ~10 ms -> Unite a page (4KB): Can only unite to a clean page (all bits are set to 1) (program) program bits to Os to Unite data ~ 100 ms -> erase a block [1-8MB]: erase all pages within the block (all 15) Slow operastion ~ 1-3 ms To do in place update on SSD program u/ Want to reprogram this page new data (metadata) L] clean page & there's a valid bit BBODD Crase DDDDD program $\square \square \square \square \square$ associated v/ each (11) page, easy way to mark programmed garbage page Save valid programmed page (s) restore valid pages w/ saved data elsewhere (some clean pages)

SSD Reliability -> a page can only reliably endure 10-100 k wites -> repeated mites to pages cause frequently modified pages to wear out faster (no longer retain data reliably) -> wear-leveling: spread notes to different pages to near out pages evenly => each notes the data to a new page unent version of z moving data around all & Flash Translation Layer M W W the time not good for SSD clients, better transparent! -> translate logical black address physical block adobess -> garbage collection -> more sparsely valid pages into a new block, frees up a block for evasue

SSD Request Latony

total time = access laterry + transfer time + (erasure time)

-> latency of a read page request given 10 ms read latency & 500 MTB/S

-> way less sensitive to access patterns 17.8 ms 7.8 ms

-> much closer performance for seguenotial & random accesses

may be parallelzeel.

