

Setup Help + Extra OH

CSE 444 - Section 2

Administrivia

- Lab 1 Part 1 - Due Monday
- HW 1 - Due Friday the 11th
- Lab 1 Part 2 - Due the 16th

Write Up Tips

Don't stress it !

- Never say “I didn't make a design decision” (thats cap), any design decision even if small will net you the points (don't have to say everything)
- Brief description of how components you implemented interact with one another (e.x. BufferPool and HeapPage)
- Extra unit tests you come up with (could be just 1)

Lab Code Tips

- `ConcurrentHashMap` is highly recommended! (Basically just `HashMap` but supports under the hood locking)
- For `TupleDesc equals()`, compare length and types instead of types and names
- Remember to add checks for possible edge cases (off-by-one errors, size errors, etc.)

Setup/Lab/HW Questions?

Everyone free to leave if no questions :D

Have a good day!

