CSE 444: Database Internals

Section 6: Transactions

Today

- Serializability and Conflict Serializability

 Precedence graph
- Two-Phase Locking

 Strict two phase locking

Problem 1: Serializability and Locking What is

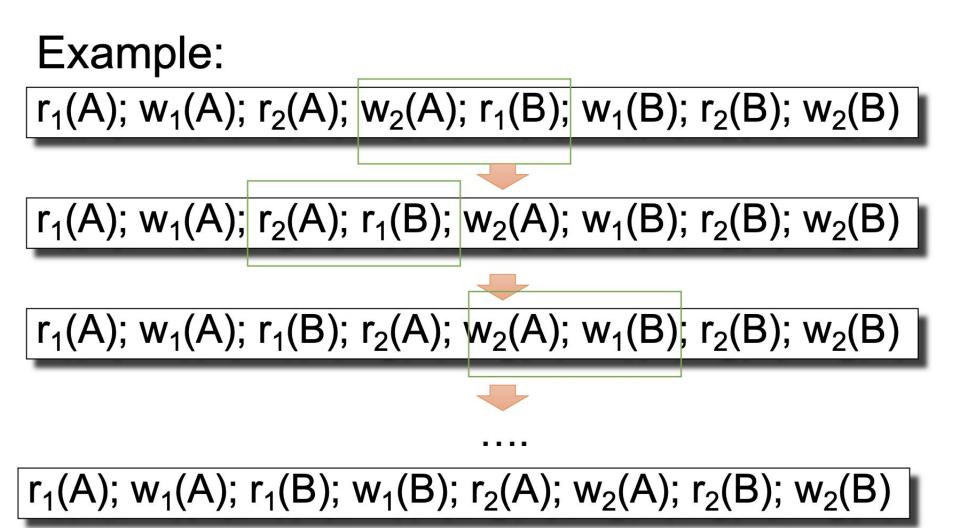
- Is this schedule conflict serializab
- SerializabilityConflict Serializability?

Τ _ο	T ₁
R ₀ (A) W ₀ (A)	
W ₀ (A)	
	R ₁ (A)
	R ₁ (B)
	C ₁
R ₀ (B)	
R ₀ (B) W ₀ (B) C ₀	
C ₀	

Review: (Conflict) Serializable Schedule

• A schedule is *serializable* if it is equivalent to a serial schedule

 A schedule is <u>conflict serializable</u> if it can be transformed into a serial schedule by a series of swappings of adjacent non-conflicting actions

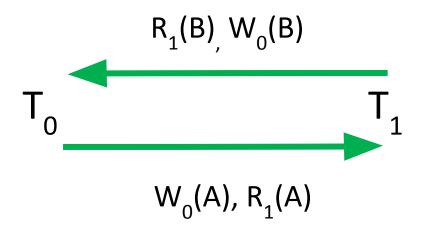


Problem 1: Serializability and Locking

• Is this schedule conflict serializable?

Τ _ο	T ₁
R ₀ (A) W ₀ (A)	
W ₀ (A)	
	R ₁ (A)
	R ₁ (B)
	C ₁
R ₀ (B)	
R ₀ (B) W ₀ (B) C ₀	
C ₀	

- No.
- The precedence graph contains a cycle



So, use 2PL ...
Original schedule below

Τ _ο	T ₁
R ₀ (A)	
W ₀ (A)	
	R ₁ (A)
	R ₁ (B)
	C ₁
R ₀ (B)	
W ₀ (B)	
C ₀	

• So, use 2PL	
Original schedule below	What is
	 Two Phase Locking
	 Strict Two Phase Locking?

Τ _ο	T ₁
R ₀ (A)	
R ₀ (A) W ₀ (A)	
	R ₁ (A)
	R ₁ (A) R ₁ (B)
	C ₁
R ₀ (B)	
R ₀ (B) W ₀ (B) C ₀	
C ₀	

Review:

(Strict) Two Phase Locking (2PL)

The 2PL rule:

In every transaction, all lock requests must precede all unlock requests

Strict 2PL:

All locks held by a transaction are released when the transaction is completed

- Ensures that schedules are <u>recoverable</u>
 - Transactions commit only after all transactions whose changes they read also commit
- Avoids <u>cascading rollbacks</u>

- How can 2PL ensure a conflict-serializable schedule?
 - Original schedule below

Τ _ο	T ₁
R ₀ (A) W ₀ (A)	
W ₀ (A)	
	R ₁ (A)
	R ₁ (B)
	C ₁
R ₀ (B)	
R ₀ (B) W ₀ (B)	
C ₀	

Τ _ο	T ₁
L ₀ (A) R ₀ (A) W ₀ (A)	
R ₀ (A)	
W ₀ (A)	

T ₁
L ₁ (A) : Block

Τ _ο	Τ ₁
L ₀ (A) R ₀ (A) W ₀ (A)	
R ₀ (A)	
W ₀ (A)	
	L ₁ (A) : Block
L _o (B)	
R ₀ (B)	
W ₀ (B)	
U ₀ (A)	
<mark>U₀(В)</mark> С ₀	
C ₀	

Τ _o	T ₁
L _o (A)	
R ₀ (A)	
W ₀ (A)	
	L ₁ (A) : Block
L _o (B)	
R ₀ (B)	
W ₀ (B)	
U ₀ (A)	
U ₀ (В)	
C ₀	
	L ₁ (A) : Granted
	R ₁ (A)
	L ₁ (B)
	R ₁ (B)
	U ₁ (A)
	U ₁ (A) U ₁ (B) C ₁
	C ₁

Τ _ο	Τ ₁
L _o (A)	
R ₀ (A)	
W ₀ (A)	
	L ₁ (A) : Block
L _o (B)	
R ₀ (B)	Is this strict 2PL?
W ₀ (B)	
U _o (A)	No, release locks after commit
U ₀ (В)	
C ₀	
	L ₁ (A) : Granted
	R ₁ (A)
	L ₁ (B)
	R ₁ (B)
	U ₁ (A)
	U ₁ (B)
	C ₁

 That example ended in a serial schedule, 2PL doesn't necessarily require that
 Here's an example that doesn't become serial

Τ _ο	T ₁
R ₀ (A)	
R ₀ (A) W ₀ (A)	
	R ₁ (A)
	R ₁ (A) R ₁ (B)
	C ₁
R ₀ (B)	
R ₀ (B) W ₀ (B) C ₀	
C ₀	

Τ _ο		T ₁
L _o (A)		
R _o (A) W _o (A)		
		get the lock on B first, we have
L _o (B) U _o (A)	completed the locking phase, and can start unlocking	
		Start amouning

	Τ _ο		T ₁	
	L _o (A)			
	R _o (A) W _o (A)			
	W ₀ (A)			
	L ₀ (В)			
	U _o (A)			
Allowing for interleaving	z can	L ₁ (A)		
	help performance	7	R ₁ (A)	
			L ₁ (B): Block	
			1	
			1	
			I	
			1	
			I	

Τ _ο	T ₁
L _o (A)	
R ₀ (A) W ₀ (A)	
W _o (A)	
L _o (B)	
U _o (A)	
	L ₁ (A)
	L ₁ (A) R ₁ (A)
	L ₁ (B): Block
R ₀ (B)	
W ₀ (В)	
U ₀ (B)	
C ₀	

Τ _ο		T ₁
L ₀ (A)		
R ₀ (A)		
W _o (A)		
L _o (B)	Ma hav	a uplacks in a transaction before
U _o (A)		e unlocks in a transaction before mitting, so this isn't strict 2 PL
		1
		R ₁ (A)
		L ₁ (B): Block
R ₀ (B)		
W ₀ (B)		
U ₀ (B)		
C ₀		
		L ₁ (B): Granted
		R ₁ (B)
		U ₁ (A)
		U ₁ (A) U ₁ (B) C ₁
		C,

Common 2PL Misconceptions

- Remember, only one transaction can hold a lock for an element at once
- In 2PL, within each transaction there must be a growing (lock acquiring phase) followed by a shrinking (unlocking) phase
 - Before unlocking, all locks must be be granted, not just requested
- Both 2PL and Strict 2PL ensure conflict serializability, neither guarantee a serial schedule or prevent deadlocks

Lab 3 - Transactions

• NO STEAL / FORCE buffer management policy

• you shouldn't evict dirty (updated) pages from the buffer pool to disk if they are locked by an uncommitted transaction. (this is **NO STEAL**)

 <u>on transaction commit</u>, you should <u>force dirty pages to disk</u> (e.g., write the pages out) (this is FORCE)

• Recommend - locking at page level

• you can acquire locks in <u>BufferPool.getPageO</u>, instead of adding calls to each of your operators. Since we are implementing strict 2PL, we release locks after transaction completes.

 Might have to change previous implementations to access pages using <u>BufferPool.getPageO</u>

You need to implement <u>shared</u> and <u>exclusive</u> locks

• Before read, it must have a shared lock or exclusive lock

- Before write, it must have an exclusive lock
- <u>Multiple transactions can have a shared lock</u>
- Only one transaction may have an exclusive lock on an object

o If transaction t is the only transaction holding a shared lock on an object o, t may upgrade its lock on o to an exclusive lock when it needs to write!

You need to implement strict two-phase locking • transactions should acquire the appropriate type of lock on any object before accessing that object

 transaction shouldn't release any locks until after the transaction commits.

- You will need to implement a LockManager class that will hold data structures to keep track of which locks each transaction holds and that check to see if a lock should be granted to a transaction when it is requested.
- Read about <u>Synchronization</u> in Java, and use the <u>synchronized</u> keyword in appropriate places in <u>LockManager</u>
- You will have to also throw appropriate exceptions like **TransactionAbortedException** when transaction aborts

Synchronized method :

Only one thread executing that method per instance public synchronized void releaseLock()

public void acquireLock() {

// wait for lock

synchronized (this) {

// update state

Synchronized block :

Only one thread executing that block of code at one time

Usually used to update that object's state synchronously

• Handling deadlocks

- implement a simple timeout policy that aborts a transaction if it has not completed after a given period of time
- implement a cycle-detection in a dependency graph data structure, if cycle exists when granting a new lock abort something.
- Design Choices:
 - Locking Granularity: page-level vs tuple-level (our tests assume page-level)
 - Deadlock Detection: timeout vs dependency graphs
 - Deadlock Resolution: aborting yourself vs aborting others

• Read the spec carefully for more details about various methods and edge cases .

Problem 2: Timestamp-based Concurrency Control

Timestamp-based Concurrency Control

- Some transaction, T.
- Some element (tuple/page), X.
- **TS**(**T**) timestamp for transaction **T**
 - Stays constant for all of T's operations
- WT(X) latest write timestamp for X
 - Set WT(X) = TS(T)
- RT(X) latest read timestamp for X
 - Set RT(X) = TS(T)
- C(X) X's value has been committed
 - 1 if true, 0 if not

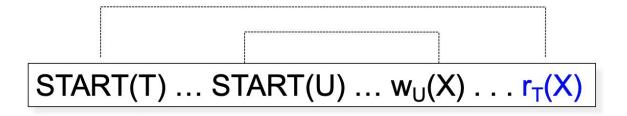
Timestamp-based Concurrency Control

Actions for transaction T

- **Grant** a read/write request for a transaction
- Abort (in case T violates physical reality late actions)
- **Delay** (make the Grant or Abort decision later)
 - When writing, the change is always tentative until we decide to commit. For this, we use a commit bit C to keep track if the transaction that last wrote X has committed
- Ignore *Thomas Write Rule* ignore outdated writes

Timestamp-based Concurrency Control - Four Rules

• Rule 1: Read request on X by T



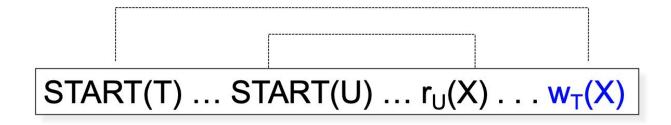
– TS(T) < WT(X), abort, (read too late)</p>

- TS(T) >= WT(X), physically realizable

- If C = 1, grant, update RT(X)
- If C = 0, **delay** T

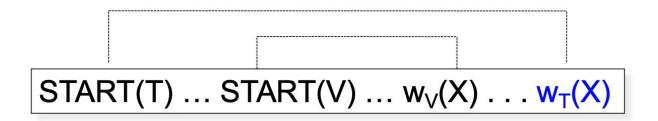
Timestamp-based Concurrency Control - Four Rules

• Rule 2: Write request on X by T



TS(T) < RT(X) (write too late)
Abort

- $-TS(T) \ge RT(X)$, physically realizable
 - TS(T) >= WT(X)
 - then grant, update WT(X), set C = 0 (as it's not committed yet)



- TS(T) < WT(X)
 - If C = 1, don't write X at all! (*Thomas Write Rule* ignore outdated writes)
 - If C = 0, delay

Timestamp-based Concurrency Control - Four Rules

- Rule 3: Commit request by T
 - Set C = 1 for all X written by T
 - Allow waiting transactions to proceed
- Rule 4: Abort transaction T
 - Check if the waiting transactions can proceed now.

Summary

Transaction wants to READ element X If WT(X) > TS(T) then ROLLBACK Else If C(X) = false, then WAIT Else READ and update RT(X) to larger of TS(T) or RT(X)

Transaction wants to WRITE element X If RT(X) > TS(T) then ROLLBACK Else if WT(X) > TS(T) Then If C(X) = false then WAIT else IGNORE write (Thomas Write Rule) Otherwise, WRITE, and update WT(X)=TS(T), C(X)=false

Timestamp-based Concurrency Control

Two transactions get started.

• $S_{tart}(T_1) \rightarrow S_{tart}(T_2)$

What will happen at the last request?

• $S_{tart}(T_1) \rightarrow S_{tart}(T_2) \rightarrow R_{T_1}(A) \rightarrow R_{T_2}(A) \rightarrow W_{T_1}(B) \rightarrow W_{T_2}(B)$

What will happen at the last request?

• $S_{tart}(T_1) \rightarrow S_{tart}(T_2) \rightarrow R_{T_1}(A) \rightarrow R_{T_2}(A) \rightarrow W_{T_1}(B) \rightarrow W_{T_2}(B) - ACCEPTED$

What will happen at the last request?

• Start(T_1) -> Start(T_2) -> $R_{T1}(A)$ -> $R_{T2}(A)$ -> $W_{T1}(B)$ -> $W_{T2}(B)$ - ACCEPTED

• $S_{tart}(T_1) \rightarrow S_{tart}(T_2) \rightarrow R_{T2}(A) \rightarrow C_{ommit}_{T2} \rightarrow R_{T1}(A) \rightarrow W_{T1}(A)$

What will happen at the last request?

• Start(T_1) -> Start(T_2) -> $R_{T1}(A)$ -> $R_{T2}(A)$ -> $W_{T1}(B)$ -> $W_{T2}(B)$ - ACCEPTED

• Start(T_1) -> Start(T_2) -> $R_{T2}(A)$ -> C_{ommit}_{T2} -> $R_{T1}(A)$ -> $W_{T1}(A)$ - ABORT T_1 because $R_{T2}(A)$ precedes

Problem 2: Timestamp-based Concurrency Control

T1	T2	Т3	T4	X	Y	Z
1	2	3	4		RT = 0, WT = 0, C = 1	
R ₁ (X)						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)					

T1	T2	Т3	T4	X	Y	Z			
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1			
R ₁ (X)				RT=1					
	R ₂ (X)								
		TS(T ₁	 Physically realizable: TS(T₁) >= WT(X) C = 1: grant request 						
			3. Update RT : TS(T_1) > RT(X)						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)					

T1	T2	Т3	T4	X	Y	Z	
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	
R ₁ (X)				RT=1			
	R ₂ (X)			RT=2			
	W ₂ (X)						
			ysically rea				
		TS(T ₂	$\underline{P} >= WT(X)$				
		2. C =	= 1: grant re	equest			
			0				
		3. Update WT					

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X)						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT =	RT = 0, WT	RT = 0, WT
				0, C = 1	= 0, C = 1	= 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
1.	NOT Physi	cally realiza	able:			
	1. NOT Physically realizable: TS(T ₁) < RT(X)					
, i						
Ab	ort/rollba	ck				

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)				

T1	T2	Т3	T4	X	Y	Z			
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1			
R ₁ (X)				RT=1					
	R ₂ (X)			RT=2					
	W ₂ (X)			WT=2, C=0					
W ₁ (X): abort									
		W ₃ (Y)			WT=3, C=0				
			1. Physically realizable: TS(T₃) >= RT(Y) and TS(T ₃) >= WT(Y)						
		2. Updat	te WT and	l C (not com	mitted yet)				

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y)					

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					

 Physically realizable: TS(T₂) >= RT(Y) although TS(T₂) < WT(Y)
 We could not apply Thomas' write rule (ignore W₂(Y)) since C=0

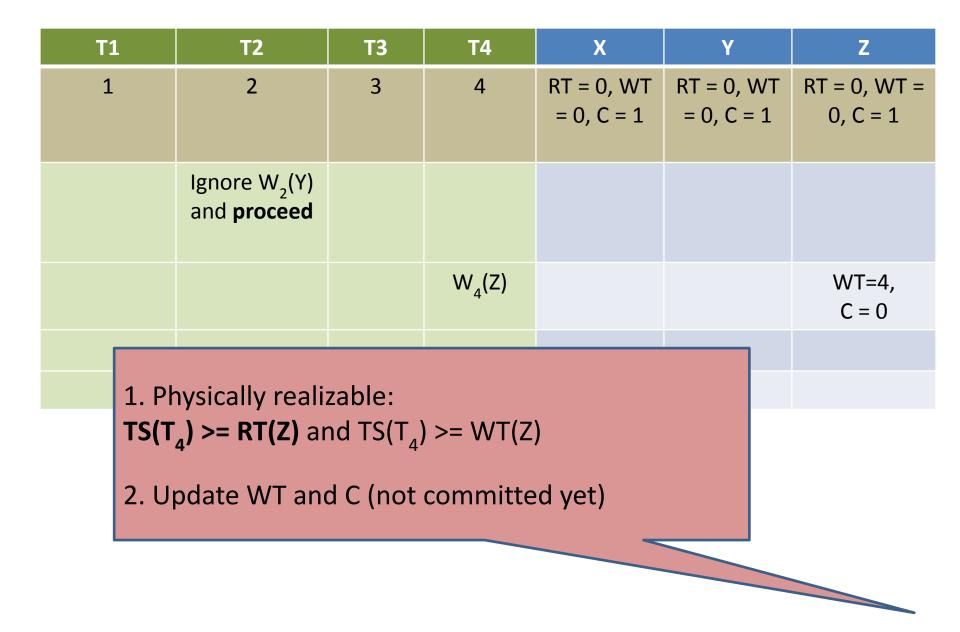
T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT =	RT = 0, WT	RT = 0, WT
				0, C = 1	= 0, C = 1	= 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					
		C ₃				

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					
		C ₃			C=1	

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					
		C ₃			C=1	
	1					
			_			
A later write by T ₃ has been						
	committed!					

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R ₁ (X)				RT=1		
	R ₂ (X)			RT=2		
	W ₂ (X)			WT=2, C=0		
W ₁ (X): abort						
		W ₃ (Y)			WT=3, C=0	
	W ₂ (Y): delay					
		C ₃			C=1	
	Ignore W ₂ (Y) and proceed					

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	lgnore W ₂ (Y) and proceed					
			W ₄ (Z)			



T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	Ignore W ₂ (Y) and proceed					
			W ₄ (Z)			WT=4, C = 0
			C ₄			C=1

T1	Т2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	lgnore W ₂ (Y) and proceed					
			W ₄ (Z)			WT=4, C = 0
			C ₄			C=1
	R ₂ (Z)					

1. NOT Physically realizable:	T4	X	Y	Z
TS(T ₂) < WT(Z) Abort/rollback	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
d proceed				
	W ₄ (Z)			WT=4, C = 0
	C ₄			C=1
R ₂ (Z): abort				

Questions?

Multiversion Concurrency Control

- Maintains old versions of database elements in addition the current version in the database itself.
- The idea is to allow reads that would otherwise result in an abort (as the current version was written by future transaction)

Problem with Timestamp-Based Scheduling

	T1	Т2	Т3	T4	Α
	150	200	175	225	RT = 0 WT = 0
	R ₁ (A)				RT = 150
	W ₁ (A)				WT = 150
		R ₂ (A)			RT = 200
		W ₂ (A)			WT = 200
			R ₃ (A)		
			Abort		
to al	bort because			R ₄ (A)	RT = 225
A) is	greater than timestamp		ad acces	ve been us s to an old (from 150)	version

Had

WΤ(

m\

Multiversion Timestamps

	T1	T2	Т3	T4	A ₀	А ₁₅₀	A ₂₂₅	
	150	200	175	225	RT = 0 WT = 0			
	R ₁ (A)				RT = 150			
	W ₁ (A)					Create		
		$R_2(A)$				RT=200		
		W ₂ (A)					Create	
			R ₃ (A)			RT=175		
				$R_4(A)$			RT=225	
Don't have to abort Just read a previous value of A								