## CSE 444: Database Internals

Section 6: Transactions

## Today

- Serializability and Conflict Serializability
  - Precedence graph
- Two-Phase Locking
  - Strict two phase locking
- Lab 3 Intro

## Problem 1: Serializability and

Locking

wnat is
• Serializability

• Is this schedule conflict serializab

Conflict Serializability?

T <sub>0</sub>	T <sub>1</sub>
R <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A)
	R <sub>1</sub> (B)
	C <sub>1</sub>
R <sub>0</sub> (B)	
W <sub>0</sub> (B)	
C <sub>0</sub>	

# Review: (Conflict) Serializable Schedule

• A schedule is <u>serializable</u> if it is equivalent to a serial schedule

 A schedule is <u>conflict serializable</u> if it can be transformed into a serial schedule by a series of swappings of adjacent non-conflicting actions

#### Example:

$$r_1(A)$$
;  $w_1(A)$ ;  $r_2(A)$ ;  $w_2(A)$ ;  $r_1(B)$ ;  $w_1(B)$ ;  $r_2(B)$ ;  $w_2(B)$ 

$$r_1(A)$$
;  $w_1(A)$ ;  $r_2(A)$ ;  $r_1(B)$ ;  $w_2(A)$ ;  $w_1(B)$ ;  $r_2(B)$ ;  $w_2(B)$ 

$$r_1(A)$$
;  $w_1(A)$ ;  $r_1(B)$ ;  $r_2(A)$ ;  $w_2(A)$ ;  $w_1(B)$ ;  $r_2(B)$ ;  $w_2(B)$ 



. . .

 $r_1(A)$ ;  $w_1(A)$ ;  $r_1(B)$ ;  $w_1(B)$ ;  $r_2(A)$ ;  $w_2(A)$ ;  $r_2(B)$ ;  $w_2(B)$ 

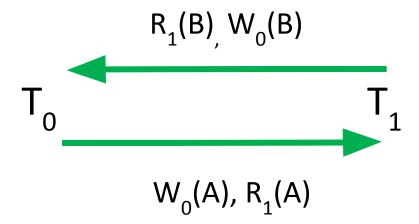
# Problem 1: Serializability and Locking

• Is this schedule conflict serializable?

T <sub>o</sub>	<b>T</b> <sub>1</sub>
R <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A)
	R <sub>1</sub> (B)
	C <sub>1</sub>
R <sub>0</sub> (B)	
W <sub>0</sub> (B)	
C <sub>0</sub>	

• No.

• The **precedence graph** contains a cycle



• So, use 2PL ...

☐ Original schedule below

T <sub>o</sub>	<b>T</b> <sub>1</sub>
R <sub>0</sub> (A) W <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A) R <sub>1</sub> (B)
	R <sub>1</sub> (B)
	C <sub>1</sub>
R <sub>0</sub> (B)	
R <sub>0</sub> (B) W <sub>0</sub> (B)	
C <sub>0</sub>	

- So, use 2PL ...
  - Original schedule below

#### What is

- Two Phase Locking
- Strict Two Phase Locking?

T <sub>0</sub>	T <sub>1</sub>
R <sub>0</sub> (A) W <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A)
	R <sub>1</sub> (A) R <sub>1</sub> (B)
	$C_{_{1}}$
R <sub>0</sub> (B)	
R <sub>0</sub> (B) W <sub>0</sub> (B) C <sub>0</sub>	
C <sub>0</sub>	

# Review: (Strict) Two Phase Locking (2PL)

#### The 2PL rule:

In every transaction, all lock requests must precede all unlock requests

#### Strict 2PL:

All locks held by a transaction are released when the transaction is completed

- Ensures that schedules are recoverable
  - Transactions commit only after all transactions whose changes they read also commit
- Avoids <u>cascading rollbacks</u>

- How can 2PL can ensure a conflict-serializable schedule?
  - ☐ Original schedule below

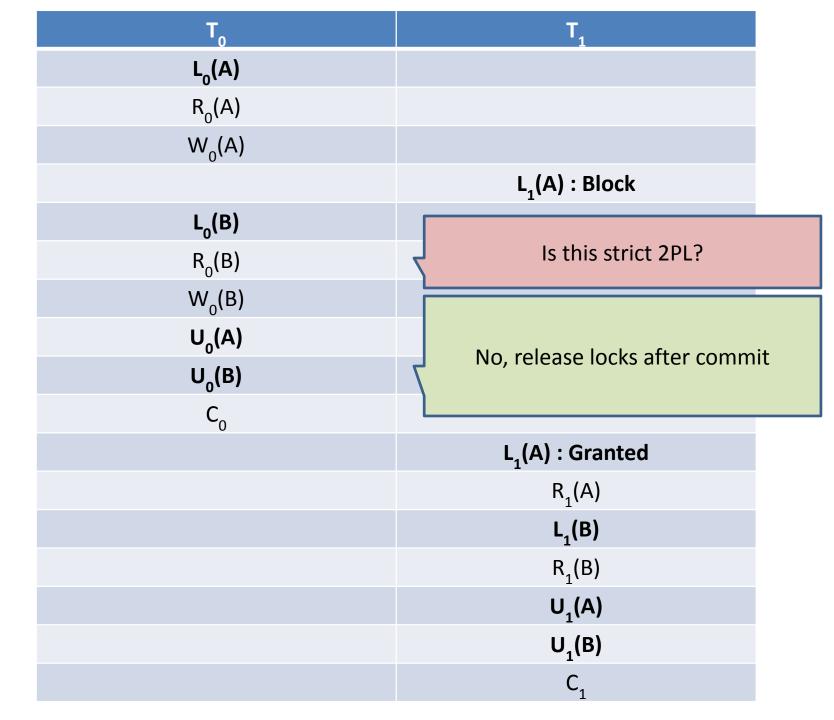
T <sub>0</sub>	T <sub>1</sub>
R <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A)
	R <sub>1</sub> (B)
	C <sub>1</sub>
R <sub>0</sub> (B)	
W <sub>0</sub> (B)	
$C_0$	

T <sub>1</sub>

T <sub>0</sub>	$T_1$
L <sub>o</sub> (A)	
<b>L<sub>0</sub>(A)</b> R <sub>0</sub> (A) W <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	L <sub>1</sub> (A) : Block

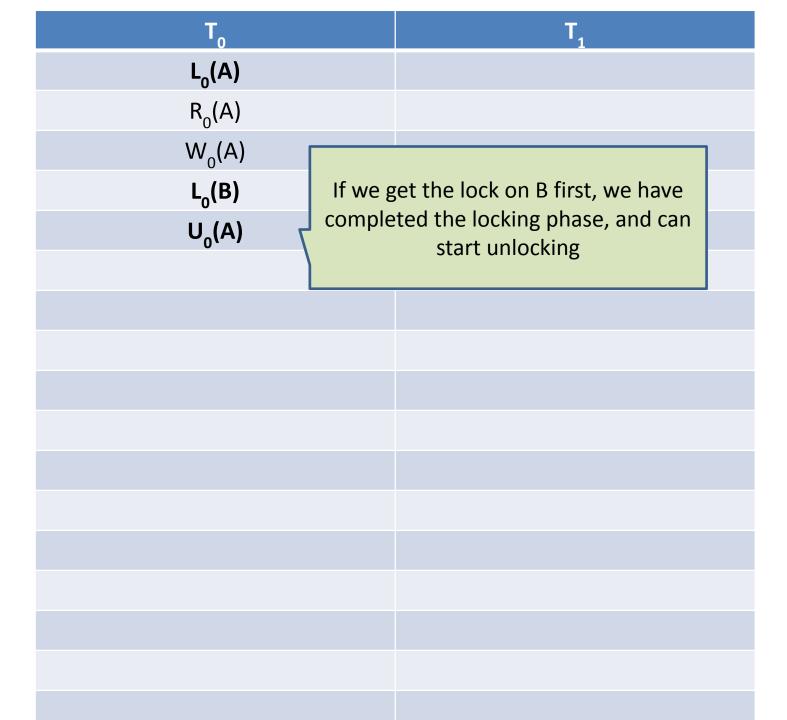
T <sub>o</sub>	$T_1$
L <sub>o</sub> (A)	
L <sub>0</sub> (A)  R <sub>0</sub> (A)  W <sub>0</sub> (A)	
W <sub>o</sub> (A)	
	L <sub>1</sub> (A) : Block
L <sub>o</sub> (B)	
R <sub>0</sub> (B)	
W <sub>o</sub> (B)	
U <sub>o</sub> (A)	
<b>U<sub>0</sub>(B)</b> C <sub>0</sub>	
$C_{o}$	

$T_0$	$T_1$
L <sub>o</sub> (A)	
R <sub>0</sub> (A)	
W <sub>o</sub> (A)	
	L <sub>1</sub> (A) : Block
L <sub>o</sub> (B)	
R <sub>0</sub> (B)	
W <sub>0</sub> (B)	
U <sub>o</sub> (A)	
U <sub>o</sub> (B)	
$C_{0}$	
	L <sub>1</sub> (A): Granted
	R <sub>1</sub> (A)
	L <sub>1</sub> (B)
	R <sub>1</sub> (B)
	U <sub>1</sub> (A)
	U <sub>1</sub> (A) U <sub>1</sub> (B)
	C <sub>1</sub>



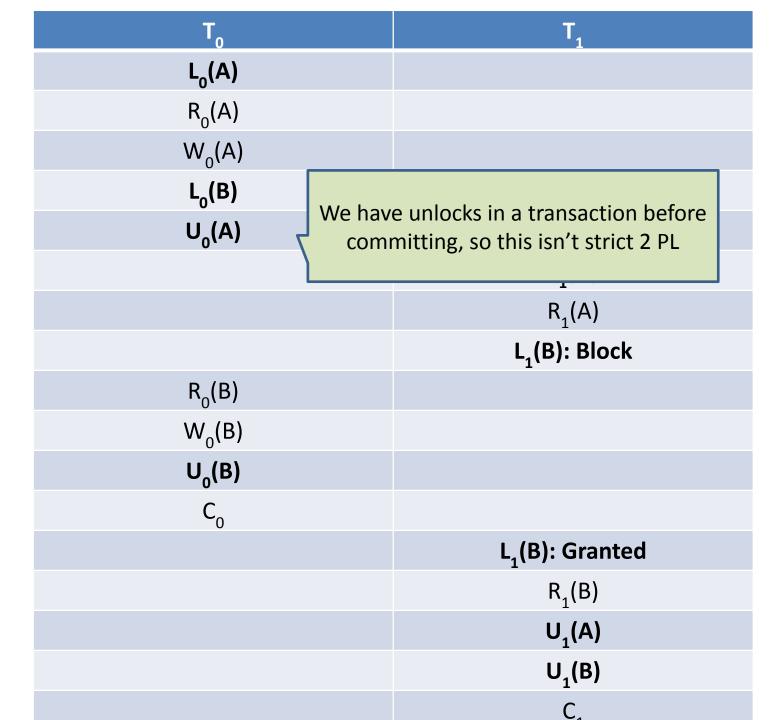
- That example ended in a serial schedule, 2PL doesn't necessarily require that
  - ☐ Here's an example that doesn't become serial

T <sub>0</sub>	T <sub>1</sub>
R <sub>0</sub> (A) W <sub>0</sub> (A)	
W <sub>0</sub> (A)	
	R <sub>1</sub> (A)
	R <sub>1</sub> (A) R <sub>1</sub> (B)
	C <sub>1</sub>
R <sub>0</sub> (B)	
W <sub>0</sub> (B)	
R <sub>0</sub> (B)  W <sub>0</sub> (B)  C <sub>0</sub>	



T <sub>0</sub>	T <sub>1</sub>
L <sub>o</sub> (A)	
R <sub>o</sub> (A)	
R <sub>0</sub> (A) W <sub>0</sub> (A)	
L <sub>o</sub> (B)	
U <sub>0</sub> (A)	
Allowing for interleaving	can L <sub>1</sub> (A)
help performance	$R_1(A)$
	L <sub>1</sub> (B): Block

T <sub>0</sub>	T <sub>1</sub>
L <sub>o</sub> (A)	
R <sub>o</sub> (A) W <sub>o</sub> (A)	
W <sub>o</sub> (A)	
L <sub>o</sub> (B)	
U <sub>o</sub> (A)	
	L <sub>1</sub> (A)
	R <sub>1</sub> (A)
	L <sub>1</sub> (B): Block
R <sub>o</sub> (B)	
W <sub>o</sub> (B)	
U <sub>o</sub> (B)	
$C_0$	



## Common 2PL Misconceptions

- Remember, only one transaction can hold a lock for an element at once
- In 2PL, within each transaction there must be a growing (lock acquiring phase) followed by a shrinking (unlocking) phase
  - Before unlocking, all locks must be be granted, not just requested
- Both 2PL and Strict 2PL ensure conflict serializability, neither guarantee a serial schedule or prevent deadlocks

## Lab 3 - Transactions

- NO STEAL / FORCE buffer management policy
- o you shouldn't evict dirty (updated) pages from the buffer pool if they are locked by an uncommitted transaction. (this is NO STEAL)
- on transaction commit, you should force dirty pages to disk (e.g., write the pages out) (this is **FORCE**)
- Recommend locking at page level
- you can acquire and release locks in <u>BufferPool.getPageO</u>, instead of adding calls to each of your operators
- Might have to change previous implementations to access pages using <u>BufferPool.getPage()</u>

- You need to implement <u>shared</u> and <u>exclusive</u> locks
  - o Before read, it must have a shared lock or exclusive lock
  - o Before write, it must have an exclusive lock
  - o Multiple transactions can have a shared lock
  - o Only one transaction may have an exclusive lock on an object
- o If transaction t is the <u>only transaction holding a shared lock</u> on an object o, t <u>may upgrade</u> its lock on o to an exclusive lock
  - You need to implement <u>strict two-phase locking</u>
  - o transactions should acquire the appropriate type of lock on any object before accessing that object
  - transaction shouldn't release any locks until after the transaction commits.

- You will need to implement a <u>LockManager class</u> that will hold data structures to keep track of which locks each transaction holds and that check to see if a lock should be granted to a transaction when it is requested.
- Read about <u>Synchronization</u> in Java, and use the <u>synchronized</u> keyword in appropriate places in <u>LockManager</u>
- You will have to also throw appropriate exceptions like

#### TransactionAbortedException

#### Synchronized method:

Only one thread executing that method per instance

```
public synchronized void releaseLock()
```

```
public void acquireLock() {
    // wait for lock
    synchronized (this) {
        // update state
    }
}
```

#### Synchronized block:

Only one thread executing that block of code at one time

Usually used to update that object's state synchronously

#### Handling deadlocks

- o implement a simple timeout policy that aborts a transaction if it has not completed after a given period of time
- o implement a cycle-detection in a dependency graph data structure, if cycle exists when granting a new lock abort something.

#### • Design Choices:

- Locking Granularity: page-level vs tuple-level (our tests assume page-level)
- Deadlock Detection: timeout vs dependency graphs
- Deadlock Resolution: aborting yourself vs aborting others
- Read the spec carefully for more details about various methods and edge cases.

# Problem 2: Timestamp-based Concurrency Control

## Timestamp-based Concurrency Control

- Some transaction, T.
- Some element (tuple/page), X.
- TS(T) timestamp for transaction T
  - Stays constant for all of T's operations
- WT(X) latest write timestamp for X
  - Set WT(X) = TS(T)
- RT(X) latest read timestamp for X
  - Set RT(X) = TS(T)
- C(X) X's value has been committed
  - 1 if true, 0 if not

## Timestamp-based Concurrency Control

#### Actions for transaction T

- Grant a read/write request for a transaction
- Abort (in case T violates physical reality late actions)
- Delay (make the Grant or Abort decision later)
  - When writing, the change is always tentative until we decide to commit. For this, we use a commit bit C to keep track if the transaction that last wrote X has committed
- Ignore Thomas Write Rule ignore outdated writes

## Timestamp-based Concurrency Control - Four Rules

Rule 1: Read request on X by T

START(T) ... START(U) ... 
$$w_U(X)$$
 ...  $r_T(X)$ 

— TS(T) < WT(X), abort, (read too late)</p>

- -TS(T) >= WT(X), physically realizable
  - If C = 1, grant, update RT(X)
  - If C = 0, **delay** T

## Timestamp-based Concurrency Control - Four Rules

Rule 2: Write request on X by T

```
START(T) ... START(U) ... r_U(X) ... w_T(X)
```

- TS(T) < RT(X) (write too late)</p>
  - Abort

- -TS(T) >= RT(X), physically realizable
  - TS(T) >= WT(X)
    - then grant, update WT(X), set C = 0 (as it's not committed yet)

START(T) ... START(V) ... 
$$w_{V}(X)$$
 ...  $w_{T}(X)$ 

- TS(T) < WT(X)
  - If C = 1, don't write X at all! (Thomas Write Rule ignore outdated writes)
  - If C = 0, delay

## Timestamp-based Concurrency Control - Four Rules

- Rule 3: Commit request by T
  - Set C = 1 for all X written by T
  - Allow waiting transactions to proceed
- Rule 4: Abort transaction T
  - Check if the waiting transactions can proceed now.

## Summary

```
Transaction wants to READ element X

If WT(X) > TS(T) then ROLLBACK

Else If C(X) = false, then WAIT

Else READ and update RT(X) to larger of TS(T) or RT(X)
```

```
Transaction wants to WRITE element X

If RT(X) > TS(T) then ROLLBACK

Else if WT(X) > TS(T)

Then If C(X) = false then WAIT

else IGNORE write (Thomas Write Rule)

Otherwise, WRITE, and update WT(X)=TS(T), C(X)=false
```

# Timestamp-based Concurrency Control

Two transactions get started.

•  $Start(T_1) \rightarrow Start(T_2)$ 

• 
$$S_{tart}(T_1) \rightarrow S_{tart}(T_2) \rightarrow R_{T_1}(A) \rightarrow R_{T_2}(A) \rightarrow W_{T_1}(B) \rightarrow W_{T_2}(B)$$

- $S_{tart}(T_1) -> S_{tart}(T_2) -> R_{T_1}(A) -> R_{T_2}(A) -> W_{T_1}(B) -> W_{T_2}(B)$ 
  - ACCEPTED

- Start( $T_1$ ) -> Start( $T_2$ ) ->  $R_{T1}(A)$  ->  $R_{T2}(A)$  ->  $W_{T1}(B)$  ->  $W_{T2}(B)$ 
  - ACCEPTED

• 
$$Start(T_1) -> Start(T_2) -> R_{T2}(A) -> C_{ommit_{T2}} -> R_{T1}(A) -> W_{T1}(A)$$

- $S_{tart}(T_1) -> S_{tart}(T_2) -> R_{T_1}(A) -> R_{T_2}(A) -> W_{T_1}(B) -> W_{T_2}(B)$ 
  - ACCEPTED

- Start( $T_1$ ) -> Start( $T_2$ ) ->  $R_{T2}(A)$  ->  $R_{T1}(A)$  ->  $R_{T1}(A)$ 
  - ABORT  $T_1$  because  $R_{T_2}(A)$  precedes

# Problem 2: Timestamp-based Concurrency Control

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1		
R <sub>1</sub> (X)						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)					

T1	T2	Т3	T4	X	Υ	Z			
1	2	3	4	RT = 0, WT	RT = 0, WT	RT = 0, WT			
				= 0, C = 1	= 0, C = 1	= 0, C = 1			
R <sub>1</sub> (X)				RT=1					
	R <sub>2</sub> (X)								
		1. Ph	1. Physically realizable:						
		TS(T <sub>1</sub>	) >= WT(X)						
		2. C =	: 1: grant re	equest					
		3. Up	date RT : T	$S(T_1) > RT()$	<b>(</b> )				
				\ 1'	,				

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	$R_2(X)$ $W_2(X)$			RT=2		
	W <sub>2</sub> (X)					

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)					
			ysically rea			
		TS(T <sub>2</sub>	(2) >= WT(X)			
		2 C =	= 1: grant re	eallest		
		2.0	- 1. grant n	equest —		
		3. Up	odate WT			

T1	T2	Т3	T4	Х	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X)						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						
1. ľ	<b>NOT</b> Physi					
$TS(T_1) < RT(X)$						
	-					
Ab	ort/rollba	ck				

T1	T2	Т3	T4	Х	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)				

T1	T2	Т3	T4	Х	Υ	Z		
1	2	3	4	RT = 0, WT =	RT = 0, WT	RT = 0, WT		
				0, C = 1	= 0, C = 1	= 0, C = 1		
R <sub>1</sub> (X)				RT=1				
	R <sub>2</sub> (X)			RT=2				
	$W_2(X)$			WT=2, C=0				
W <sub>1</sub> (X): abort								
		W <sub>3</sub> (Y)			WT=3, C=0			
1. Physically realizable:								

 $TS(T_3) >= RT(Y)$  and  $TS(T_3) >= WT(Y)$ 

2. Update WT and C (not committed yet)

T1	T2	Т3	T4	Х	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	$W_2(X)$			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	$W_2(Y)$					

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): delay					

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): delay					

#### 1. Physically realizable:

 $TS(T_2) >= RT(Y)$  although  $TS(T_2) < WT(Y)$ 

2. We could not apply Thomas' write rule (ignore W<sub>2</sub>(Y)) since C=0

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	W <sub>2</sub> (X)			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): delay					
		C <sub>3</sub>				

T1	T2	Т3	T4	Х	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	$W_2(X)$			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): delay					
		C <sub>3</sub>			C=1	

T1	T2	Т3	T4	Х	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	$W_2(X)$			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): <b>delay</b>					
		C <sub>3</sub>			C=1	
	A later write by T <sub>3</sub> has been committed!					

T1	T2	Т3	T4	Х	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
R <sub>1</sub> (X)				RT=1		
	R <sub>2</sub> (X)			RT=2		
	$W_2(X)$			WT=2, C=0		
W <sub>1</sub> (X): abort						
		W <sub>3</sub> (Y)			WT=3, C=0	
	W <sub>2</sub> (Y): <b>delay</b>					
		C <sub>3</sub>			C=1	
	Ignore W <sub>2</sub> (Y) and proceed					

T1	T2	Т3	T4	X	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	Ignore W <sub>2</sub> (Y) and <b>proceed</b>					
			W <sub>4</sub> (Z)			

T1	T2	Т3	T4	X	Y	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	Ignore W <sub>2</sub> (Y) and <b>proceed</b>					
			W <sub>4</sub> (Z)			WT=4, C = 0

1. Physically realizable:

$$TS(T_4) >= RT(Z)$$
 and  $TS(T_4) >= WT(Z)$ 

2. Update WT and C (not committed yet)

T1	T2	Т3	T4	X	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	Ignore W <sub>2</sub> (Y) and <b>proceed</b>					
			W <sub>4</sub> (Z)			WT=4, C = 0
			C <sub>4</sub>			C=1

T1	T2	Т3	T4	X	Υ	Z
1	2	3	4	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1	RT = 0, WT = 0, C = 1
	Ignore W <sub>2</sub> (Y) and <b>proceed</b>					
			W <sub>4</sub> (Z)			WT=4, C = 0
			C <sub>4</sub>			C=1
	R <sub>2</sub> (Z)					

1. <b>NOT</b> Physically realizable:	T4	X	Y	Z
$TS(T_2) < WT(Z)$	4	RT = 0, WT	RT = 0, WT	RT = 0, WT =
Abort/rollback		= 0, C = 1	= 0, C = 1	0, C = 1
d <b>proceed</b>				
	W <sub>4</sub> (Z)			WT=4, C = 0
	C <sub>4</sub>			C=1
R <sub>2</sub> (Z): <b>abort</b>				

Questions?

## Multiversion Concurrency Control

- Maintains old versions of database elements in addition the current version in the database itself.
- The idea is to allow reads that would otherwise result in an abort (as the current version was written by future transaction)

# Problem with Timestamp-Based Scheduling

T1	T2	Т3	T4	A
150	200	175	225	RT = 0 WT = 0
R <sub>1</sub> (A)				RT = 150
W <sub>1</sub> (A)				WT = 150
	R <sub>2</sub> (A)			RT = 200
	W <sub>2</sub> (A)			WT = 200
		$R_3(A)$		
		Abort		
hort hacausa			$R_4(A)$	RT = 225

Had to abort because WT(A) is greater than my own timestamp

Would have been useful if I had access to an old version of A (from 150)...

### **Multiversion Timestamps**

T1	T2	Т3	T4	A <sub>0</sub>	A <sub>150</sub>	A <sub>225</sub>
150	200	175	225	RT = 0 WT = 0		
R <sub>1</sub> (A)				RT = 150		
W <sub>1</sub> (A)					Create	
	R <sub>2</sub> (A)				RT=200	
	$R_2(A)$ $W_2(A)$					Create
		$R_3(A)$			RT=175	
			$R_4(A)$			RT=225

Don't have to abort

Just read a previous value of A