Computer Systems

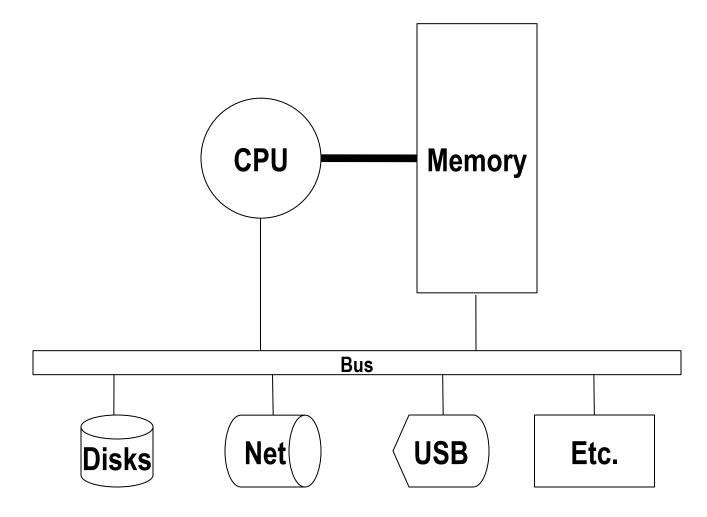
CSE 410 Spring 2012

2 – Memory and its Data

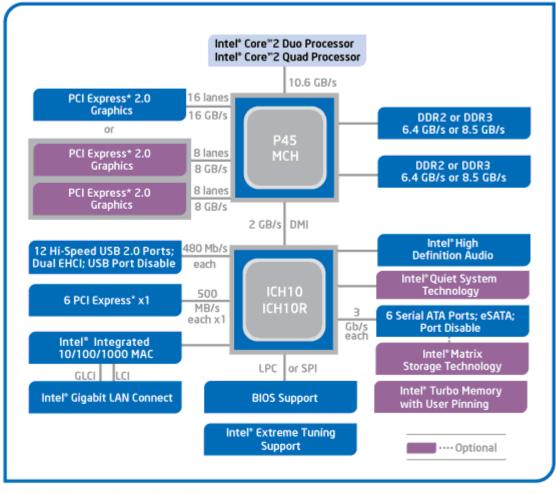
Today's (and Friday's) topics

- Memory and its bits, bytes, and integers
- Representing information as bits
- Bit-level manipulations
 - Boolean algebra
 - Boolean algebra in C
- Reading: Bryant/O'Hallaron sec. 2.1

Hardware: Logical View

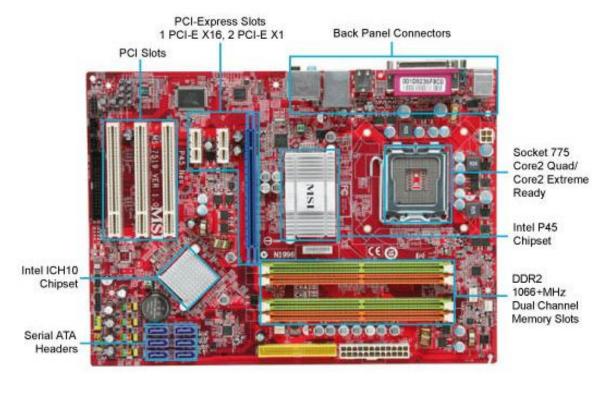


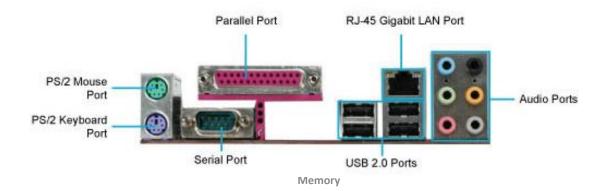
Hardware: Semi-Logical View



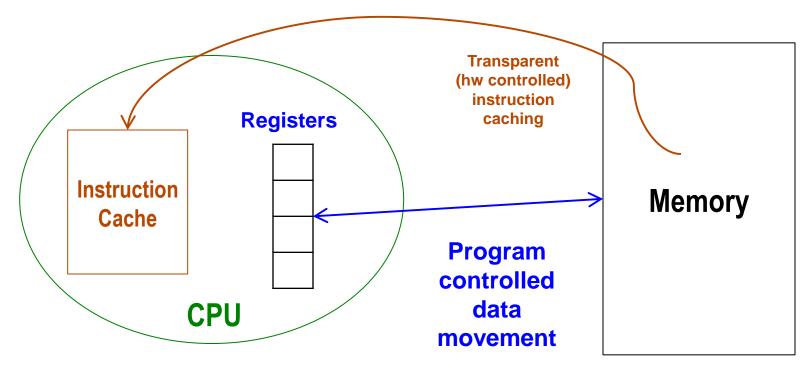
Intel® P45 Express Chipset Block Diagram

Hardware: Physical View





CPU "Memory": Registers and Instruction Cache



- There are a fixed number of <u>registers</u> in the CPU
 - Registers hold data
- There is an <u>I-cache</u> in the CPU that holds recently fetched instructions
 - If you execute a loop that fits in the cache, the CPU goes to memory for those instructions only once, then executes it out of its cache
- This slide is just an introduction.
 We'll see a fuller explanation later in the course.

Performance: It's Not Just CPU Speed

Data and instructions reside in memory

- To execute an instruction, it must be fetched into the CPU
- Next, the data the instruction operates on must be fetched into the CPU

■ CPU – Memory bandwidth can limit performance

- Improving performance 1: hardware improvements to increase memory bandwidth (e.g., DDR → DDR2 → DDR3)
- Improving performance 2: move less data into/out of the CPU
 - Put some "memory" in the CPU chip itself (this is "cache" memory)

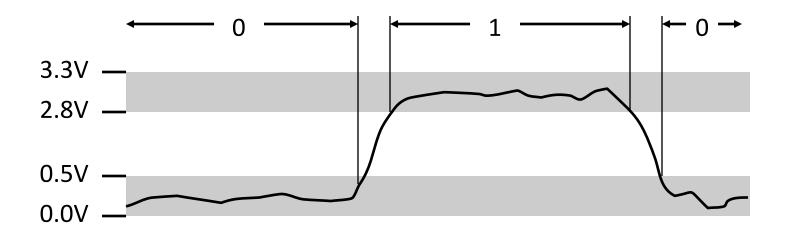
Binary Representations

Base 2 number representation

Represent 351₁₀ as 0000000101011111₂ or 101011111₂

Electronic implementation

- Easy to store with bi-stable elements
- Reliably transmitted on noisy and inaccurate wires



Encoding Byte Values

Binary

- $00000000_2 11111111_2$
- Byte = 8 bits (binary digits)
- Decimal

0₁₀ -- **255**₁₀

Hexadecimal

- 00₁₆ -- FF₁₆
- Byte = 2 hexadecimal (hex) or base 16 digits
- Base-16 number representation
- Use characters '0' to '9' and 'A' to 'F'
- Write FA1D37B₁₆ in C
 - as 0xFA1D37B or 0xfa1d37b

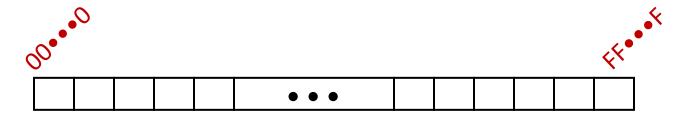
Hex Deciman

0	0	0000
1	1	0001
2	2	0010
3	ന	0011
4	4	0100
5	5	0101
6	6	0110
7	7	0111
8	8	1000
9	9	1001
Α	10	1010
В	11	1011
C	12	1100
D	13	1101
1 2 3 4 5 6 7 8 9 A B C D	0 1 3 4 5 6 7 8 9 10 11 12 13 14 15	0000 0001 0010 0011 0100 0101 0110 0111 1000 1001 1011 1100 1111
F	15	1111

What is memory, really?

■ How do we find data in memory?

Byte-Oriented Memory Organization



Programs refer to addresses

- Conceptually, a very large array of bytes
- System provides an <u>address space</u> private to each "process"
 - Process = program being executed + its data + its "state"
 - Program can clobber its own data, but not that of others
 - Clobbering code or "state" often leads to crashes (or security holes)

Compiler + run-time system control memory allocation

- Where different program objects should be stored
- All allocation within a single address space

Machine Words

Machine has a "word size"

- Nominal size of integer-valued data
 - Including addresses
- Until recently, most machines used 32 bits (4 bytes) words
 - Limits addresses to 4GB
 - Became too small for memory-intensive applications
- More recent and high-end systems use 64 bits (8 bytes) words
 - Potential address space $\approx 1.8 \times 10^{19}$ bytes (18 EB exabytes)
 - x86-64 supports 48-bit physical addresses: 256 TB (terabytes)
- Machines support multiple data formats
 - Fractions or multiples of word size
 - Always integral (actually power of 2) number of bytes: 1, 2, 4, 8, ...

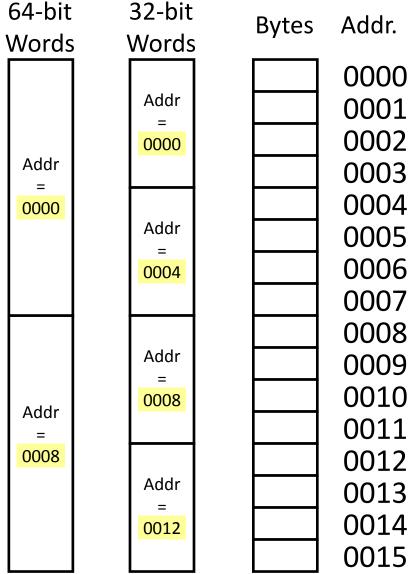
Word-Oriented Memory Organization

- Addresses specify locations of bytes in memory
 - Address of first byte in word
 - Addresses of successive words differ by 4 (32-bit) or 8 (64-bit)
 - Address of word 0, 1, .. 10?

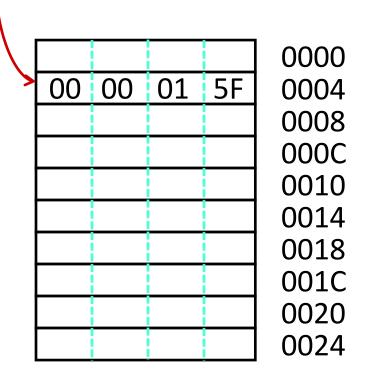
64-bit 32-bit Words Words Addr ?? Addr ?? Addr ?? Addr ?? Addr ?? Addr ??

Word-Oriented Memory Organization

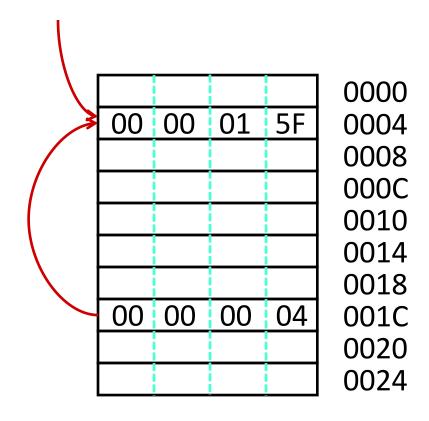
- Addresses specify locations of bytes in memory
 - Address of first byte in word
 - Addresses of successive words differ by 4 (32-bit) or 8 (64-bit)
 - Address of word 0, 1, .. 10?



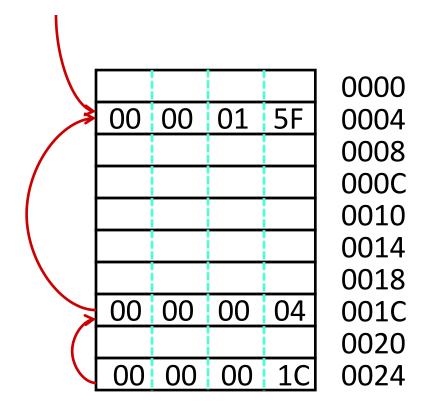
- Address is a *location* in memory
- Pointer is a data object that contains an address
- Address 0004
 stores the value 351 (or 15F₁₆)



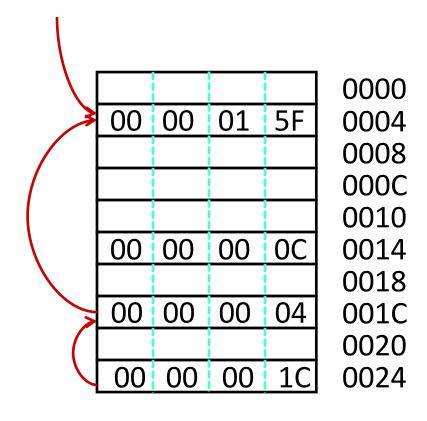
- Address is a *location* in memory
- Pointer is a data object that contains an address
- Address 0004 stores the value 351 (or 15F₁₆)
- Pointer to address 0004 stored at address 001C



- Address is a *location* in memory
- Pointer is a data object that contains an address
- Address 0004 stores the value 351 (or 15F₁₆)
- Pointer to address 0004 stored at address 001C
- Pointer to a pointer in 0024



- Address is a *location* in memory
- Pointer is a data object that contains an address
- Address 0004
 stores the value 351 (or 15F₁₆)
- Pointer to address 0004 stored at address 001C
- Pointer to a pointer in 0024
- Address 0014 stores the value 12
 - Is it a pointer?



Data Representations

Sizes of objects (in bytes)

Java data type	C data type	Typical 32-bit	x86-64
boolean	bool	1	1
byte	char	1	1
char		2	2
short	short int	2	2
• int	int	4	4
float	float	4	4
•	long int	4	8
double	double	8	8
long	long long	8	8
•	long double	8	16
(reference)	pointer *	4	8

Byte Ordering

- How should bytes within multi-byte word be ordered in memory?
 - Peanut butter or chocolate first?
- Say you want to store 0xaabbccdd
 - What order will the bytes be stored?

Byte Ordering

- How should bytes within multi-byte word be ordered in memory?
 - Peanut butter or chocolate first?
- Say you want to store 0xaabbccdd
 - What order will the bytes be stored?

Conventions!

- Big-endian, Little-endian
- Based on "Gulliver's Travels"
 - tribes cut eggs on different sides (big, little)

Byte Ordering Example

- Big-Endian (PowerPC, Sun, Internet)
 - Least significant byte has highest address
- Little-Endian (x86)
 - Least significant byte has lowest address
- Example
 - Variable has 4-byte representation 0x01234567
 - Address of variable is 0×100

		0x100	0x101	0x102	0x103		
Big Endian		01	23	45	67		
		0x100	0x101	0x102	0x103	_	
Little Endian		67	45	23	01		

Reading Byte-Reversed Listings

Disassembly

- Text representation of binary machine code
- Generated by program that reads the machine code

Example instruction in memory

add value 0x12ab to register 'ebx' (a special location in CPU's memory)

Address Instruction Code Assembly Rendition

8048366: 81 c3 ab 12 00 00 add \$0x12ab,%ebx

Reading Byte-Reversed Listings

- Disassembly
 - Text representation of binary machine code
 - Generated by program that reads the machine code
- Example instruction in memory
 - add value 0x12ab to register 'ebx' (a special location in CPU's memory)

Address Instruction Code 8048366: 81 c3 ab 12 00 00 add \$0x12ab,%ebx

Deciphering numbers

Value:

■ Pad to 32 bits:

Split into bytes:

Reverse (little-endian):

0x12ab

0x000012ab

00 00 12 ab

ab 12 00 00

Addresses and Pointers in C

& = 'address of value'
* = 'value at address'
 or 'de-reference'

Pointer declarations use *

*(&x) is equivalent to ??

- int * ptr; int x, y; ptr = &x;
- Declares a variable ptr that is a pointer to a data item that is an integer
- Declares integer values named x and y
- Assigns ptr to point to the address where x is stored

We can do arithmetic on pointers

- ptr = ptr + 1; // really adds 4 (because an integer uses 4 bytes?)
- Changes the value of the pointer so that it now points to the next data item in memory (that may be y, or it may not – this is <u>dangerous</u>!)

■ To use the value pointed to by a pointer we use de-reference

- y = *ptr + 1; is the same as y = x + 1;
- But, if ptr = &y then y = *ptr + 1; is the same as y = y + 1;
- *ptr is the value stored at the location to which the pointer ptr is pointing

Arrays

- Arrays represent adjacent locations in memory storing the same type of data object
 - e.g., int big_array[128];
 allocated 512 adjacent locations in memory starting at 0x00ff0000
- Pointers to arrays point to a certain type of object

```
e.g., int * array_ptr;
array_ptr = big_array;
array_ptr = &big_array[0];
array_ptr = &big_array[3];
array_ptr = &big_array[0] + 3;
array_ptr = big_array + 3;
*array_ptr = *array_ptr + 1;
array_ptr = &big_array[130];
```

- In general: &big_array[i] is the same as (big_array + i)
 - which implicitly computes: &bigarray[0] + i*sizeof(bigarray[0]);

Arrays

- Arrays represent adjacent locations in memory storing the same type of data object
 - E.g., int big_array[128];
 allocated 512 adjacent locations in memory starting at 0x00ff0000
- Pointers to arrays point to a certain type of object

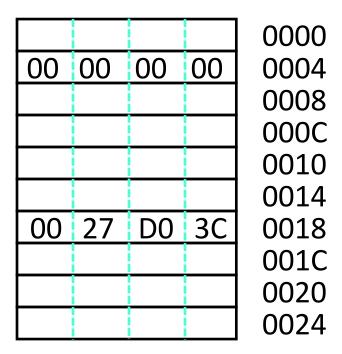
```
E.g., int * array_ptr;
array_ptr = big_array;
array_ptr = &big_array[0];
array_ptr = &big_array[3];
array_ptr = &big_array[0] + 3;
array_ptr = big_array + 3;
*array_ptr = *array_ptr + 1;
array_ptr = &big_array[130];
Ox00ff000c (adds 3 * size of int)
Ox00ff000c (but big_array[3] is incremented)
Ox00ff0208 (out of bounds, C doesn't check)
```

In general: &big_array[i] is the same as (big_array + i) which implicitly computes: &bigarray[0] + i*sizeof(bigarray[0]);

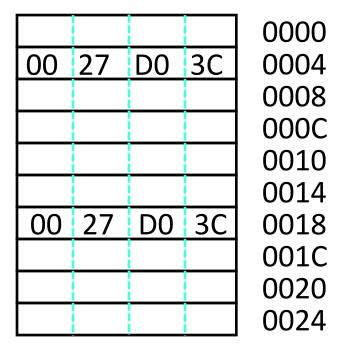
- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- **■** E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C

	_	_	
			0000
00	00	00	0004
			0008
			000C
			0010
			0014
27	D0	3C	0018
			001C
			0020
			0024

- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x



- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x



- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x
 - int * x; int y;
 x = &y + 3; // get address of y add 12

				1
				0000
00	27	D0	3C	0004
				0008
				000C
				0010
				0014
00	27	D0	3C	0018
				001C
				0020
				0024

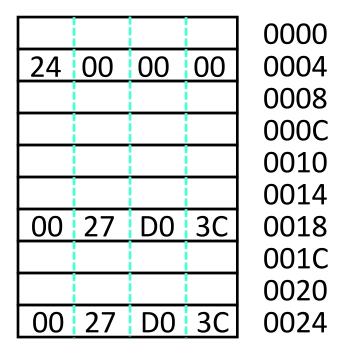
- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x
 - int * x; int y;
 x = &y + 3; // get address of y add 12

				•
				0000
24	00	00	00	0004
				0008
				000C
				0010
				0014
00	27	D0	3C	0018
				001C
				0020
				0024

- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- **■** E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x
 - int * x; int y; x = &y + 3; // get address of y add 12
 - int * x; int y;
 *x = y; // value of y copied to
 // location to which x points

				0000
24	00	00	00	0004
				0008
				000C
				0010
				0014
00	27	D0	3C	0018
				001C
				0020
				0024

- Left-hand-side = right-hand-side
 - LHS must evaluate to a memory LOCATION
 - RHS must evaluate to a VALUE (could be an address)
- E.g., x at location 0x04, y at 0x18
 - x originally 0x0, y originally 0x0027D03C
 - int x, y; x = y; // get value at y and put it in x
 - int * x; int y; x = &y + 3; // get address of y add 12
 - int * x; int y;
 *x = y; // value of y copied to
 // location to which x points



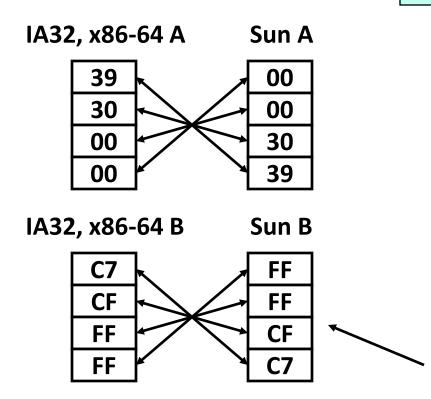
Representing Integers

- \blacksquare int A = 12345;
- int B = -12345;
- long int C = 12345;

Decimal: 12345

Binary: 0011 0000 0011 1001

Hex: 3 0 3 9

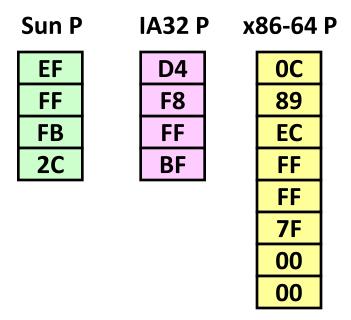


IA32 C X86-64 C Sun C 00 39 39 30 00 30 **30** 00 00 **39** 00 00 00 00 00 00

Two's complement representation for negative integers (covered later)

Representing Pointers

```
■ int B = -12345;
■ int *P = &B;
```



Different compilers & machines assign different locations to objects

Examining Data Representations

- Code to print byte representation of data
 - Casting pointer to unsigned char * creates byte array

```
typedef unsigned char * pointer;

void show_bytes(pointer start, int len)
{
  int i;
  for (i = 0; i < len; i++)
    printf("0x%p\t0x%.2x\n", start+i, start[i]);
  printf("\n");
}</pre>
```

```
void show_int (int x)
{
   show_bytes( (pointer) &x, sizeof(int));
}
```

Some printf directives:

%p: Print pointer

%x: Print hexadecimal

"\n": New line

show_bytes Execution Example

```
int a = 12345; // represented as 0x00003039
printf("int a = 12345;\n");
show_int(a); // show_bytes((pointer) &a, sizeof(int));
```

Result (Linux):

Representing strings

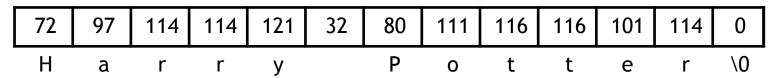
- A C-style string is represented by an array of bytes.
 - Elements are one-byte ASCII codes for each character.
 - A 0 value marks the end of the array.

32	space		48	0	64	@	80	Р	96	`	112	р
33	!		49	1	65	Α	81	Q	97	a	113	q
34	"		50	2	66	В	82	R	98	b	114	r
35	#		51	3	67	C	83	S	99	С	115	S
36	\$		52	4	68	D	84	Т	100	d	116	t
37	%		53	5	69	Ε	85	U	101	e	117	u
38	&		54	6	70	F	86	٧	102	f	118	٧
39	,		55	7	71	G	87	W	103	g	119	W
40	(56	8	72	Н	88	Χ	104	h	120	Х
41)		57	9	73	I	89	Υ	105	1	121	у
42	*		58	:	74	J	90	Z	106	j	122	Z
43	+		59	;	75	K	91	[107	k	123	{
44	,		60	<	76	L	92	\	108	ι	124	1
45	-		61	=	77	M	93]	109	m	125	}
46			62	>	78	Ν	94	^	110	n	126	~
47	/	L	63	?	79	0	95	_	111	0	127	del

40

Null-terminated Strings

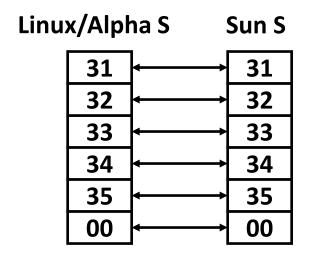
■ For example, "Harry Potter" can be stored as a 13-byte array.



Why do we put a 0, or null, at the end of the string?

Computing string length?

Compatibility



- Byte ordering not an issue
- Unicode characters up to 4 bytes/character
 - ASCII codes still work (leading 0 bit) but can support the many characters in all languages in the world
 - Java and C have libraries for Unicode (Java commonly uses 2 bytes/char)

Boolean Algebra

Developed by George Boole in 19th Century

- Algebraic representation of logic
 - Encode "True" as 1 and "False" as 0
- AND: A&B = 1 when both A is 1 and B is 1
- OR: A|B = 1 when either A is 1 or B is 1
- XOR: A^B = 1 when either A is 1 or B is 1, but not both
- NOT: ~A = 1 when A is 0 and vice-versa
- DeMorgan's Law: ~(A | B) = ~A & ~B

	0	1
0	0	1
1	1	1

^	0	1
0	0	1
1	1	0

~	
0	1
1	0

General Boolean Algebras

- Operate on bit vectors
 - Operations applied bitwise

 01101001
 01101001

 & 01010101
 01010101

 ^01010101
 01010101

All of the properties of Boolean algebra apply

01010101 ^ 01010101

How does this relate to set operations?

Representing & Manipulating Sets

Representation

■ Width w bit vector represents subsets of {0, ..., w-1}

```
■ a_j = 1 \text{ if } j \in A

01101001 { 0, 3, 5, 6 }

76543210
```

01010101 { 0, 2, 4, 6 } 76543210

Operations

&	Intersection	01000001	{ 0, 6 }
	Union	01111101	{ 0, 2, 3, 4, 5, 6 }
٨	Symmetric difference	00111100	{ 2, 3, 4, 5 }
~	Complement	10101010	{1,3,5,7}

Bit-Level Operations in C

- Operations &, |, ^, ~ are available in C
 - Apply to any "integral" data type
 - long, int, short, char, unsigned
 - View arguments as bit vectors
 - Arguments applied bit-wise

Examples (char data type)

- 0x69 & 0x55 --> 0x41
 01101001₂ & 01010101₂ --> 01000001₂
- $0x69 \mid 0x55 \quad --> \quad 0x7D$ $01101001_2 \mid \quad 01010101_2 \quad --> \quad 01111101_2$

Contrast: Logic Operations in C

Contrast to logical operators

- **&** & & , | | , !
 - View 0 as "False"
 - Anything nonzero as "True"
 - Always return 0 or 1
 - Early termination

Examples (char data type)

- !0x00 --> 0x01
- !!0x41 --> 0x01
- 0x69 && 0x55 --> 0x01
- $-0x69 \mid | 0x55 --> 0x01$
- p && *p++ (avoids null pointer access, null pointer = 0x000000000)
- if (p) *p++;