## Student Activity

## How long (in ns) does each instruction take?

|            | Single<br>Cycle | Multi-<br>cycle |
|------------|-----------------|-----------------|
| loads      |                 |                 |
| stores     |                 |                 |
| branches   |                 |                 |
| arithmetic |                 |                 |
| Cycle Time |                 |                 |

Which ones are faster in Multi-cycle?

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## Student Activity

How long does it take to execute a sequence of four lw instructions?

|                | Instruction<br>Fetch | Register<br>Read | ALU<br>Operation | Data Access | Register<br>Write |
|----------------|----------------------|------------------|------------------|-------------|-------------------|
| Load Word (lw) | 200 ps               | 100 ps           | 200 ps           | 200 ps      | 100 ps            |

lw \$1, 100 (\$0)

lw \$1, 104 (\$0)

lw \$1, 108 (\$0)

lw \$1, 112(\$0)

|                | (Time in ps) |             | (Time in ps) |
|----------------|--------------|-------------|--------------|
| Single Cycle = |              | Pipelined = |              |

## Student Activity **Reading Pipeline Diagrams** Clock cycle 2 4 6 7 9 \$t0, 4(\$sp) \$v0, \$a0, \$a1 ID EX MEM WB IF ID ΕX MEM WB and \$t1, \$t2, \$t3 IF ID EX MEM WB \$s0, \$s1, \$s2 IF ID EX MEM WB add \$sp, \$sp, -4 IF MEM WB ID EX 1. What cycle is the "add" instruction being fetched in? 2. What is the last cycle where the "or" instruction is active? 3. What happens in cycle 4?

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