Simplify the following equations:

$$f(x, y, z) = \overline{xy + \bar{x}z}$$

$$f(x, y, z) = \overline{\overline{xy + z}} + x\bar{y}z + x\overline{yz}$$

Implement with only NAND gates:

$$f(a,b,c) = \overline{ab} + c$$

Draw a schematic for the following (in DesignWorks):

$$f(x, y, z) = \overline{x}\,\overline{y} + xy + x\overline{y}$$