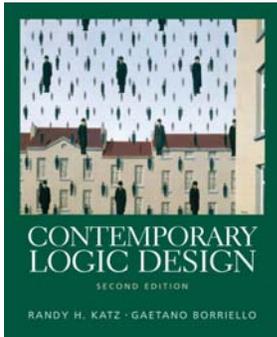


CSE 370 Spring 2006

Introduction to Digital Design

Lecture 2: Binary Number Systems



Last Lecture

- Course Overview
- The Digital Age

Today

- Binary numbers
- Base conversion
- Negative binary numbers
- Switches/CMOS

Administrivia

Make sure

- Signed up to the mailing list

Homework

- Will be assigned on Friday prior to due date (so that it can haunt you over the weekend!)

- Homework guru: Adrienne Wang (axwang@cs)
Office hours: W 3-5pm in CSE 218

Office hours

- Benjamin Ylvisaker (ben8@cs)
Office hours: Th 1:30-3:30 in CSE 003

Digital

Digital = Discrete

- Decimal digits
- DNA nucleotides *CATG U*
- Binary codes
 - symbols mapped to bits

Digital Computers

- I/O is digital
 - ASCII, decimal, binary, etc.
- Internal representation
 - binary

Dec	Bin	BCD
0	0000	0000
1	0001	0001
2	0010	0010
3	0011	0011
4	0100	0100
5	0101	0101
6	0110	0110
7	0111	0111
8	1000	1000
9	1001	1001

Number Systems

Bases In This Class

- Binary (2), Octal (8), Decimal (10), Hexadecimal(16)
- Positional numbering systems ("significant digits")

$$101_2 = 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 = 4 + 0 + 1 = 5_{10}$$

$$67_8 = 6 \times 8^1 + 7 \times 8^0 = 48 + 7 = 55_{10}$$

$$AB_{16} = 10 \times 16^1 + 11 \times 16^0 = 160 + 11 = 171_{10}$$

$$41.7_8 = 4 \times 8^1 + 1 \times 8^0 + 7 \times 8^{-1} = 32 + 1 + 7 \times 8^{-1} = 33.875_{10}$$

Adding, Subtracting "There are 10 kinds of people in the world—those who understand binary numbers, and those who don't."

$\begin{array}{r} 111 \\ 1011_2 \\ +1110_2 \\ \hline 11001_2 \end{array}$	$\begin{array}{l} 0+0=0 \\ 0+1=1 \\ 1+0=1 \\ 1+1=0 \\ \text{Carry } 1 \end{array}$	$\begin{array}{r} 11 \\ 52_{16} \\ +AF_{16} \\ \hline 1011_6 \end{array}$	$\begin{array}{r} 10111_2 \\ -00101_2 \\ \hline 10010_2 \end{array}$
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Conversions

Binary to Octal and Hexadecimal

$$1011011001_2 = \underbrace{101}_4 \underbrace{011}_4 \underbrace{001}_4 = 1331_8$$

$$\underline{1011011001}_2 = \underbrace{10}_{8+4} \underbrace{1101}_{8} \underbrace{1001}_8 = 2D9_{16}$$

Octal and Hexadecimal to Binary

$$401_8 = \begin{matrix} 4 & 0 & 1 \\ 100 & 000 & 001 \end{matrix}_2 = 100000001_2$$

$$B10_{16} = \begin{matrix} B & 1 & 0 \\ 1011 & 0001 & 0000 \end{matrix}_2 = 101100010000_2$$

Decimal to Others

Decimal to Binary

$$\begin{aligned} 58/2 &= 29 \text{ remainder } 0 \\ 29/2 &= 14 \text{ r } 1 \\ 14/2 &= 7 \text{ r } 0 \\ 7/2 &= 3 \text{ r } 1 \\ 3/2 &= 1 \text{ r } 1 \\ 1/2 &= 0 \text{ r } 1 \end{aligned}$$

Decimal to Octal

$$\begin{aligned} 58/8 &= 7 \text{ r } 2 \\ 7/8 &= 0 \text{ r } 7 \end{aligned}$$

$$\underline{111010}_2$$

Why does this work?

$$\begin{aligned} 111010/2_{10} &= 11101 \text{ rem } 0 \\ 11101/2_{10} &= 1110 \text{ rem } 1 \end{aligned}$$

Negative Numbers

Negative binary numbers?

Historically

- sign/magnitude
- ones-complement
- twos-complement

For all three:

- most significant bit (msb) is the sign
 - 0=positive 1=negative



- twos-complement universally most used
 - simplifies arithmetic

Sign/Magnitude

most significant bit is sign
 ■ 0=positive, 1=negative

4 bits

remaining bits are magnitude

max +7
 min -7

$$\begin{aligned} 0101_2 &= +5_{10} \\ 1101_2 &= -5_{10} \end{aligned}$$

Problem 1: two zeros!

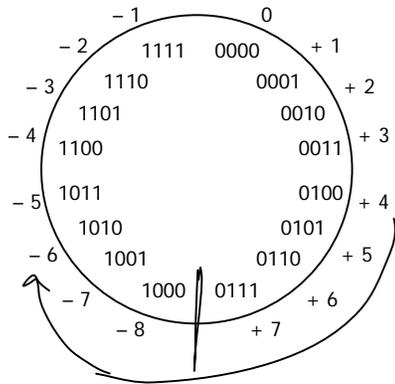
$$0000_2 = 0_{10} \text{ and } 1000_2 = -0_{10} = 0_{10}$$

Problem 2: arithmetic is messy (hard to implement)

$$\begin{array}{r} 4_{10} = 0100_2 \\ +3_{10} = 0011_2 \\ \hline +7_{10} \quad 0111_2 = +7_{10} \end{array} \quad \begin{array}{r} 4_{10} = 0100_2 \\ -3_{10} = 1011_2 \\ \hline 1_{10} \neq 1111 = -7_{10} \end{array} \quad \begin{array}{r} -4_{10} = 0100_2 \\ +3_{10} = 1011_2 \\ \hline -1_{10} \neq 1111 = -7_{10} \end{array}$$

Twos-Complement Overflow

- Numbers may add out of range (overflow)



$$\begin{array}{r} +4=0100 \\ +6=0110 \\ \hline +10=1010 \end{array}$$

Twos-Complement Overflow

- Numbers may add out of range (overflow)

carry bits	<u>0100</u>	<u>11000</u>	10000
	+4=0100	+4=0100	-4= 1011
	+6=0110	-3=1100	-3= 1100
	<u>+10=1010</u>	<u>+1=1000</u>	<u>+1=0111</u>

Last two carry bits: c_{last} and c_{2last}

Overflow: f

c_{last}	c_{2last}	f
0	0	0
0	1	1
1	0	1
1	1	0

Twos-Complement Misc

- sign extension

$$+6_{10} = 0110_2$$

$$-6_{10} = 1001_2$$

- extend to eight bits (a byte):

$$+6_{10} = \underline{00000110}_2$$

$$-6_{10} = \underline{11111001}_2$$

- different binary numbers have different values

- 11001 = 25_{10} unsigned

- 11001 = -9_{10} sign/magnitude

- 11001 = -6_{10} ones-complement $0110 = +6$

- 11001 = -7_{10} twos-complement

- The weird number: ~~1111₂~~

$$1000_2 = -8$$

Machine Independent?

- HAKMEM Item 154 (Bill Gosper)

The myth that any given programming language is machine independent is easily exploded by computing the sum of powers of 2.

If the result loops with period = 1 with sign +, you are on a sign-magnitude machine.

If the result loops with period = 1 at -1, you are on a twos-complement machine.

If the result loops with period > 1, including the beginning, you are on a ones-complement machine.

If the result loops with period > 1, not including the beginning, your machine isn't binary -- the pattern should tell you the base.

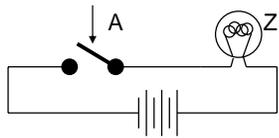
If you run out of memory, you are on a string or Bignum system.

If arithmetic overflow is a fatal error, some fascist pig with a read-only mind is trying to enforce machine independence. But the very ability to trap overflow is machine dependent.

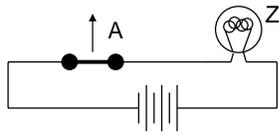
DELETED Proves universe = twos complement

Switches

- Implementing a simple circuit (arrow shows action if wire changes to "1"):



close switch (if A is "1" or asserted) and turn on light bulb (Z)

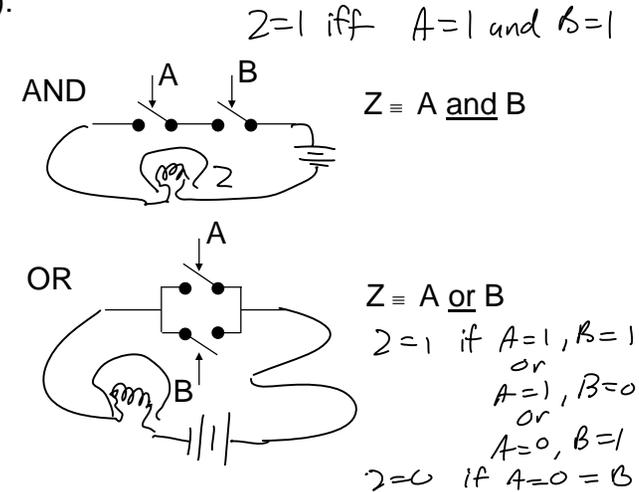


open switch (if A is "0" or unasserted) and turn off light bulb (Z)

$$Z \equiv A$$

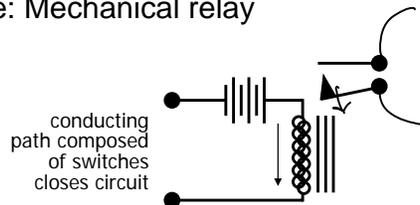
Switches

- Compose switches into more complex ones (Boolean functions):



Switching Networks

- Switch settings determine whether a conducting network to a light bulb
- Larger computations?
 - Use a light bulb (output) to set other switches (input)
 - Example: Mechanical relay



current flowing through coil magnetizes core and causes normally closed (nc) contact to be pulled open

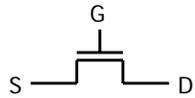
when no current flows, the spring of the contact returns it to its normal position

Transistor Networks

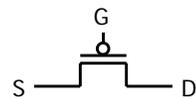
- Relays no more: slow and big
- Modern digital electronics predominately uses CMOS technology
 - MOS: metal-oxide semiconductor
 - C: complementary (both p and n type transistors arranged so that power is dissipated during switching.)

MOS Transistors

- MOS transistors have three terminals: drain, gate, and source
- Act as switches: if the voltage on the gate terminal is (some amount) higher/lower than the source terminal then a conducting path will be established between the drain and source terminals.

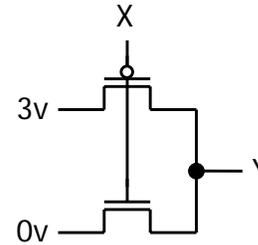


n-channel
open when voltage at G is low
closes when:
voltage(G) > voltage (S) + ε



p-channel
closed when voltage at G is low
opens when:
voltage(G) < voltage (S) - ε

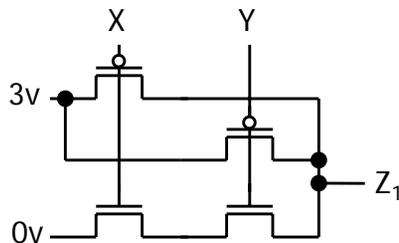
MOS Networks



what is the relationship between x and y?

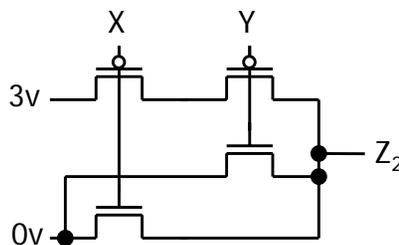
x	y
0 volts	
3 volts	

Two Input Networks



what is the relationship between x, y and z?

x	y	z1	z2
0 volts	0 volts		
0 volts	3 volts		
3 volts	0 volts		
3 volts	3 volts		



Your To Do List

- Things Internet
 - Sign up for mailing list
- Things Reading
 - Week 1 reading (on website): pp.1-27, Appendix A, pp.33-46
- Things Homework
 - Homework 1 posted on website (due this Friday)
- Things Laboratory
 - Attend first lab session if you haven't already