

Practice Counter

Implement a **counter** that goes through the state sequence:
 000→001→011→010→110→111→101→100→000→...

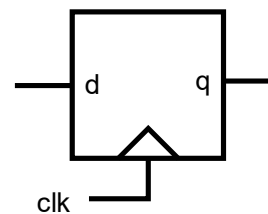
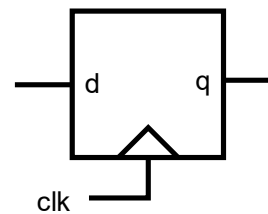
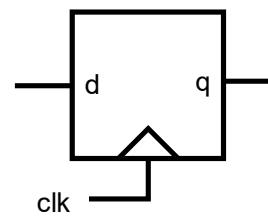
Include Enable and Reset signals

$N_2 =$

$N_1 =$

$N_0 =$

P_2	P_1	P_0	N_2	N_1	N_0
0	0	0	0	0	1
0	0	1	0	1	1
0	1	0	1	1	0
0	1	1	0	1	0
1	0	0	0	0	0
1	0	1	1	0	0
1	1	0	1	1	1
1	1	1	1	0	1



FPGA Routing

(Quartus says 🤖: HAHA LOSER NOW IT'S YOUR TURN)

