

Memory & Caches II

CSE 351 Winter 2024

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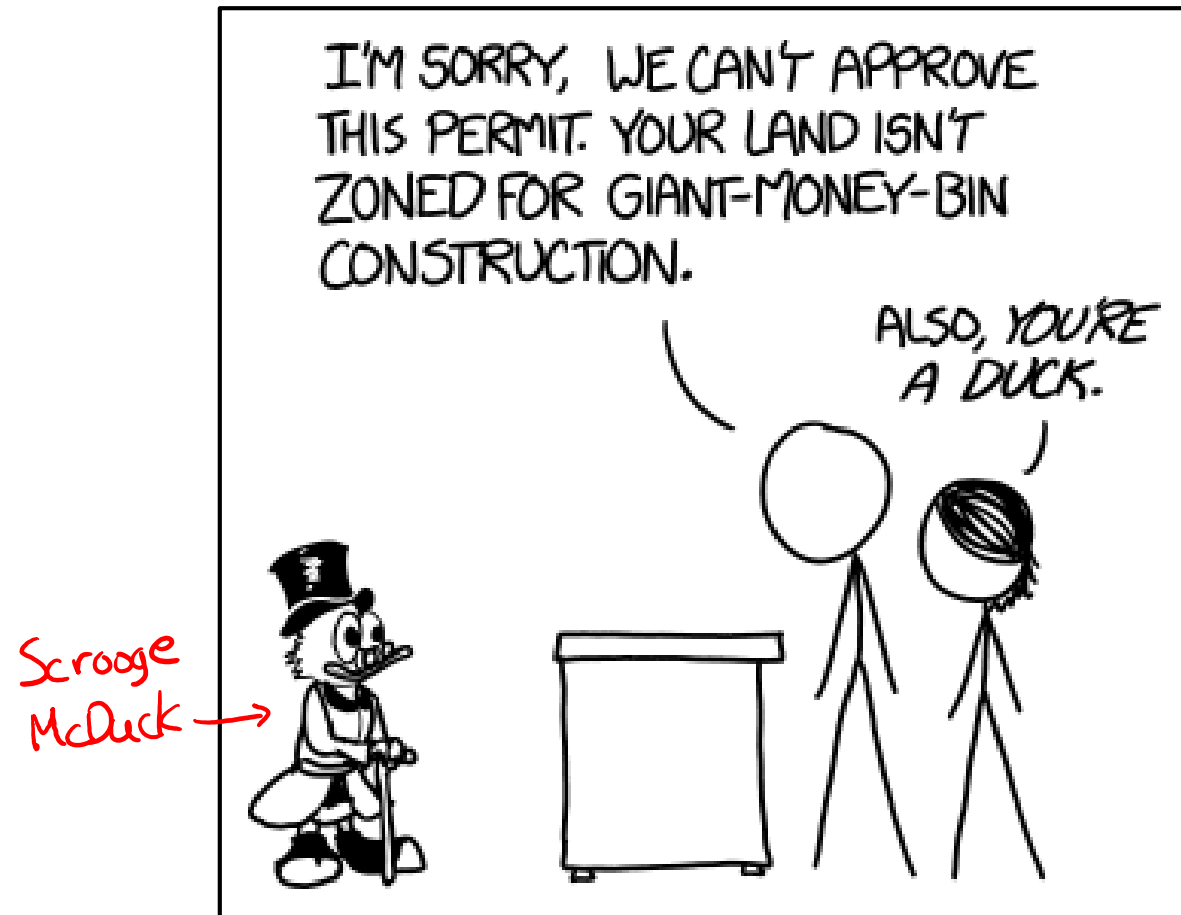
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<https://what-if.xkcd.com/111/>

Relevant Course Information

- ❖ HW14 due tonight, HW15 due Wednesday, HW16 due Friday
- ❖ Lab 3 due Friday (2/16), late deadline is Monday (2/19)
 - President's Day: no lecture, but some support hours (see Ed)
- ❖ Midterm grades will be released when we can
 - Regrade requests will be available afterward

Mid-Quarter Survey Summary

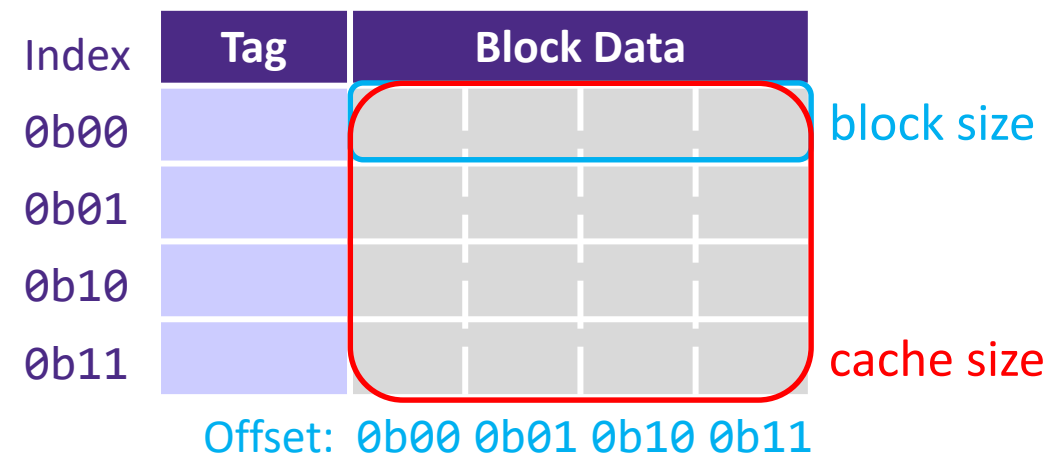
- ❖ Lessons:
 - There's a lot of content... some lessons (videos) are too long
 - Wish there were more practice problems and worked examples
 - Would like if the slides used in the videos were posted (will do)
 - Organization of Ed Lessons could be improved (FYI, website schedule by due dates)
- ❖ Lecture:
 - Would like a different balance of content review & work time (but no agreement)
 - Would like more practice problems and worked examples
- ❖ Section: want more practice problems (?)
- ❖ Support hours: want more total, more later in day, more on Zoom

A detailed, colorful micrograph of a microchip die, showing a complex grid of circuitry and various colored regions. The text 'Caches II' is overlaid on the left side of the image.

Caches II

Lesson Summary (1/2)

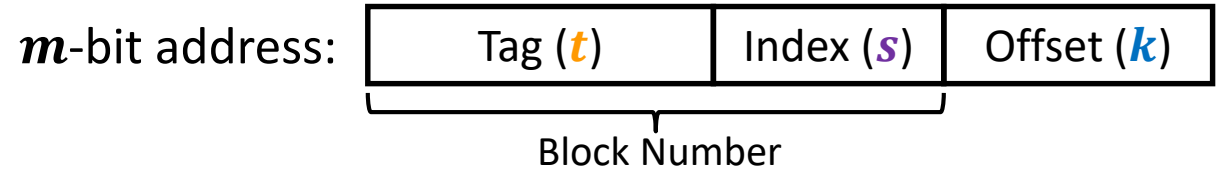
- ❖ Cache parameters define the cache geometry:
 - **Block size** is number of bytes per block
 - **Cache size** is number of bytes (or blocks) of data the cache can hold
- ❖ Finding a byte in the cache:
 - **Offset** refer to which byte in block
 - **Index** refers to which block in cache
- ❖ Example:
 - $K = 4 \text{ B}$, $C = 16 \text{ B} = 4 \text{ blocks}$



Lesson Summary (2/2)

- ❖ **Direct-mapped cache:** each block in cache is assigned a unique index
 - Uses hash function of (block number) mod (# of cache indices)
 - Deterministic placement of each block, with many blocks mapping into the same index
 - Tag bits stored in cache and used to distinguish between blocks that map to same index

- ❖ Accessing the cache:
(TIO address breakdown)



- 1) **Index** field tells you where to look in cache (width $s = \log_2 S$)
- 2) **Tag** field lets you check that data is the block you want (width $t = m - s - k$)
- 3) **Offset** field selects specified start byte within block (width $k = \log_2 K$)

Lesson Q&A

- ❖ Learning Objectives:
 - Determine how memory addresses and data interact with the cache (*i.e.*, cache lookups, data movement).
 - Analyze how changes to cache parameters [and policies] affect performance metrics such as AMAT.
- ❖ What lingering questions do you have from the lesson?
 - Chat with your neighbors about the lesson for a few minutes to come up with questions

A detailed, colorful micrograph of a microchip die, showing a complex grid of circuitry and various colored regions. The text "Caches II – Practice" is overlaid in the center.

Caches II – Practice

Polling Questions (1/2)

❖ We have a direct-mapped cache with the following parameters:

▪ Block size of 8 bytes $K = 2^3 B$

▪ Cache size of 4 KiB $C = 2^{12} B$
 $2^2 \uparrow \uparrow 2^{10}$

❖ How many blocks can the cache hold? $C/K = 2^{12-3} = 2^9 = \boxed{512 \text{ blocks}}$

❖ How many bits wide is the block offset field? $k = \log_2(k) = \boxed{3 \text{ bits}}$

❖ Which of the following addresses would fall under block number 3?

A. **0x3**

$$\lfloor 3/8 \rfloor = 0$$

0b 00 0b11
block num 0

B. **0x1F**

$$\lfloor 31/8 \rfloor = 3$$

0b 01 1111
block num 3

C. **0x30**

$$\lfloor 48/8 \rfloor = 6$$

0b 11 0000
block num 6

D. **0x38**

$$\lfloor 56/8 \rfloor = 7$$

0b 11 1000
block num 7

Polling Questions (2/2)

❖ Based on the following behavior, which of the following block sizes is NOT possible for our cache?

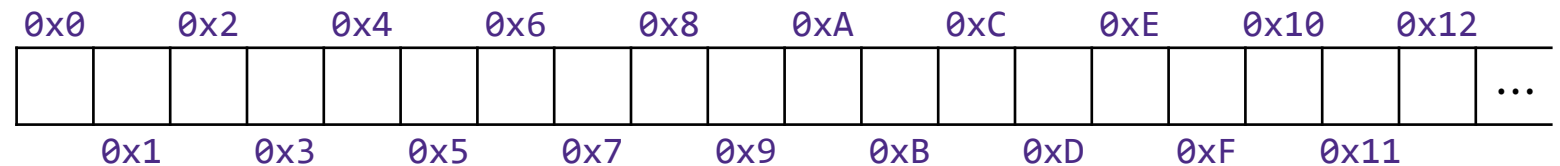
- Cache starts *empty*, also known as a *cold cache*
- Access (addr: hit/miss) stream:
 - (0xE: miss), (0xF: hit), (0x10: miss)

hit: block with data already in \$

miss: data not in \$, pulls block containing data from Mem

↳ ① pulls block containing 14 into \$
 ↳ ② 14 & 15 are in the same block
 ↳ ③ 16 is in a different block

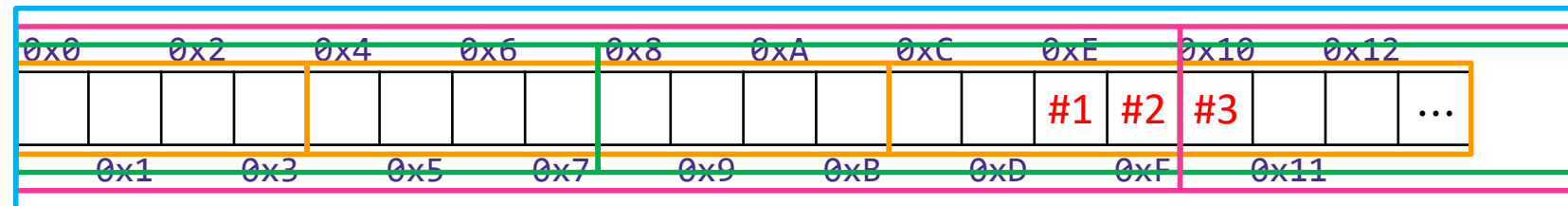
- A. 4 bytes
- B. 8 bytes
- C. 16 bytes
- D. 32 bytes



Practice Questions (2/2)

- ❖ Based on the following behavior, which of the following block sizes is NOT possible for our cache?
 - Cache starts *empty*, also known as a *cold cache*
 - Access (addr: hit/miss) stream:
 - (0xE: miss), (0xF: hit), (0x10: miss)
 - Need 0xE and 0xF in same block; 0x10 in different block

- A. 4 bytes
- B. 8 bytes
- C. 16 bytes
- D. 32 bytes



Homework Setup (1/2)

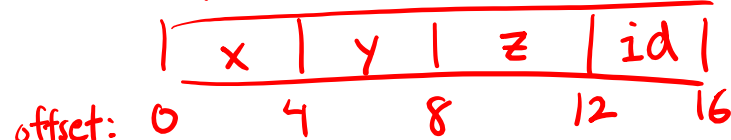
$$\& \text{grid}[R][C] = \& \text{grid} + (16 * R + C) * \text{sizeof}(\text{struct WolfPos})$$

```

K struct WolfPos {
4   float x;
4   float y;
4   float z;
4   int id;

```

struct WolfPos:



}; K_{max} = 4

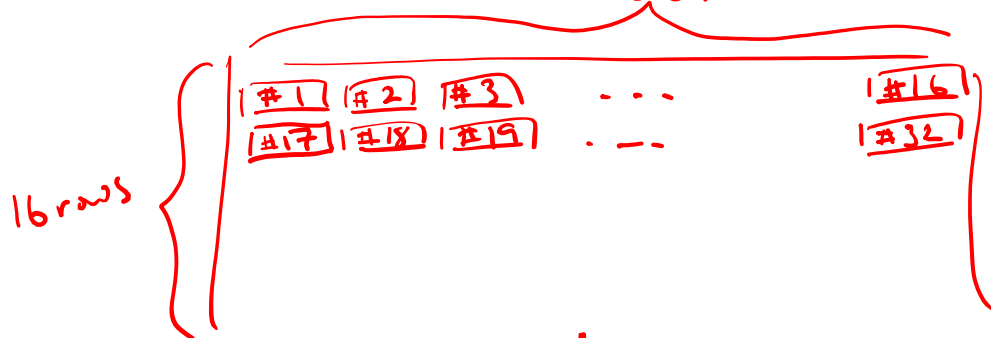
```

struct WolfPos grid[16][16];

```

- Assume $\&\text{grid} = 0$

C is row-major: 16 columns



❖ What are the addresses of the following pieces of data?

- $\&(\text{grid}[0][0].\text{id}) = 12 = 0 \times C$

- $\&(\text{grid}[1][0].\text{y}) = 260 = 0 \times 104$

- $\&(\text{grid}[3][4].\text{x}) = 832 = 0 \times 340$

Homework Setup (2/2)

```
struct WolfPos {
    float x;
    float y;
    float z;
    int id;
};
struct WolfPos grid[16][16];
```

- Assume &grid = 0

$\& \text{grid}[0][0].x = 0x0 = 0b \overset{\text{tag}}{0000} \overset{\text{index}}{0000} \overset{\text{offset}}{0000}$
 $\& \text{grid}[4][0].x = 0x400 = 0b \overset{\text{tag}}{0} \overset{\text{index}}{0000} \overset{\text{offset}}{0000}$
 (16*4+0)*16

$S = C/K = \text{cache holds } 64 \text{ blocks}$
 $s = \log_2(C/K) = 6 \text{ bits}$

- ❖ Cold direct-mapped cache with $C = 1024 \text{ B}$ and $K = 16 \text{ B} \Rightarrow k = 4 \text{ bits}$
 - What happens if we access $\text{grid}[0][0].x$ and then $\text{grid}[4][0].x$? *offset 0*

Miss in index 0 \Rightarrow load block with tag 0

Miss in index 0 \Rightarrow kick out block with tag 0
load block with tag 1

Group Work Time

- ❖ During this time, you are encouraged to work on the following:
 - 1) If desired, continue your discussion
 - 2) Work on the homework problems
 - 3) Work on the lab (if applicable)

- ❖ Resources:
 - You can revisit the lesson material
 - Work together in groups and help each other out
 - Course staff will circle around to provide support