

## Arithmetic Example

```
long simple_arith(long x, long y)
{
    long t1 = x + y;
    long t2 = t1 * 3;
    return t2;
}
```

Register	Use(s)
%rdi	1 <sup>st</sup> argument (x)
%rsi	2 <sup>nd</sup> argument (y)
%rax	return value

```
y += x;
y *= 3;
long r = y;
return r;
```

```
simple_arith:
    addq    %rdi, %rsi
    imulq   $3, %rsi
    movq    %rsi, %rax
    ret
```

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## Understanding swap ()

### Registers

%rdi	0x120
%rsi	0x100
%rax	
%rdx	

### Memory

Word Address
123 0x120
0x118
0x110
0x108
456 0x100

```
swap:
    movq    (%rdi), %rax    # t0 = *xp
    movq    (%rsi), %rdx    # t1 = *yp
    movq    %rdx, (%rdi)    # *xp = t1
    movq    %rax, (%rsi)    # *yp = t0
    ret
```

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## Address Computation Examples

%rdx	0xf000
%rcx	0x0100

$D(Rb, Ri, S) \rightarrow$   
 $Mem[Reg[Rb] + Reg[Ri] * S + D]$   
 ignore the memory access for now

Expression	Address Computation	Address (8 bytes wide)
$0x8(\%rdx)$		
$(\%rdx, \%rcx)$		
$(\%rdx, \%rcx, 4)$		
$0x80(, \%rdx, 2)$		

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## Review Questions

- ❖ Which of the following x86-64 instructions correctly calculates  $\%rax = 9 * \%rdi$ ?
  - `leaq (, %rdi, 9), %rax`
  - `movq (, %rdi, 9), %rax`
  - `leaq (%rdi, %rdi, 8), %rax`
  - `movq (%rdi, %rdi, 8), %rax`
- ❖ If  $\%rsi$  is `0x B0BACAFE 1EE7 F0 0D`, what is its value after executing `movswl %si, %esi`?

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## Arithmetic Example

```
long arith(long x, long y, long z)
{
    long t1 = x + y;
    long t2 = z + t1;
    long t3 = x + 4;
    long t4 = y * 48;
    long t5 = t3 + t4;
    long rval = t2 * t5;
    return rval;
}
```

```
arith:
    leaq    (%rdi,%rsi), %rax
    addq    %rdx, %rax
    leaq    (%rsi,%rsi,2), %rdx
    salq    $4, %rdx
    leaq    4(%rdi,%rdx), %rcx
    imulq   %rcx, %rax
    ret
```

Register	Use(s)
%rdi	1 <sup>st</sup> argument (x)
%rsi	2 <sup>nd</sup> argument (y)
%rdx	3 <sup>rd</sup> argument (z)

### ❖ Interesting Instructions

- leaq: “address” computation
- salq: shift
- imulq: multiplication
  - Only used once!

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## Example Condition Code Setting

- ❖ Assuming that %al = 0x80 and %bl = 0x81, which flags (CF, ZF, SF, OF) are set when we execute **cmpb %al, %bl**?

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## Practice Question 1

Register	Use(s)
%rdi	1 <sup>st</sup> argument (x)
%rsi	2 <sup>nd</sup> argument (y)
%rax	return value

- A. `cmpq %rsi, %rdi`  
`jle .L4`
- B. `cmpq %rsi, %rdi`  
`jg .L4`
- C. `testq %rsi, %rdi`  
`jle .L4`
- D. `testq %rsi, %rdi`  
`jg .L4`
- E. We're lost...

```
long absdiff(long x, long y)
{
    long result;
    if (x > y)
        result = x-y;
    else
        result = y-x;
    return result;
}
```

```
absdiff:
    _____
    _____
    _____ # x > y:
    movq    %rdi, %rax
    subq    %rsi, %rax
    ret
.L4:      # x <= y:
    movq    %rsi, %rax
    subq    %rdi, %rax
    ret
```

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