

Executables & Arrays

CSE 351 Summer 2022

Instructor:

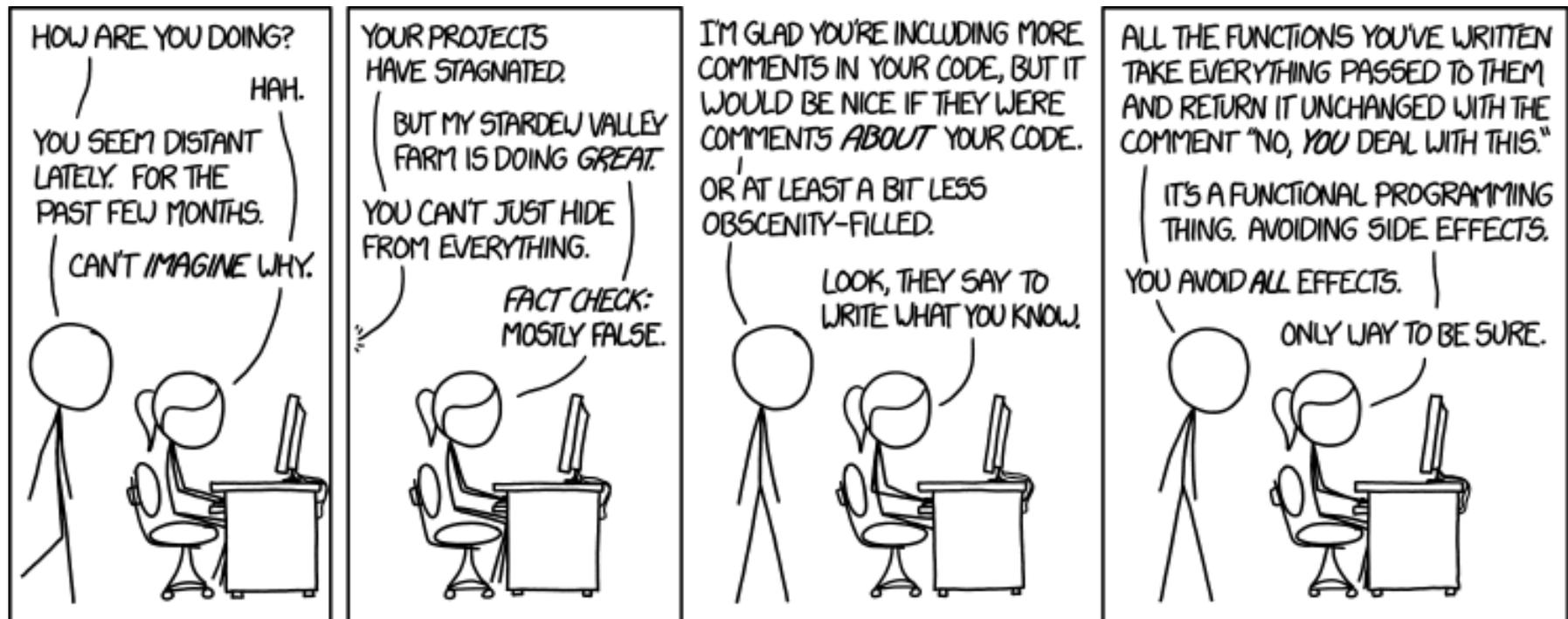
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Relevant Course Information

- ❖ Lab 2 due tonight at 11:59 pm
 - Weekend counts as *one* late day so latest time to submit is Monday at 11:59 pm using two late days

- ❖ hw11 due tonight, hw12 due Monday

- ❖ hw13 due *next* Friday (7/29)
 - Based on the next two lectures, longer than normal

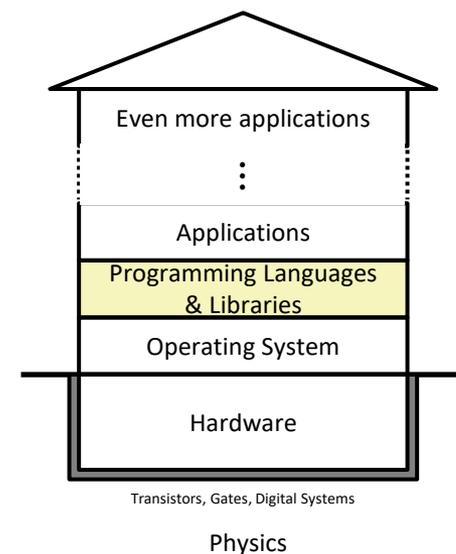
Relevant Course Information

- ❖ Lab 3 due next Friday
 - All based on the Stack, Procedures, and Buffer Overflow lectures

- ❖ Unit Portfolio 2 releasing on Monday, due 8/3
 - Based on the topics from lecture 7 through 14 (Monday's lecture)
 - Feedback for the first unit portfolio will be released before the second one is due

The Hardware/Software Interface

- ❖ Topic Group 2: **Programs**
 - x86-64 Assembly, Procedures, Stacks, **Executables**



- ❖ How are programs created and executed on a CPU?
 - How does your source code become something that your computer understands?
 - How does the CPU organize and manipulate local data?

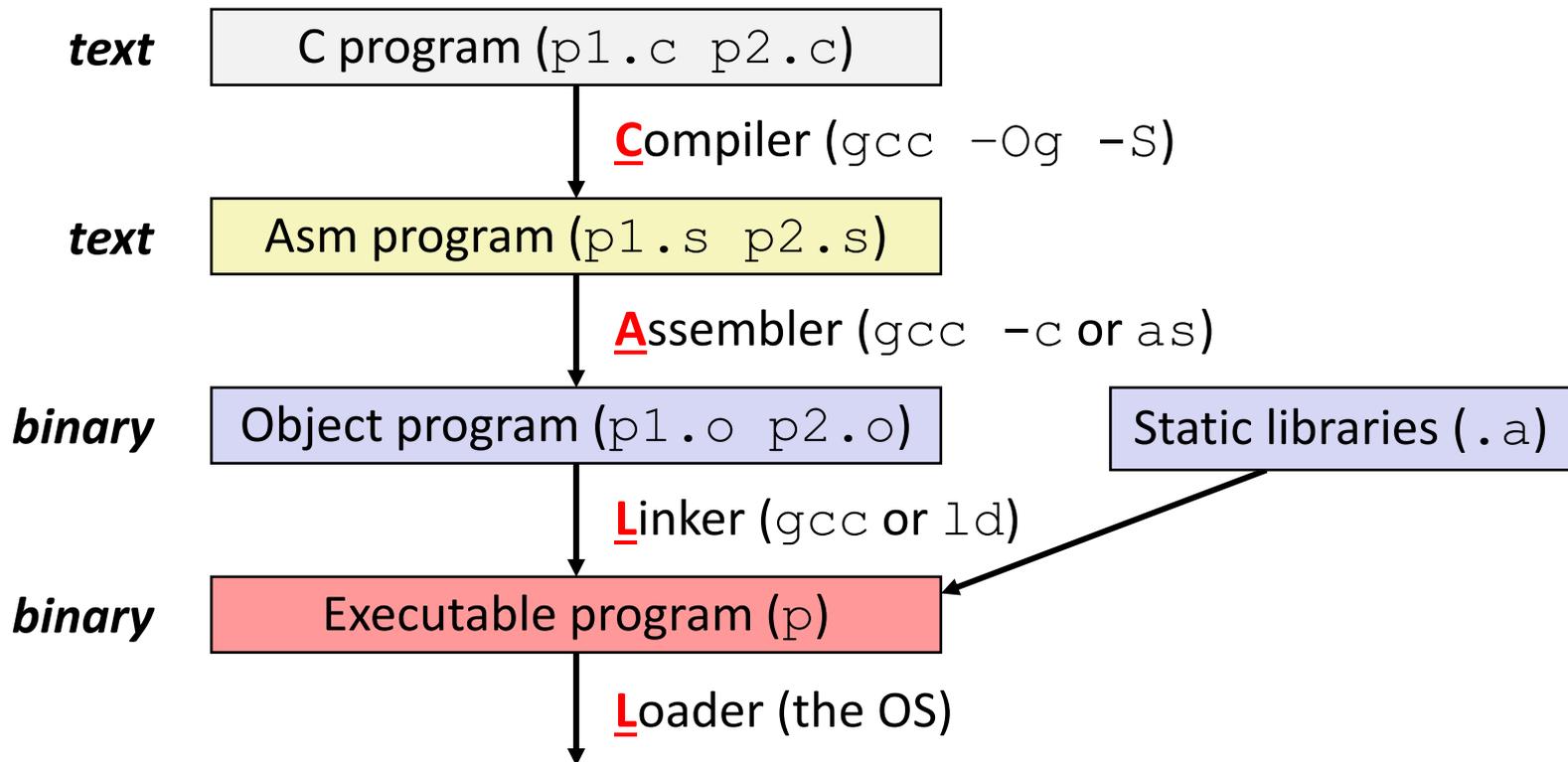
Reading Review

- ❖ Terminology:
 - CALL: compiler, assembler, linker, loader
 - Object file: symbol table, relocation table
 - Disassembly
 - Multidimensional arrays, row-major ordering
 - Multilevel arrays

- ❖ Questions from the Reading?

Building an Executable with C (Review)

- ❖ Code in files `p1.c` `p2.c`
- ❖ Compile with command: `gcc -Og p1.c p2.c -o p`
 - Put resulting machine code in file `p`
- ❖ Run with command: `./p`



Compiler (Review)

- ❖ **Input:** Higher-level language code (*e.g.*, C, Java)
 - `foo.c`
 - ❖ **Output:** Assembly language code (*e.g.*, x86, ARM, MIPS)
 - `foo.s`
-
- ❖ First there's a preprocessor step to handle `#directives`
 - Macro substitution, plus other specialty directives
 - If curious/interested: <http://tigcc.ticalc.org/doc/cpp.html>
 - ❖ Super complex, whole courses devoted to these! (CSE 401)
 - ❖ Compiler optimizations
 - “Level” of optimization specified by capital ‘O’ flag (*e.g.* `-Og`, `-O3`)
 - Options: <https://gcc.gnu.org/onlinedocs/gcc/Optimize-Options.html>

Compiling Into Assembly (Review)

❖ C Code (sum.c)

```
void sumstore(long x, long y, long *dest) {  
    long t = x + y;  
    *dest = t;  
}
```

❖ x86-64 assembly (gcc -Og -S sum.c)

```
sumstore(long, long, long*):  
    addq    %rdi, %rsi  
    movq    %rsi, (%rdx)  
    ret
```

Warning: You may get different results with other versions of gcc and different compiler settings

Assembler (Review)

- ❖ **Input:** Assembly language code (*e.g.*, x86, ARM, MIPS)
 - `foo.s`
- ❖ **Output:** Object files (*e.g.*, ELF, COFF)
 - `foo.o`
 - Contains *object code* and *information tables*

- ❖ Reads and uses *assembly directives*
 - *e.g.*, `.text`, `.data`, `.quad`
 - x86: https://docs.oracle.com/cd/E26502_01/html/E28388/eoiyg.html
- ❖ Produces “machine language”
 - Does its best, but object file is *not* a completed binary
- ❖ Example: `gcc -c foo.s`

Producing Machine Language (Review)

- ❖ **Simple cases:** arithmetic and logical operations, shifts, etc.
 - All necessary information is contained in the instruction itself
- ❖ **Addresses and labels are problematic because the final executable hasn't been constructed yet!**
 - Conditional and unconditional jumps
 - Accessing static data (*e.g.*, global variable or jump table)
 - `call`
- ❖ So how do we deal with these in the meantime?

Object File Information Tables (Review)

- ❖ Each object file has its own symbol and relocation tables
- ❖ **Symbol Table** holds list of “items” that may be used by other files
 - *Non-local labels* – function names for `call`
 - *Static Data* – variables & literals that might be accessed across files
- ❖ **Relocation Table** holds list of “items” that this file needs the address of later (currently undetermined)
 - Any *label* or piece of *static data* referenced in an instruction in this file
 - Both internal and external

Object File Format

- 1) object file header: size and position of the other pieces of the object file
 - 2) text segment: the machine code
 - 3) data segment: data in the source file (binary)
 - 4) relocation table: identifies lines of code that need to be “handled”
 - 5) symbol table: list of this file’s labels and data that can be referenced
 - 6) debugging information
- ❖ More info: ELF format
- http://www.skyfree.org/linux/references/ELF_Format.pdf

Practice Questions

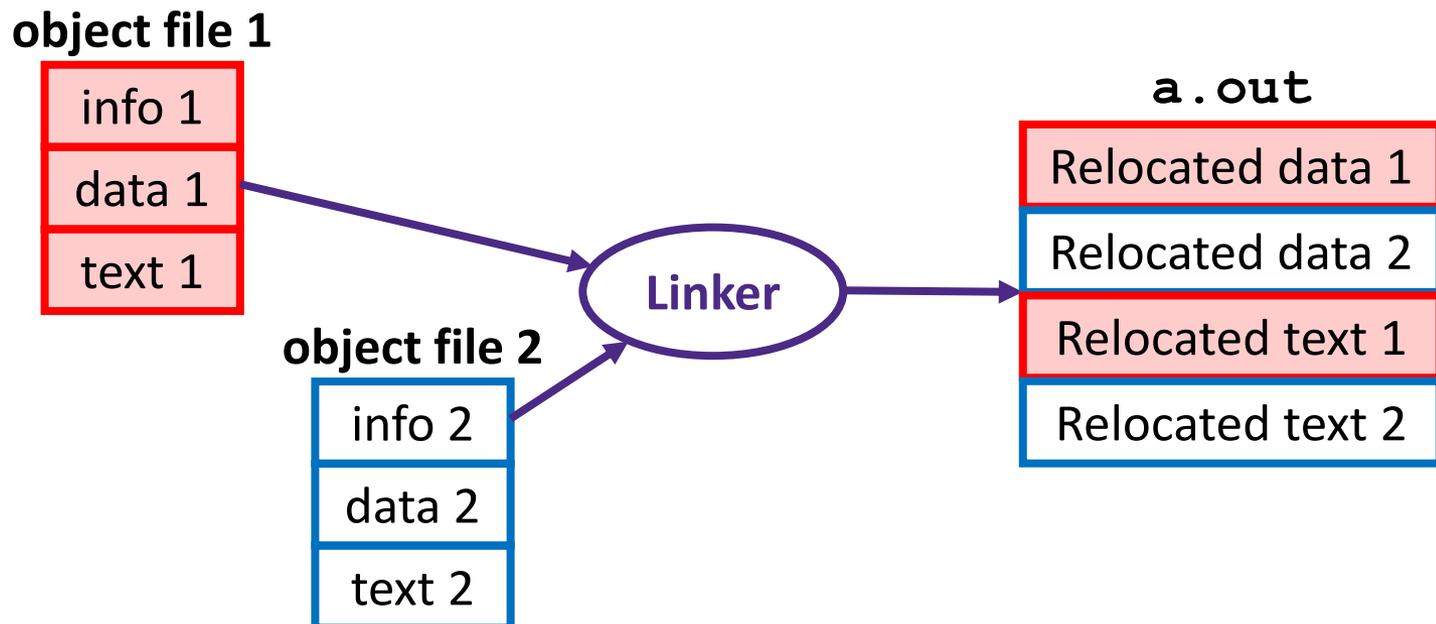
- ❖ The following labels/symbols will show up in which table(s) in the object file?
 - A **(non-static) user-defined function**
 - A **local variable**
 - A **library function**

Linker (Review)

- ❖ **Input:** Object files (*e.g.*, ELF, COFF)
 - `foo.o`
 - ❖ **Output:** executable binary program
 - `a.out`
-
- ❖ Combines several object files into a single executable (*linking*)
 - ❖ Enables separate compilation/assembling of files
 - Changes to one file do not require recompiling of whole program

Linking (Review)

- 1) Take text segment from each `.o` file and put them together
- 2) Take data segment from each `.o` file, put them together, and concatenate this onto end of text segments
- 3) Resolve References
 - Go through Relocation Table; handle each entry



Disassembling Object Code (Review)

❖ Disassembled:

```
0000000000400536 <sumstore>:  
 400536:  48 01 fe      add    %rdi,%rsi  
 400539:  48 89 32      mov    %rsi,(%rdx)  
 40053c:  c3           retq
```

❖ **Disassembler** (`objdump -d sum`)

- Useful tool for examining object code (`man 1 objdump`)
- Analyzes bit pattern of series of instructions
- Produces approximate rendition of assembly code
- Can run on either executable or object file

What Can be Disassembled?

```
% objdump -d WINWORD.EXE

WINWORD.EXE:      file format pei-i386

No symbols in "WINWORD.EXE".
Disassembly of section .text:

30001000 <.text>:
30001000:
30001001:
30001003:
30001005:
3000100a:
```

**Reverse engineering forbidden by
Microsoft End User License Agreement**

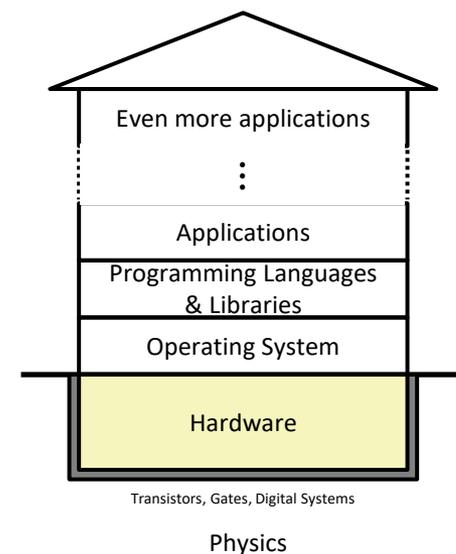
- ❖ Anything that can be interpreted as executable code
- ❖ Disassembler examines bytes and attempts to reconstruct assembly source

Loader (Review)

- ❖ **Input:** executable binary program, command-line arguments
 - `./a.out arg1 arg2`
 - ❖ **Output:** <program is run>
-
- ❖ Loader duties primarily handled by OS/kernel
 - More about this when we learn about processes
 - ❖ Memory sections (Instructions, Static Data, Stack) are set up
 - ❖ Registers are initialized

The Hardware/Software Interface

- ❖ Topic Group 1: **Data**
 - Memory, Data, Integers, Floating Point, **Arrays**, Structs



- ❖ How do we store information for other parts of the house of computing to access?
 - How do we represent data and what limitations exist?
 - What design decisions and priorities went into these encodings?

Data Structures in C

❖ Arrays

- One-dimensional
- Multidimensional (nested)
- Multilevel

❖ Structs

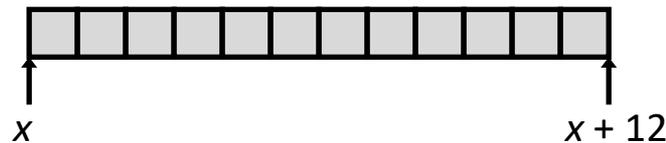
- Alignment

Array Allocation (Review)

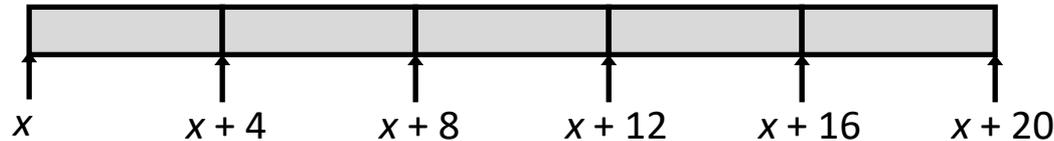
❖ Basic Principle

- $\mathbf{T} \ A[N]; \rightarrow$ array of data type \mathbf{T} and length N
- *Contiguously* allocated region of $N * \text{sizeof}(\mathbf{T})$ bytes
- Identifier A returns address of array (type \mathbf{T}^*)

```
char msg[12];
```



```
int val[5];
```



```
double a[3];
```



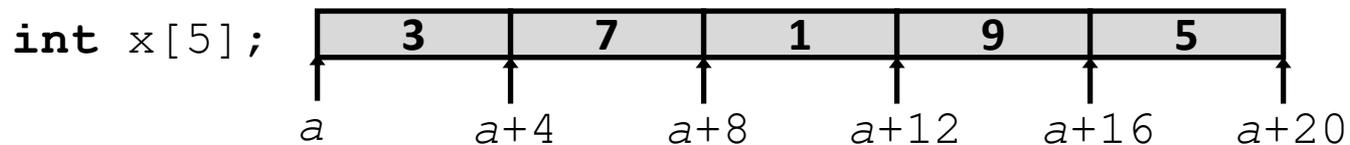
```
char* p[3];  
(or char *p[3];)
```



Array Access (Review)

❖ Basic Principle

- $\mathbf{T} \ A[N]; \rightarrow$ array of data type \mathbf{T} and length N
- Identifier A returns address of array (type \mathbf{T}^*)



❖ Reference

Type

Value

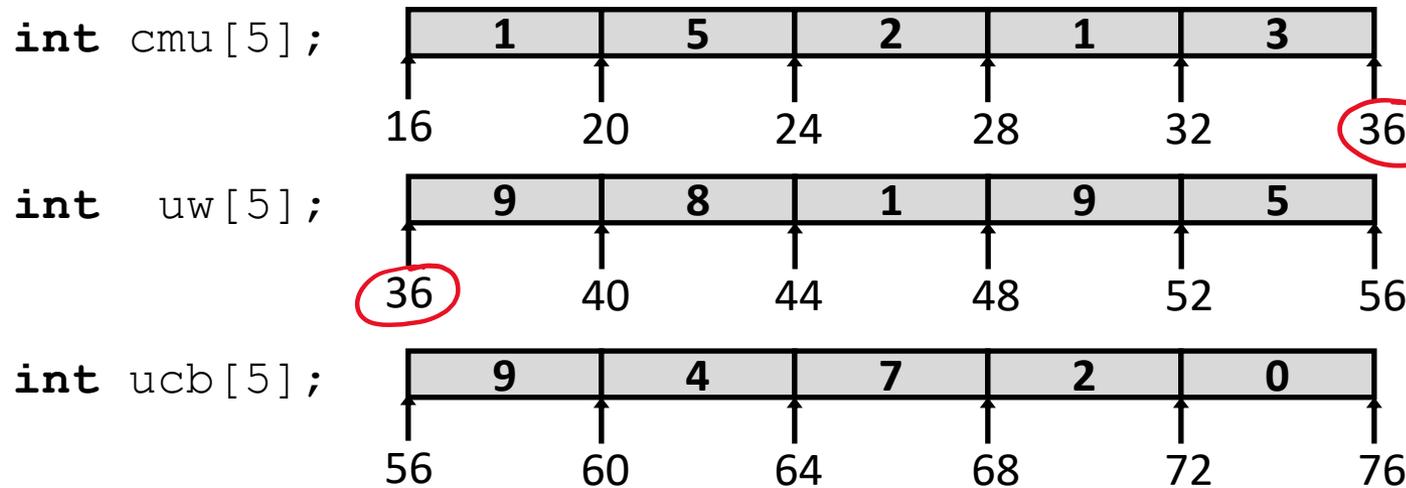
<code>x[4]</code>	<code>int</code>	5
<code>x</code>	<code>int*</code>	<code>a</code>
<code>x+1</code>	<code>int*</code>	<code>a + 4</code>
<code>&x[2]</code>	<code>int*</code>	<code>a + 8</code>
<code>x[5]</code>	<code>int</code>	?? (whatever's in memory at addr <code>x+20</code>)
<code>*(x+1)</code>	<code>int</code>	7
<code>x+i</code>	<code>int*</code>	<code>a + 4*i</code>

Array Example

brace-enclosed list initialization

```
// arrays of ZIP code digits
int cmu[5] = { 1, 5, 2, 1, 3 };
int uw[5] = { 9, 8, 1, 9, 5 };
int ucb[5] = { 9, 4, 7, 2, 0 };
```

20 bytes each



no gap in this example

- ❖ Example arrays happened to be allocated in successive 20 byte blocks
 - Not guaranteed to happen in general

(could have allocated variables in-between)

C Details: Arrays and Pointers

- ❖ Arrays are (almost) identical to pointers
 - `char* string` and `char string[]` are nearly identical declarations
 - Differ in subtle ways: initialization, `sizeof()`, etc.
- ❖ An array name is an expression (not a variable) that returns the address of the array
 - It *looks* like a pointer to the first (0th) element
 - `*ar` same as `ar[0]`, `*(ar+2)` same as `ar[2]`
 - An array name is read-only (no assignment) because it is a *label*
 - Cannot use `"ar = <anything>"`

C Details: Arrays and Functions

- ❖ Declared arrays only allocated while the scope is valid:

```
char* foo() {  
    char string[32]; ...;  
    return string;  
}
```

BAD!

- ❖ An array is passed to a function as a pointer:
 - Array size gets lost!

```
int foo(int ar[], unsigned int size) {  
    ... ar[size-1] ...  
}
```

Really int* ar

Must explicitly
pass the size!

Data Structures in C

❖ Arrays

- One-dimensional
- **Multidimensional (nested)**
- Multilevel

❖ Structs

- Alignment

Nested Array Example

```
int sea[4][5] =  
  {{ 9, 8, 1, 9, 5 },  
   { 9, 8, 1, 0, 5 },  
   { 9, 8, 1, 0, 3 },  
   { 9, 8, 1, 1, 5 }};
```

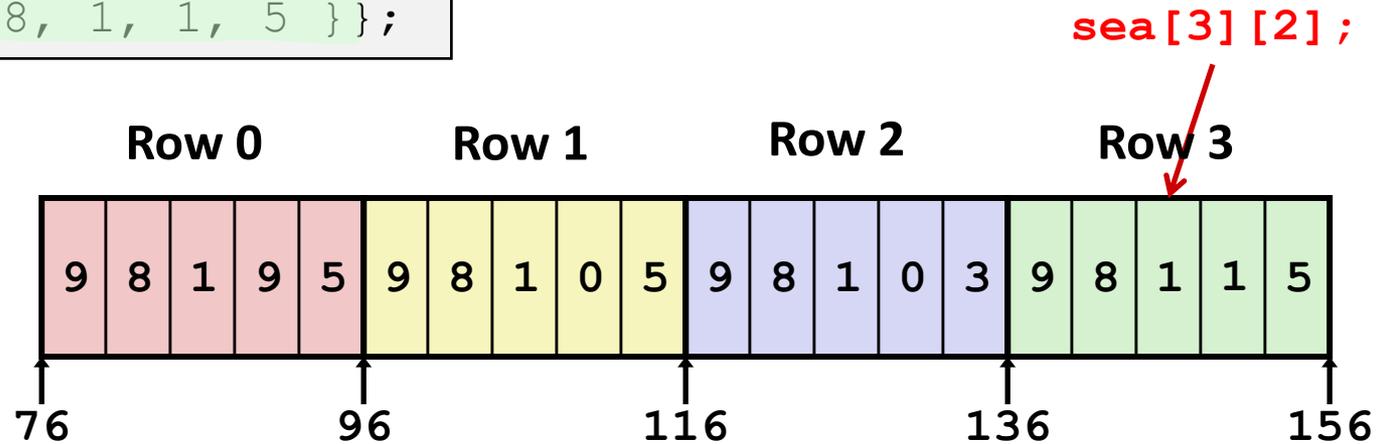
Remember, $\mathbf{T} \ A[N]$ is an array with elements of type \mathbf{T} , with length N

- ❖ What is the layout in memory?

Nested Array Example

```
int sea[4][5] =  
  { { 9, 8, 1, 9, 5 },  
    { 9, 8, 1, 0, 5 },  
    { 9, 8, 1, 0, 3 },  
    { 9, 8, 1, 1, 5 } };
```

Remember, $\mathbf{T} \ A[N]$ is an array with elements of type \mathbf{T} , with length N



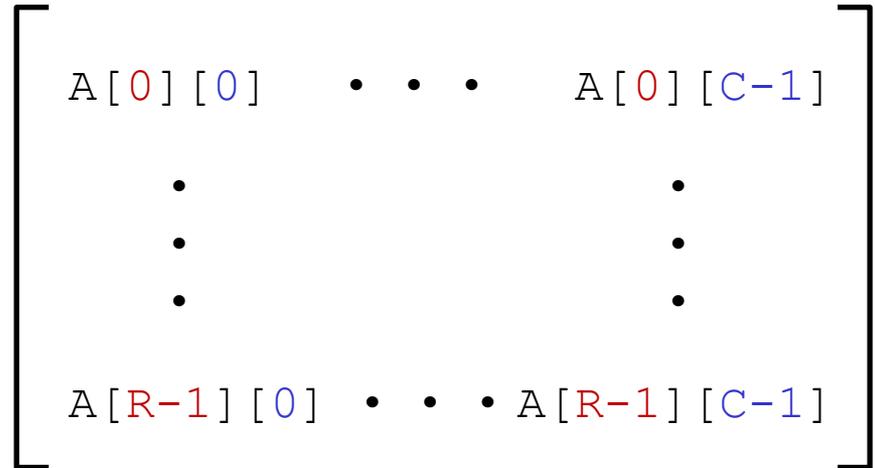
- ❖ “Row-major” ordering of all elements
 - Elements in the same row are contiguous
 - Guaranteed (in C)

Two-Dimensional (Nested) Arrays

❖ Declaration: $\mathbf{T} \ A[R][C];$

- 2D array of data type T
- R rows, C columns
- Each element requires $\mathbf{sizeof}(T)$ bytes

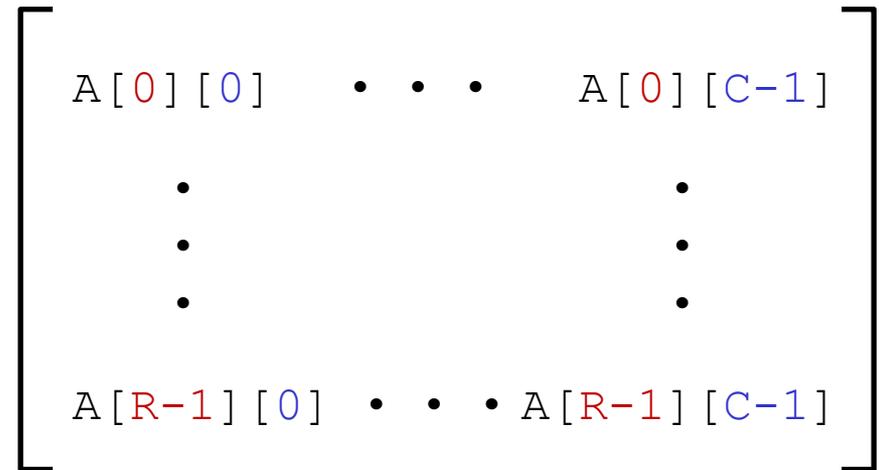
❖ Array size?



Two-Dimensional (Nested) Arrays

❖ Declaration: $\mathbf{T} \ A[R][C];$

- 2D array of data type T
- R rows, C columns
- Each element requires **sizeof(T)** bytes

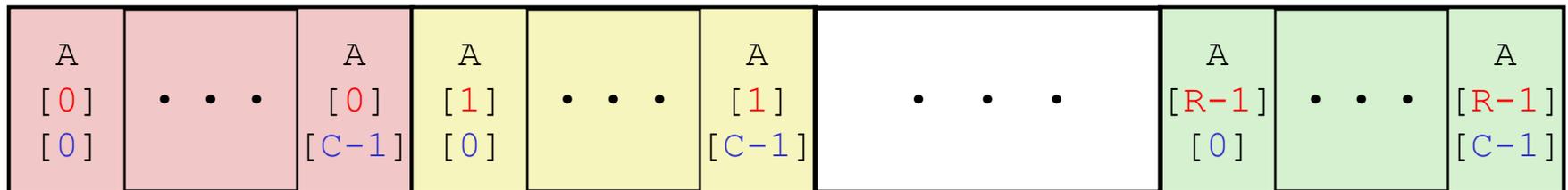


❖ Array size:

- $R * C * \mathbf{sizeof(T)}$ bytes

❖ Arrangement: **row-major** ordering

```
int A[R][C];
```



4 * R * C bytes

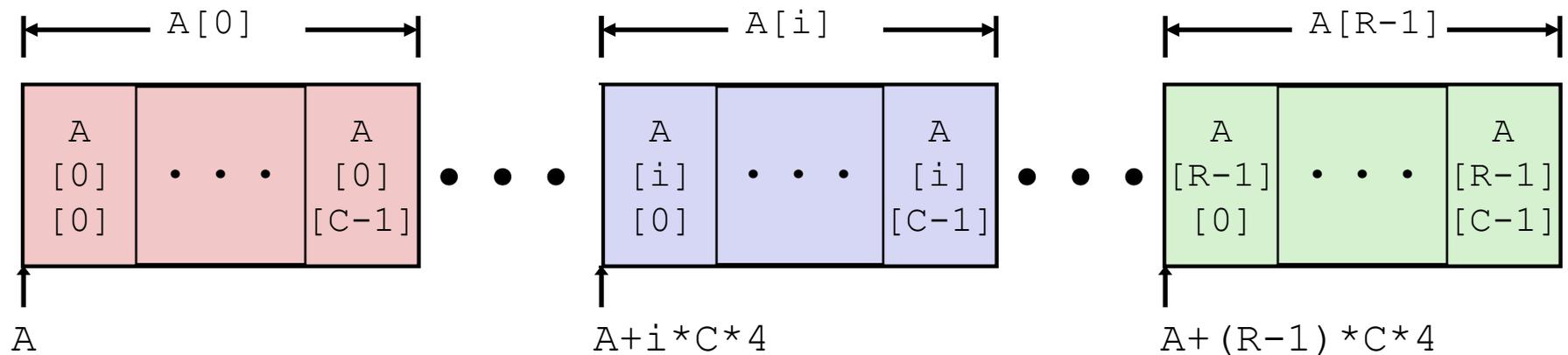
Nested Array Row Access

❖ Row vectors

■ Given \mathbf{T} `A[R][C]`,

- `A[i]` is an array of `C` elements (“row `i`”)
- `A` is address of array
- Starting address of row `i` = `A + i * (C * sizeof(\mathbf{T}))`

```
int A[R][C];
```

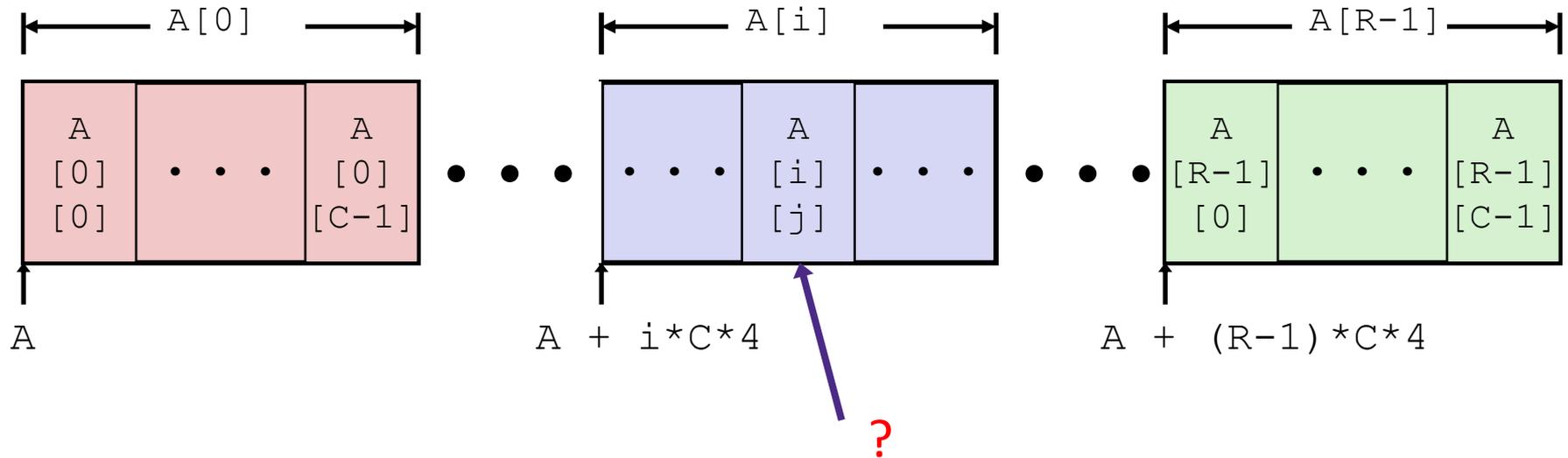


Nested Array Element Access

❖ Array Elements

- $A[i][j]$ is element of type **T**; let `sizeof(T) = t` bytes
- Address of $A[i][j]$ is

```
int A[R][C];
```



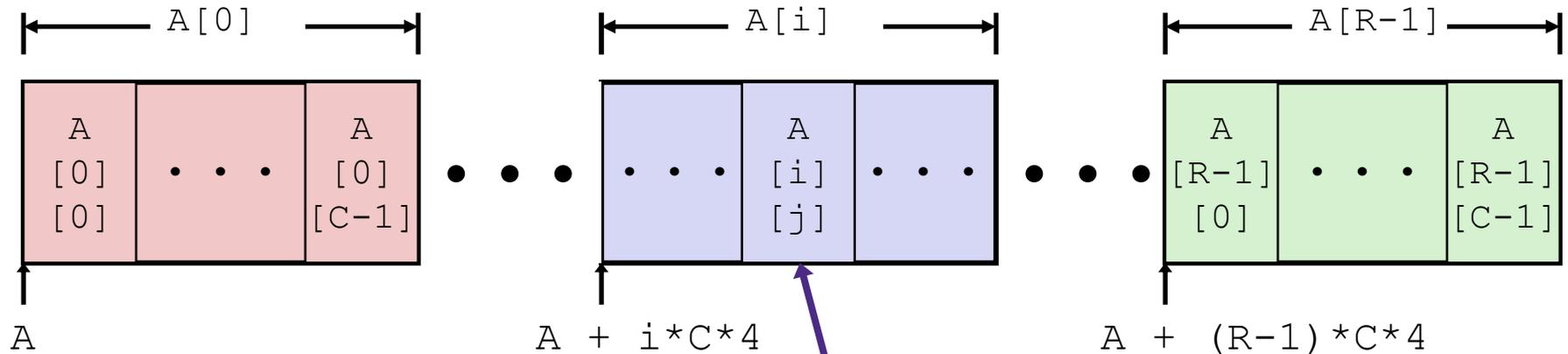
Nested Array Element Access

❖ Array Elements

- $A[i][j]$ is element of type **T**; let $\text{sizeof}(T) = t$ bytes
- Address of $A[i][j]$ is

$$A + i * (C * t) + j * t = A + (i * C + j) * t$$

```
int A[R][C];
```



$$A + i * C * 4 + j * 4$$

Data Structures in C

❖ Arrays

- One-dimensional
- Multidimensional (nested)
- **Multilevel**

❖ Structs

- Alignment

Multilevel Array Example

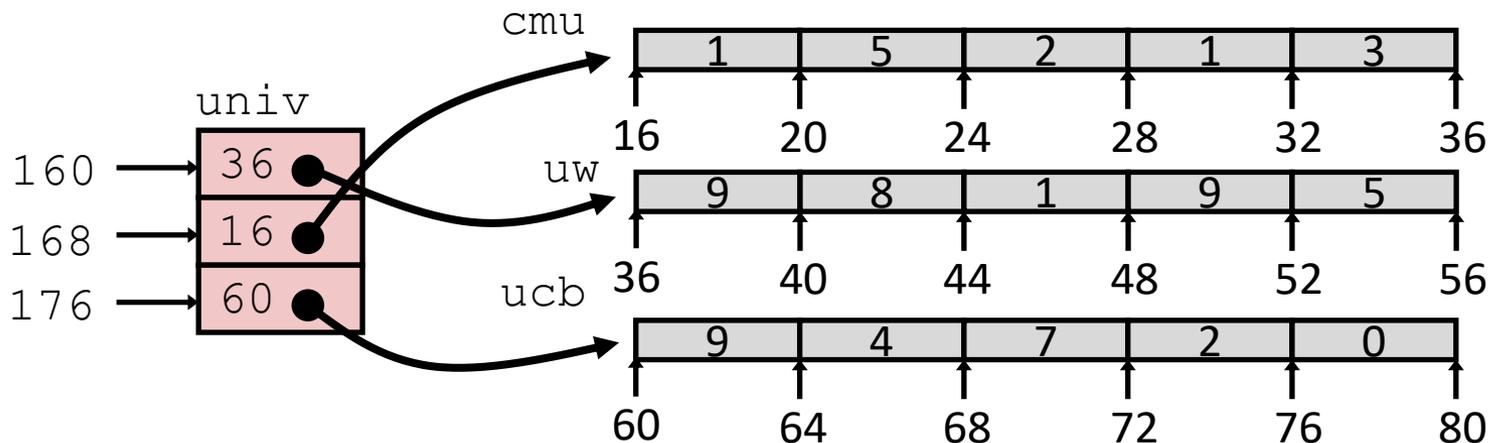
Note: this is how Java represents multidimensional arrays!

❖ Multilevel Array Declaration(s):

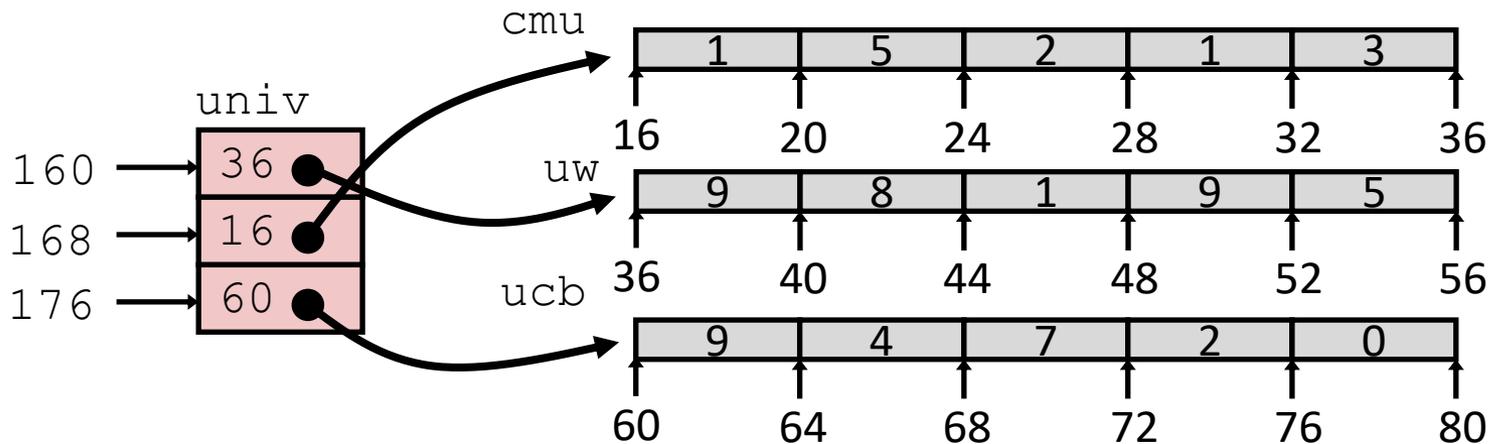
```
int cmu[5] = { 1, 5, 2, 1, 3 };  
int uw[5] = { 9, 8, 1, 9, 5 };  
int ucb[5] = { 9, 4, 7, 2, 0 };
```

```
int* univ[3] = {uw, cmu, ucb};
```

- Variable `univ` denotes array of 3 pointer elements
- Each pointer points to a separate array of `ints`
 - *Could* have inner arrays of different lengths!



Multilevel Array Element Access



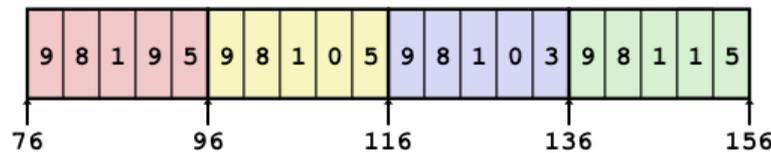
```
int get_univ_digit (int index, int digit) {
    return univ[index][digit];
}
```

- ❖ `Mem[Mem[univ+8*index]+4*digit]`
 - Must do **two memory reads**: (1) get pointer to row array, (2) access element within array

Array Element Accesses

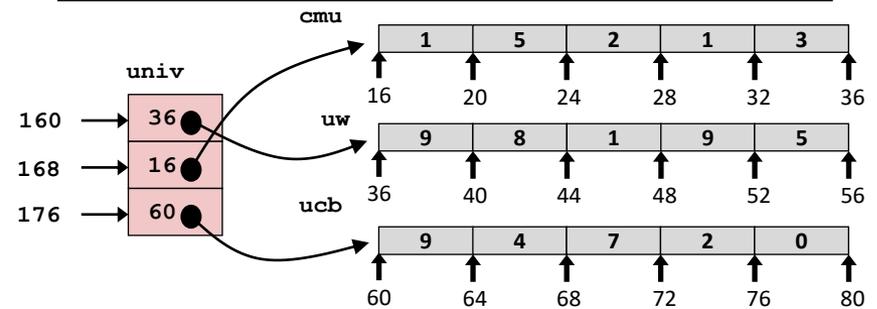
Multidimensional array:

```
int get_sea_digit
(int index, int digit)
{
    return sea[index][digit];
}
```



Multilevel array:

```
int get_univ_digit
(int index, int digit)
{
    return univ[index][digit];
}
```



❖ Accesses *look* the same, but aren't:

$\text{Mem}[\text{sea} + 20 * \text{index} + 4 * \text{digit}]$ $\text{Mem}[\text{Mem}[\text{univ} + 8 * \text{index}] + 4 * \text{digit}]$

❖ Memory layout is different:

- One array declaration = one contiguous block of memory

Summary

- ❖ Building an executable
 - Multistep process: compiling, assembling, linking
 - Object code finished by linker using symbol and relocation tables to produce machine code (with finalized addresses)
 - Loader sets up initial memory from executable
- ❖ Arrays
 - Contiguous allocations of memory
 - **No bounds checking** (and no default initialization)
 - Can usually be treated like a pointer to first element
 - Multidimensional → array of arrays in one contiguous block
 - Multilevel → array of pointers to arrays
 - Each array/part separate in memory