Memory, Data, & Addressing II

CSE 351 Spring 2020

Instructor:

Porter Jones

Teaching Assistants:

Amy Xu Callum Walker Sam Wolfson Tim Mandzyuk



http://xkcd.com/138/

Administrivia

- Questions doc for today: <u>https://tinyurl.com/CSE351-6-26</u>
- Assignments Overview
- ✤ Lab 0 due Tonight (6/26) 11:59pm
- ✤ hw2 due Monday (6/29) 10:30am
- ✤ hw3 due Wednesday (7/1) 10:30am
- Lab 1a released today, due a week from Monday (7/6)
 - Suggested Due Date is 7/3 to give time for lab1b (due 7/10)
 - Pointers in C
 - Reminder: last submission graded, individual work
- Study group survey results released today!
 - Can still fill out the survey if interested in finding a group

Late Days

- You are given 7 late days for the whole quarter
 - Late days can only apply to Labs & Unit Summaries
 - No benefit to having leftover late days
- Count lateness in *days* (even if just by a second)
 - Special: weekends count as one day
 - No submissions accepted more than two days late
- The late penalty for using more than 7 late days is a 20% deduction of your score per excess day
 - Only late work is eligible for penalties
 - Penalties applied at end of quarter to maximize your grade
- Use at own risk don't want to fall too far behind
 - Intended to allow for unexpected circumstances

Where We Left Off: Byte Ordering

- Big-endian (SPARC, z/Architecture)
 - Least significant byte has highest address
- Little-endian (x86, x86-64)
 - Least significant byte has lowest address
- Bi-endian (ARM, PowerPC)
 - Endianness can be specified as big or little
- Example: 4-byte data 0xa1b2c3d4 at address 0x100



int x = 12345;

 $// \text{ or } x = 0 \times 3039;$

Byte Ordering Examples

Decimal:	12345							
Binary:	0011	0000	0011	1001				
Hex:	3	0	3	9				





Polling Question

- We store the value 0x 01 02 03 04 as a *word* at address 0x100 in a big-endian, 64-bit machine
- What is the byte of data stored at address 0x104?
 - Vote at <u>http://pollev.com/pbjones</u>
 - A. 0x04
 - **B.** 0x40
 - C. 0x01
 - D. 0x10
 - E. We're lost...

Endianness

- Endianness only applies to memory storage
- Often programmer can ignore endianness because it is handled for you
 - Bytes wired into correct place when reading or storing from memory (hardware)
 - Compiler and assembler generate correct behavior (software)
- Endianness still shows up:
 - Logical issues: accessing different amount of data than how you stored it (e.g. store int, access byte as a char)
 - Need to know exact values to debug memory errors
 - Manual translation to and from machine code (in 351)

Memory, Data, and Addressing

- Representing information as bits and bytes
 - Binary, hexadecimal, fixed-widths
- Organizing and addressing data in memory
 - Memory is a byte-addressable array
 - Machine "word" size = address size = register size
 - Endianness ordering bytes in memory
- Manipulating data in memory using C
 - Assignment
 - Pointers, pointer arithmetic, and arrays
- Boolean algebra and bit-level manipulations

Addresses and Pointers in C

- ✤ & = "address of" operator
- * * = "value at address" or "dereference" operator

- A variable is represented by a location
- ◆ Declaration ≠ initialization (initially holds "garbage")
- * int x, y;
 - x is at address 0x04, y is at 0x18

	0x00	0x01	0x02	0x03	
0x00	A7	00	32	00	
0x04	00	01	29	F3	Х
0x08	EE	EE	EE	EE	
0x0C	FA	CE	CA	FE	
0x10	26	00	00	00	
0x14	00	00	10	00	
0x18	01	00	00	00	У
0x1C	FF	00	F4	96	
0x20	DE	AD	BE	EF	
0x24	00	00	00	00	

- A variable is represented by a location
- ◆ Declaration ≠ initialization (initially holds "garbage")
- * int x, y;
 - x is at address 0x04, y is at 0x18

32-bit example (pointers are 32-bits wide)

- Ieft-hand side = right-hand side;
 - LHS must evaluate to a *location*
 - RHS must evaluate to a value (could be an address)
 - Store RHS value at LHS location
- * int x, y;
- * x = 0;

32-bit example (pointers are 32-bits wide)

- Ieft-hand side = right-hand side;
 - LHS must evaluate to a location
 - RHS must evaluate to a value (could be an address)
 - Store RHS value at LHS location
- * int x, y;
- * x = 0;
- * y = 0x3CD02700;

32-bit example (pointers are 32-bits wide)

- Ieft-hand side = right-hand side;
 - LHS must evaluate to a *location*
 - RHS must evaluate to a value (could be an address)
 - Store RHS value at LHS location
- * int x, y;
- * x = 0;
- * y = 0x3CD02700;
- * x = y + 3;
 - Get value at y, add 3, store in x

32-bit example (pointers are 32-bits wide)

- Ieft-hand side = right-hand side;
 - LHS must evaluate to a location
 - RHS must evaluate to a value (could be an address)
 - Store RHS value at LHS location
- * int x, y;
- * x = 0;
- * y = 0x3CD02700;
- * x = y + 3;
 - Get value at y, add 3, store in x
- * int* z;
 - z is at address 0x20

32-bit example (pointers are 32-bits wide)

& = "address of"

* = "dereference"

- Ieft-hand side = right-hand side;
 - LHS must evaluate to a location
 - RHS must evaluate to a value (could be an address)
 - Store RHS value at LHS location
- * int x, y;
- * x = 0;
- * y = 0x3CD02700;
- * x = y + 3;
 - Get value at y, add 3, store in x

* int* z = &y + 3;

Get address of y, "add 3", store in z

Pointer Arithmetic

- Pointer arithmetic is scaled by the size of target type
 - In this example, sizeof(int) = 4
- * int* z = &y + 3;
 - Get address of y, add 3*sizeof (int), store in z

•
$$&y = 0x18 = 1*16^{1} + 8*16^{0} = 24$$

 $24 + 3*(4) = 36 = 2*16^{1} + 4*16^{0} = 0x24$

- Pointer arithmetic can be dangerous!
 - Can easily lead to bad memory accesses
 - Be careful with data types and casting

- * int x, y;
- * x = 0;
- y = 0x3CD02700;
- * x = y + 3;
 - Get value at y, add 3, store in x
- * int* z = &y + 3;
 - Get address of y, add 12, store in z

What does this do?

32-bit example (pointers are 32-bits wide)

- * int x, y;
- * x = 0;
- * y = 0x3CD02700;
- * x = y + 3;
 - Get value at y, add 3, store in x

* int* z = &y + 3;

- Get address of y, add 12, store in z
 The target of a pointer is also a location
 * z = y;
 - Get value of y, put in address stored in z

32-bit example (pointers are 32-bits wide)

Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

Declaration: int a[6];

Indexing: a[0] = 0x015f; a[5] = a[0]; Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

&a[i] is the address of a[0] plus i times the element size in bytes

Declaration: int a[6];

Indexing: $a[0] = 0 \times 015f;$ a[5] = a[0];

No bounds $a[6] = 0 \times BAD;$ checking: $a[-1] = 0 \times BAD;$ Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

&a[i] is the address of a[0] plus i times the element size in bytes

Declaration: int a[6];

Indexing: a[0] = 0x015f; a[5] = a[0];

No bounds a[6] = 0xBAD;checking: a[-1] = 0xBAD;

Pointers: int* p; equivalent { p = a; p = &a[0]; *p = 0xA; Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

a[i] is the address of a[0] plus i times the element size in bytes

Declaration: int a[6];

Indexing: a[0] = 0x015f; a[5] = a[0];

No bounds a[6] = 0xBAD;checking: a[-1] = 0xBAD;

Pointers: int* p; equivalent $\begin{cases} p = a; \\ p = &a[0]; \\ p = &a[0]; \\ a[2] \\ a[4] \end{cases}$

array indexing = address arithmetic (both scaled by the size of the type)

equivalent
$$\begin{cases} p[1] = 0xB; \\ *(p+1) = 0xB; \\ p = p + 2; \end{cases}$$

Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

&a[i] is the address of a[0] plus i times the element size in bytes

Declaration: int a[6];

Indexing: a[0] = 0x015f; a[5] = a[0];

No bounds a[6] = 0xBAD;checking: a[-1] = 0xBAD;

Pointers: int* p; equivalent $\begin{cases} p = a; \\ p = &a[0]; \\ p = &a[0]; \\ a[2] \\ a[4] \end{cases}$

array indexing = address arithmetic (both scaled by the size of the type)

equivalent
$$\begin{cases} p[1] = 0xB; \\ *(p+1) = 0xB; \\ p = p + 2; \end{cases}$$

*p = a[1] + 1;

Arrays are adjacent locations in memory storing the same type of data object

a (array name) returns the array's address

&a[i] is the address of a[0] plus i times
the element size in bytes

Question: The variable values after Line 3 executes are shown on the right. What are they after Line 4 & 5?

Vote at <u>http://pollev.com/pbjones</u>

1	<pre>void main() {</pre>		Data Address
2	int $a[] = \{5, 10\};$		(decimal) (decimal)
3	int* p = a;	a[0] a[1]	5 100
4	p = p + 1;	~[+]	
5	*p = *p + 1;	р	100
6	}		

	P	*P	<mark>a</mark> [0]	<mark>a</mark> [1]	I	P	*P	<mark>a</mark> [0]	<mark>a</mark> [1]
(A)	101	10	5	10	then	101	11	5	11
(B)	104	10	5	10	then	104	11	5	11
(C)	100	6	6	10	then	101	6	6	10
(D)	100	6	6	10	then	104	6	6	10

Representing strings

- C-style string stored as an array of bytes (char*)
 - Elements are one-byte ASCII codes for each character
 - No "String" keyword, unlike Java

32	snace	48	0	1	64	ര	80	P	96	`	1	112	n
22	I		1		65	<u>د</u>	Q1		07	2		112	۳ ۵
55	:	49	1		05	~	01	ų	57	a		113	ч
34		50	2		66	В	82	R	98	b		114	r
35	#	51	3		67	С	83	S	99	С		115	S
36	\$	52	4		68	D	84	Т	100	d		116	t
37	%	53	5		69	Ε	85	U	101	е		117	u
38	&	54	6		70	F	86	V	102	f		118	v
39	,	55	7		71	G	87	W	103	g		119	w
40	(56	8		72	н	88	Х	104	h		120	х
41)	57	9		73	I.	89	Y	105	I		121	у
42	*	58	:		74	J	90	Z	106	j		122	z
43	+	59	;		75	к	91	[107	k		123	{
44	,	60	<		76	L	92	\	108	I		124	1
45	-	61	=		77	Μ	93]	109	m		125	}
46		62	>		78	Ν	94	Λ	110	n		126	~
47	/	63	?		79	0	95	_	111	0		127	del

ASCII: American Standard Code for Information Interchange

Null-Terminated Strings

Example: "Ice Creamery" stored as a 13-byte array

- Last character followed by a 0 byte (' \ 0 ') (a.k.a. "null terminator")
 - Must take into account when allocating space in memory
 - Note that $0' \neq \langle 0'$ (*i.e.* character 0 has non-zero value)
- How do we compute the length of a string?
 - Traverse array until null terminator encountered

C (char = 1 byte)

Endianness and Strings

- Byte ordering (endianness) is not an issue for 1-byte values
 - The whole array does not constitute a single value
 - Individual elements are values; chars are single bytes

Examining Data Representations

- Code to print byte representation of data
 - Any data type can be treated as a byte array by casting it to char
 - C has unchecked casts !! DANGER !!

```
void show_bytes(char* start, int len) {
    int i;
    for (i = 0; i < len; i++)
        printf("%p\t0x%.2x\n", start+i, *(start+i));
    printf("\n");
}</pre>
```

printf directives:				
Print pointer				
Tab				
Print value as hex				
New line				

Examining Data Representations

- Code to print byte representation of data
 - Any data type can be treated as a byte array by casting it to char
 - C has unchecked casts !! DANGER !!

```
void show_bytes(char* start, int len) {
    int i;
    for (i = 0; i < len; i++)
        printf("%p\t0x%.2x\n", start+i, *(start+i));
    printf("\n");
}</pre>
```

```
void show_int(int x) {
    show_bytes( (char *) &x, sizeof(int));
}
```

show_bytes Execution Example

```
int x = 12345; // 0x00003039
printf("int x = %d;\n", x);
show_int(x); // show_bytes((char *) &x, sizeof(int));
```

- Result (Linux x86-64):
 - Note: The addresses will change on each run (try it!), but fall in same general range

int $x = 12345;$	
0x7fffb7f71dbc	0x39
0x7fffb7f71dbd	0x30
0x7fffb7f71dbe	0x00
0x7fffb7f71dbf	0x00

Summary

- Assignment in C results in value being put in memory location
- Pointer is a C representation of a data address
 - & = "address of" operator
 - * = "value at address" or "dereference" operator
- Pointer arithmetic scales by size of target type
 - Convenient when accessing array-like structures in memory
 - Be careful when using particularly when *casting* variables
- Arrays are adjacent locations in memory storing the same type of data object
 - Strings are null-terminated arrays of characters (ASCII)