Notation guide:

This Quarter	Variable	Previous Quarters
K	Block size	В
C	Cache size	
N	Associativity	\mathbf{E}
A	Address width	m
Т	Tag field width	\mathbf{t}
I	Index field width	$^{\rm k,s}$
0	Offset field width	n,b
S	Number of sets	\mathbf{S}

Address translation:

Looking at the original address:

Most-significant bits \rightarrow Tag bits | Set index bits | Block offset bits | \leftarrow Least-significant bits |

Things to think about:

Bits dedicated to block offset: If we have block size X, how many bits do we need to describe X things?

Bits dedicated to set index: If we have X sets, how many bits do we need to describe X things?

Formulas to remember:

Average Memory Access Time (AMAT) = Hit time + Miss rate \times Miss penalty

General cache organization:

