Structs & Alignment

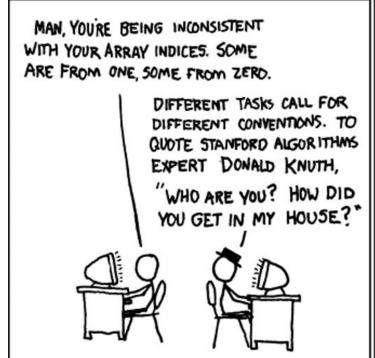
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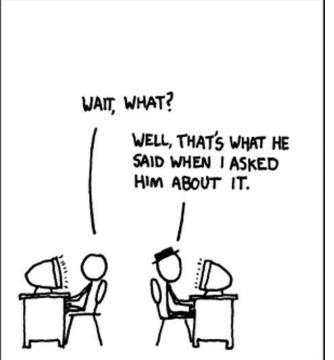
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Administrivia

- Lab 2 (x86-64) due TONIGHT (5/01)
- Homework 3, due Wednesday (5/8)
 - On midterm material, but due after the midterm
- Midterm (Fri 5/03, 4:30-5:30pm in KNE 130)
 - Review Session: Thurs 5/02,6:30-8:30pm in Sieg 134
 - No lecture on Friday 5/03
 - Ruth will hold office hours instead
 - Fri 11:30am-12:30pm in CSE 460
 - Fri 2:30-3:30pm in CSE 460

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Roadmap

C:

```
car *c = malloc(sizeof(car));
c->miles = 100;
c->gals = 17;
float mpg = get_mpg(c);
free(c);
```

Java:

Memory & data
Integers & floats
x86 assembly
Procedures & stacks
Executables

Arrays & structs
Memory & caches
Processes
Virtual memory
Memory allocation
Java vs. C

Assembly language:

```
get_mpg:
    pushq %rbp
    movq %rsp, %rbp
    ...
    popq %rbp
    ret
```

OS:

Windows 10 OS X Yosemite

Machine code:

Computer system:

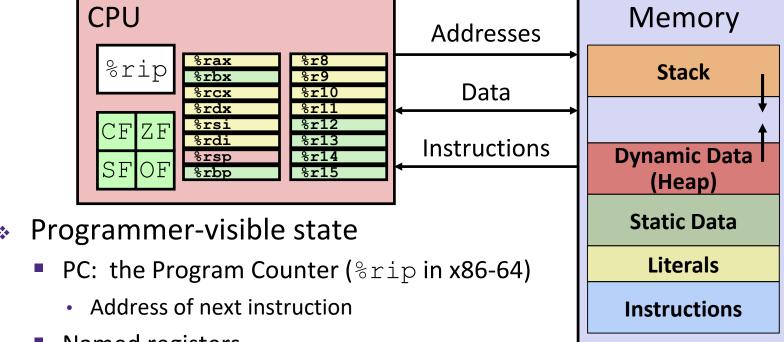








Assembly Programmer's View



- Named registers
 - Together in "register file"
 - Heavily used program data
- Condition codes
 - Store status information about most recent arithmetic operation
 - Used for conditional branching

- Memory
 - Byte-addressable array
 - Code and user data
 - Includes the Stack (for supporting procedures)

x86-64 Instructions

Data movement

mov, movs, movz, ...

Arithmetic

add, sub, shl, sar, lea, ...

Control flow

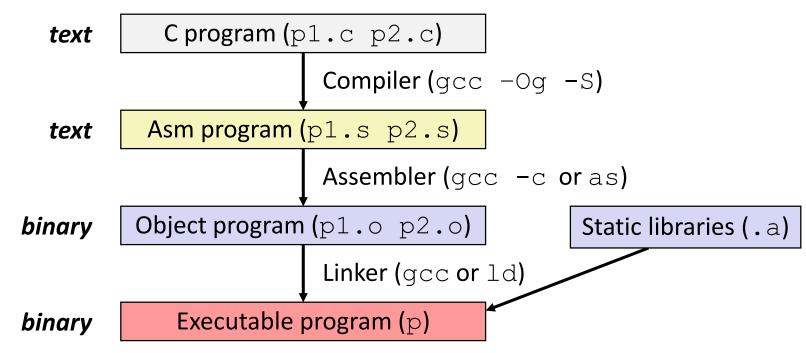
cmp, test, j*, set*, ...

Stack/procedures

push, pop, call, ret, ...

Turning C into Object Code

- Code in files p1.c p2.c
- ❖ Compile with command: gcc -Og p1.c p2.c -o p
 - Use basic optimizations (-Og) [New to recent versions of GCC]
 - Put resulting machine code in file p



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Assembling

Executable has addresses

```
000000000004004f6 <pcount r>:
     4004f6: b8 00 00 00 00
                                     $0x0, %eax
                              mov
     4004fb: 48 85 ff
                              test %rdi,%rdi
     4004fe: 74 13
                                     400513 <pcount r+0x1d>
                              jе
     400500: 53
                                     %rbx
                              push
assembler
     400501: 48 89 fb
                                     %rdi,%rbx
                              mov
     400504: 48 d1 ef
                              shr
                                     %rdi
     400507: e8 ea ff ff ff
                              callq 4004f6 <pcount r>
     40050c: 83 e3 01
                              and
                                     $0x1, %ebx
     40050f: 48 01 d8
                              add
                                     %rbx,%rax
     400512: 5b
                                     %rbx
                              pop
     400513: f3 c3
                              rep ret
```

- gcc -g pcount.c -o pcount
- objdump -d pcount

A Picture of Memory (64-bit view)

```
00000000004004f6 <pcount r>:
 4004f6: b8 00 00 00 00
                                  $0x0, %eax
                           mov
 4004fb: 48 85 ff
                                  %rdi,%rdi
                           test
 4004fe: 74 13
                                  400513 <pcount r+0x1d>
                           jе
 400500:
          53
                           push
                                  %rbx
 400501: 48 89 fb
                           mov
                                  %rdi,%rbx
 400504: 48 d1 ef
                           shr
                                  %rdi
                                  4004f6 count r>
 400507: e8 ea ff ff ff
                           callq
                                  $0x1, %ebx
 40050c: 83 e3 01
                           and
 40050f:
          48 01 d8
                           add
                                  %rbx,%rax
 400512:
          5b
                                  %rbx
                           pop
 400513:
          f3 c3
                           rep ret
```

0 8	1 9	2 a	3 b	4 c	5 d	6 e	7 f	_
								0x00
								0x08
								0x10
• • •								
						b8	00	0x4004f0
00	00	00	48	85	ff	74	13	0x4004f8
53	48	89	fb	48	d1	ef	8	0x400500
ea	ff	ff	ff	83	e3	01	48	0x400508
01	d8	5b	f3	с3				0x400510

Roadmap

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Arrays & structs

Memory & caches Processes Virtual memory Memory allocation Java vs. C

Assembly language:

```
get_mpg:
    pushq %rbp
    movq %rsp, %rbp
    ...
    popq %rbp
    ret
```

Machine code:

OS:



Computer system:

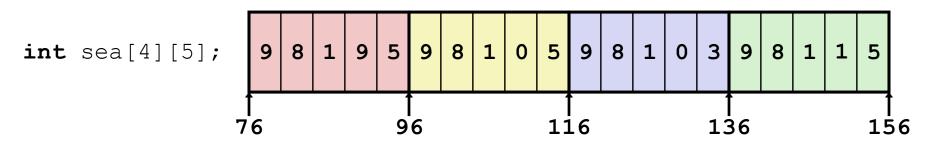






Peer Instruction Question

- Which of the following statements is FALSE?
 - Vote at http://pollev.com/rea



- A. sea[4][-2] is a *valid* array reference
- B. sea[1][1] makes two memory accesses
- C. sea [2] [1] will *always* be a higher address than sea [1] [2]
- D. sea [2] is calculated using only lea
- E. We're lost...

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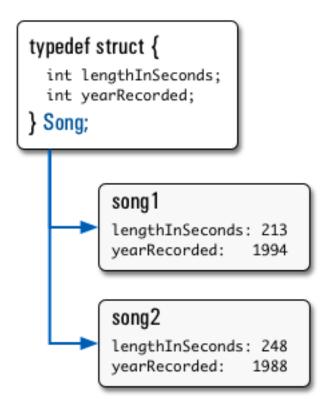
Data Structures in Assembly

- Arrays
 - One-dimensional
 - Multi-dimensional (nested)
 - Multi-level
- * Structs
 - Alignment
- Unions

Structs in C

- Way of defining compound data types
- A structured group of variables, possibly including other structs

```
typedef struct {
  int lengthInSeconds;
  int yearRecorded;
} Song;
Song song1;
song1.lengthInSeconds = 213;
                      = 1994;
song1.yearRecorded
Song song2;
song2.lengthInSeconds =
                        248;
song2.yearRecorded
                      = 1988;
```



}; **←**

struct name {

/* fields */

Easy to forget

semicolon!

Struct Definitions

- Structure definition:
 - Does NOT declare a variable
 - Variable type is "struct name" pointer

```
struct name name1, *pn, name_ar[3];
array
```

- Joint struct definition and typedef
 - Don't need to give struct a name in this case

```
struct nm {
   /* fields */
};
typedef struct nm name;
name n1;
typedef struct nm name;
name n1;
```

Scope of Struct Definition

- Why is placement of struct definition important?
 - What actually happens when you declare a variable?
 - Creating space for it somewhere!
 - Without definition, program doesn't know how much space

- Almost always define structs in global scope near the top of your C file
 - Struct definitions follow normal rules of scope

Accessing Structure Members

 Given a struct instance, access member using the . operator:

```
struct rec r1;
r1.i = val;
```

Given a pointer to a struct:

```
struct rec *r;
```

```
r = &r1; // or malloc space for r to point to
```

We have two options:

```
• Use * and . operators: (*r).i = val;
```

- Use \rightarrow operator for short: $r \rightarrow i = val;$
- In assembly: register holds address of the first byte
 - Access members with offsets

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
};
```

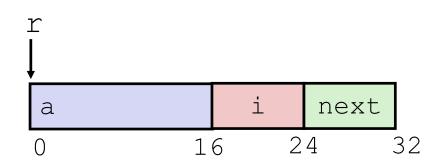
Java side-note

```
class Record { ... }
Record x = new Record();
```

- An instance of a class is like a pointer to a struct containing the fields
 - (Ignoring methods and subclassing for now)
 - So Java's x.f is like C's x->f or (*x).f
- In Java, almost everything is a pointer ("reference") to an object
 - Cannot declare variables or fields that are structs or arrays
 - Always a pointer to a struct or array
 - So every Java variable or field is ≤ 8 bytes (but can point to lots of data)

Structure Representation

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
} *r;
```



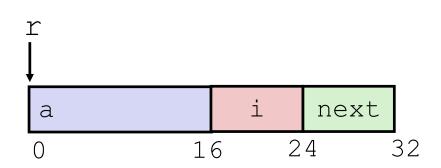
Characteristics

- Contiguously-allocated region of memory
- Refer to members within structure by names
- Members may be of different types

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Structure Representation

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
} *r;
```



- Structure represented as block of memory
 - Big enough to hold all of the fields
- Fields ordered according to declaration order
 - Even if another ordering would be more compact
- Compiler determines overall size + positions of fields
 - Machine-level program has no understanding of the structures in the source code

Accessing a Structure Member

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
} *r;
```

- r r->i
 a i next
 0 16 24 32
- Compiler knows the offset of each member within a struct
 - Compute as

```
*(r+offset)
```

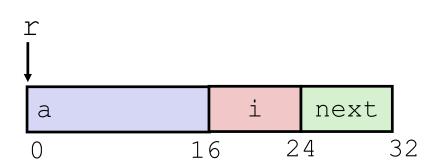
 Referring to absolute offset, so no pointer arithmetic

```
long get_i(struct rec *r)
{
   return r->i;
}
```

```
# r in %rdi, index in %rsi
movq 16(%rdi), %rax
ret
```

Exercise: Pointer to Structure Member

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
} *r;
```



```
long* addr_of_i(struct rec *r)
{
  return &(r->i);
}
```

```
# r in %rdi
_____,%rax
ret
```

```
struct rec** addr_of_next(struct rec *r)
{
   return &(r->next);
}
```

```
# r in %rdi
_____,%rax
ret
```

Generating Pointer to Array Element

```
struct rec {
   int a[4];
   long i;
   struct rec *next;
} *r;
```

- r r+4*index
 a i next
 0 16 24 32
- Generating Pointer to Array Element
 - Offset of each structure member determined at compile time
 - Compute as:
 r+4*index

```
int* find_addr_of_array_elem
  (struct rec *r, long index)
{
  return &r->a[index];
}
```

```
# r in %rdi, index in %rsi
leaq (%rdi,%rsi,4), %rax
ret
```

Review: Memory Alignment in x86-64

- Aligned means that any primitive object of K bytes must have an address that is a multiple of K
- Aligned addresses for data types:

K	Туре	Addresses
1	char	No restrictions
2	short	Lowest bit must be zero:0 ₂
4	int, float	Lowest 2 bits zero:00 ₂
8	long, double, *	Lowest 3 bits zero:000 ₂
16	long double	Lowest 4 bits zero:0000 ₂

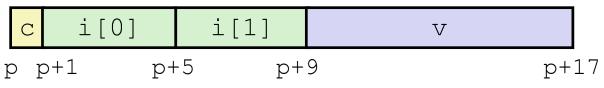
Alignment Principles

- Aligned Data
 - Primitive data type requires K bytes
 - Address must be multiple of K
 - Required on some machines; advised on x86-64

- Motivation for Aligning Data
 - Memory accessed by (aligned) chunks of bytes (width is system dependent)
 - Inefficient to load or store value that spans quad word boundaries
 - Virtual memory trickier when value spans 2 pages (more on this later)
 - Though x86-64 hardware will work regardless of alignment of data

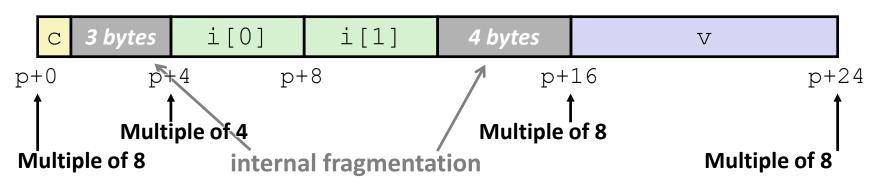
Structures & Alignment

Unaligned Data



```
struct S1 {
  char c;
  int i[2];
  double v;
} *p;
```

- Aligned Data
 - Primitive data type requires K bytes
 - Address must be multiple of K

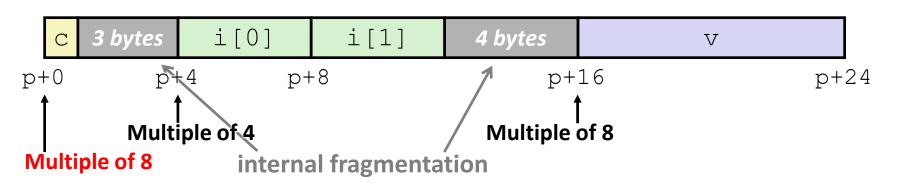


Satisfying Alignment with Structures (1)

- Within structure:
 - Must satisfy each element's alignment requirement
- Overall structure placement
 - Each structure has alignment requirement K_{\max}
 - K_{max} = Largest alignment of any element
 - Counts array elements individually as elements

```
struct S1 {
  char c;
  int i[2];
  double v;
} *p;
```

- Example:
 - K_{max} = 8, due to double element

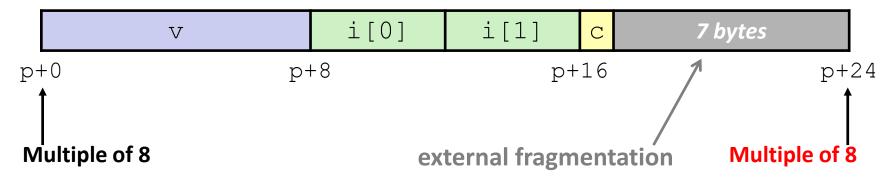


Satisfying Alignment with Structures (2)

- Can find offset of individual fields
 using offsetof()
 - Need to #include <stddef.h>
 - Example: offsetof(struct S2,c) returns 16

```
struct S2 {
  double v;
  int i[2];
  char c;
} *p;
```

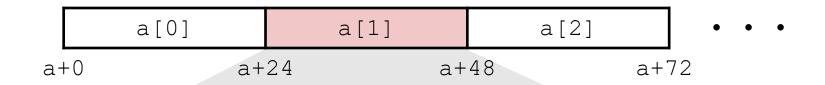
- * For largest alignment requirement K_{max} , overall structure size must be multiple of K_{max}
 - Compiler will add padding at end of structure to meet overall structure alignment requirement



Arrays of Structures

- * Overall structure length multiple of K_{max}
- Satisfy alignment requirement for every element in array

```
struct S2 {
   double v;
   int i[2];
   char c;
} a[10];
```



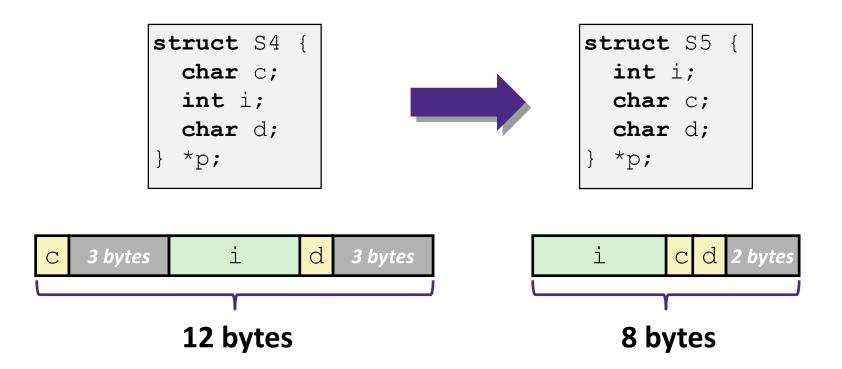


Alignment of Structs

- Compiler will do the following:
 - Maintains declared ordering of fields in struct
 - Each *field* must be aligned within the struct (may insert padding)
 - offsetof can be used to get actual field offset
 - Overall struct must be aligned according to largest field
 - Total struct size must be multiple of its alignment (may insert padding)
 - sizeof should be used to get true size of structs

How the Programmer Can Save Space

- Compiler must respect order elements are declared in
 - Sometimes the programmer can save space by declaring large data types first



Peer Instruction Question

Vote on sizeof(struct old): http://pollev.com/rea

Minimize the size of the struct by re-ordering the vars

```
struct old {
  int i;
  short s[3];
  char *c;
  float f;
};
struct new {
  int i;
  int i;
    _____;
    _____;
    _____;
    _____;
};
```

What are the old and new sizes of the struct?

```
sizeof(struct old) = _____
```

- A. 16 bytes
- B. 22 bytes
- C. 28 bytes
- D. 32 bytes
- E. We're lost...

Summary

- Arrays in C
 - Aligned to satisfy every element's alignment requirement
- Structures
 - Allocate bytes in order declared
 - Pad in middle and at end to satisfy alignment