18au Final

Question M1: Numbers [16 pts]

(A) Briefly explain why we know that there may be data loss casting from int to float, but there won't be casting from int to double. [4 pt]

Explanation:

(B) What value will be read after we try to store \(-2^{127} - 2^{104}\) in a float? (Circle one) [4 pt]

- \(-2^{127}\)
- NaN
- \(-\infty\)
- \(-2^{127} - 2^{104}\)

(C) Complete the following C function that returns whether or not a pointer \(p\) is aligned for data type size \(K\). Hint: be careful with data types! [4 pt]

```c
int aligned(void* p, int K) {
    return __________________________;
}
```

(D) Take the 32-bit numeral \(0x50000000\). Circle the number representation below that has the most positive value for this numeral. [4 pt]

Floating Point Two’s Complement Unsigned Two’s AND Unsigned

Question M2: Design Question [4 pts]

(A) If the Stack grew upwards (e.g. we switched the positions of the Stack and Heap), which assembly instructions would need their behaviors changed? Name two and briefly describe the changes.

Instruction 1:
Change:

Instruction 2:
Change:
4. Pointers, Memory & Registers (14 points)

Assuming a 64-bit x86-64 machine (little endian), you are given the following variables and initial state of memory (values in hex) shown below:

<table>
<thead>
<tr>
<th>Address</th>
<th>+0</th>
<th>+1</th>
<th>+2</th>
<th>+3</th>
<th>+4</th>
<th>+5</th>
<th>+6</th>
<th>+7</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x00</td>
<td>AB</td>
<td>EE</td>
<td>1E</td>
<td>AC</td>
<td>D5</td>
<td>8E</td>
<td>10</td>
<td>E7</td>
</tr>
<tr>
<td>0x08</td>
<td>F7</td>
<td>84</td>
<td>32</td>
<td>2D</td>
<td>A5</td>
<td>F2</td>
<td>3A</td>
<td>CA</td>
</tr>
<tr>
<td>0x10</td>
<td>83</td>
<td>14</td>
<td>53</td>
<td>B9</td>
<td>70</td>
<td>03</td>
<td>F4</td>
<td>31</td>
</tr>
<tr>
<td>0x18</td>
<td>01</td>
<td>20</td>
<td>FE</td>
<td>34</td>
<td>46</td>
<td>E4</td>
<td>FC</td>
<td>52</td>
</tr>
<tr>
<td>0x20</td>
<td>4C</td>
<td>A8</td>
<td>B5</td>
<td>C3</td>
<td>D0</td>
<td>ED</td>
<td>53</td>
<td>17</td>
</tr>
</tbody>
</table>

int* ip = 0x00;
short* sp = 0x20;
long* yp = 0x10;

a) Fill in the type and value for each of the following C expressions. If a value cannot be determined from the given information answer UNKNOWN.

<table>
<thead>
<tr>
<th>Expression (in C)</th>
<th>Type</th>
<th>Value (in hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>yp + 2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>*(sp - 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ip[5]</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;ip</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

b) Assuming that all registers start with the value 0, except %rax which is set to 0x4, fill in the values (in hex) stored in each register after the following x86 instructions are executed. Remember to give enough hex digits to fill up the width of the register name listed.

<table>
<thead>
<tr>
<th>Register</th>
<th>Value (in hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>%rax</td>
<td>0x0000 0000 0000 0004</td>
</tr>
<tr>
<td>%ebx</td>
<td></td>
</tr>
<tr>
<td>%ecx</td>
<td></td>
</tr>
<tr>
<td>%rdi</td>
<td></td>
</tr>
<tr>
<td>%si</td>
<td></td>
</tr>
</tbody>
</table>

movl 2(%rax), %ebx
leal (%rax,%rax,2), %ecx
movsbl 4(%rax), %edi
subw (%rax,2), %si
Consider the following x86-64 assembly and C code for the recursive function rfun.

```c
// Recursive function rfun
long rfun(char *s) {
    if (*s) {
        long temp = (long)*s;
        s++;
        return temp + rfun(s);
    }
    return 0;
}

// Main Function - program entry
int main(int argc, char **argv) {
    char *s = "Yay!";
    long r = rfun(s);
    printf("r: %ld\n", r);
}
```

00000000004005e6 <rfun>:

```
4005e6: 0f b6 07    movzbl (%rdi),%eax
4005e9: 84 c0      test %al,%al
4005eb: 74 13      je  400600 <rfun+0x1a>
4005ed: 53         push %rbx
4005ee: 48 0f be d8 movsbq %al,%rbx
4005f2: 48 83 c7 01 add $0x1,%rdi
4005f6: e8 eb ff ff ff callq 4005e6 <rfun>
4005fb: 48 01 d8    add %rbx,%rax
4005fe: eb 06      jmp 400606 <rfun+0x20>
400600: b8 00 00 00 00 mov $0x0,%eax
400605: c3         retq
400606: 5b         pop %rbx
400607: c3         retq
```
(A) How much space (in bytes) does this function take up in our final executable? [2 pts.]

(B) The compiler automatically creates labels it needs in assembly code. How many labels are used in rfun (including the procedure itself)? [2 pts.]

(C) In terms of the C function, what value is being saved on the stack? [2 pts.]

(D) What is the return address to rfun that gets stored on the stack during the recursive calls (in hex)? [2 pts.]

(E) Assume main calls rfun with char *s = “Yay!” and then prints the result using the printf function, as shown in the C code above. Assume printf does not call any other procedure. Starting with (and including) main, how many total stack frames are created, and what is the maximum depth of the stack? [2 pts.]

Total Frames:  
Max Depth:
(F) Assume main calls `rfun` with `char *s = "Yay!"`, as shown in the C code. After main calls `rfun`, we find that the return address to `main` is stored on the stack at address 0x7fffffffdb38. On the first call to `rfun`, the register `%rdi` holds the address 0x4006d0, which is the address of the input string "Yay!" (i.e. `char *s == 0x4006d0`). Assume we stop execution prior to executing the `movsbq` instruction (address 0x4005ee) during the fourth call to `rfun`. [14 pts.]

For each address in the stack diagram below, fill in both the value and a description of the entry.

The value field should be a hex value, an expression involving the C code listed above (e.g., a variable name such as `s` or `r`, or an expression involving one of these), a literal value (integer constant, a string, a character, etc.), “unknown” if the value cannot be determined, or “unused” if the location is unused.

The description field should be one of the following: “Return address”, “Saved %reg” (where reg is the name of a register), a short and descriptive comment, “unused” if the location is unused, or “unknown” if the value is unknown.

<table>
<thead>
<tr>
<th>Memory Address</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x7fffffffdb48</td>
<td>unknown</td>
<td><code>%rsp when main is entered</code></td>
</tr>
<tr>
<td>0x7fffffffdb38</td>
<td>0x400616</td>
<td>Return address to main</td>
</tr>
<tr>
<td>0x7fffffffdb30</td>
<td>unknown</td>
<td>original %rbx</td>
</tr>
<tr>
<td>0x7fffffffdb28</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffdb20</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffdb18</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffdb10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffdb08</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffdb00</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
4. (15 points) (x86-64 Assembly) This problem considers this assembly implementation of a C function of the form `long mystery(long x) { ... }

mystery:
  movq $0, %rax
  testq %rdi, %rdi
  jle .L2
  .L1:
    addq %rdi, %rdi
    addq $1, %rax
    testq %rdi, %rdi
    jg .L1
  .L2:
    ret

In parts (a)-(c) we ask you to modify the assembly code in ways that have no effect on the answers it produces, i.e., it should perform the same overall computation after any of your changes.

(a) Give a use of a `cmpq` instruction that could be used instead of either of the `testq` instructions.

(b) Give a use of a `shlq` instruction could be used instead of one of the `addq` instructions and indicate which instruction it is replacing.

(c) Suppose we replace the `jle .L2` with `jg .L1`. Insert an additional instruction to complete this change correctly: indicate what instruction you are adding and where.

Now we ask about what mystery is actually computing.

(d) Complete this description of what mystery computes with 1–2 English sentences: “It takes the number in %rdi and returns...”.

(e) What is the largest number mystery could possibly return? Answer in base-10.
2. (12 points) (Struct Layout) Assume x86-64 and Linux and that all fields should be properly aligned.

(a) Consider this struct definition:
```c
struct S {
    int x;
    int * p;
};
```
Do all of the following:
- Indicate what `sizeof(struct S)` would evaluate to.
- Draw the layout of the struct, indicating the size and offset of each field.
- For any padding, indicate whether it is in internal or external fragmentation.

(b) Repeat the previous problem for this struct definition:
```c
struct S {
    int x;
    int * p;
    int y;
};
```

(c) Repeat the previous problem for this struct definition:
```c
struct S {
    int * p;
    int x;
    int y;
};
```
Consider the following variable declarations, assuming x86_64 architecture:

```c
typedef struct {
    int a;
    char b;
    double c;
} struct_type;

struct_type* m;
struct_type n[2];
```

Fill in the following table:

<table>
<thead>
<tr>
<th>C Expression</th>
<th>Evaluates to?</th>
<th>Resulting data type</th>
</tr>
</thead>
<tbody>
<tr>
<td>m</td>
<td>0x10000000</td>
<td></td>
</tr>
<tr>
<td>n</td>
<td>0x20000000</td>
<td></td>
</tr>
<tr>
<td>&amp;(m-&gt;a)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;(m-&gt;b)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;(m-&gt;c)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>sizeof(struct_type)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>sizeof(*m)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>sizeof(m)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;(n[0])</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;(n[0].a)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>&amp;(n[1].a)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Errata: the struct declaration was meant to include a typedef. As it was written, "struct_type" would be a variable of the unnamed struct type, rather than a new name for the struct.
1. Caches (11 points)

You are using a byte-addressed machine where physical addresses are 22-bits. You have a 4-way associative cache of total size 1 KiB with a cache block size of 32 bytes. It uses LRU replacement and write-back policies.

a) Give the number of bits needed for each of these:

Cache Block Offset: ___________  Cache Tag: ______________

b) How many sets will the cache have? __________

c) Assume that everything except the array \( x \) is stored in registers, and that the array \( x \) starts at address 0x0. Give the hit rate (as a fraction or a %) for the following code, assuming that the cache starts out empty. Also give the total number of hits.

```
#define LEAP 1
#define SIZE 256
int x[SIZE][8];
... // Assume x has been initialized to contain values.
... // Assume the cache starts empty at this point.
for (int i = 0; i < SIZE; i += LEAP) {
    x[i][0] += x[i][4];
}
```

Hit Rate: _____________  Total Number of Hits: ______________

d) If we increase the cache block size to 64 bytes (and leave all other factors the same) what would the hit rate be?

Hit Rate: _____________  Total Number of Hits: ______________

e) For each of the changes proposed below, indicate how it would affect the hit rate of the code above in part c) assuming that all other factors remained the same as they were in the original cache:

- Change associativity from 4-way to 2-way: increase / / no change / decrease
- Change LEAP from 1 to 4: increase / / no change / decrease
- Change cache size from 1 KiB to 2 KiB: increase / / no change / decrease
2. Caches – 35 pts total (14/A, 6/B, 15C)

A. You are given a direct-mapped cache of total size 256 bytes, with cache block size of 16 bytes. The system’s page size is 4096 bytes. The following C array has been declared and initialized to contain some values:

```c
int x[2][64];
```

i. How many sets will the cache have?

ii. How many bits will be required for the cache block offset?

iii. If the physical addresses are 22 bits, how many bits are in the cache tag?

iv. Assuming that all data except for the array `x` are stored in registers, and that the array `x` starts at address 0x0. Give the miss rate (as a fraction or a %) and total number of misses for the following code, assuming that the cache starts out empty:

```c
int sum = 1;
int i;
for (i = 0; i < 64; i++) {
    sum += x[0][i] + x[1][i];
}
```

Miss Rate: ____________  Total Number of Misses: ____________

v. What if we maintain the same total cache size and cache block size, but increase the associativity to 2-way set associative. Now what will be the miss rate and total number of misses of the above code, assuming that the cache starts out empty?

Miss Rate: ____________  Total Number of Misses: ____________
2. (cont.)

B. Given the following access results in the form (address, result) on an empty cache of total size 16 bytes, what can you infer about this cache’s properties? Assume LRU replacement policy. **Circle all that apply.**

(0, Miss), (8, Miss), (0, Hit), (16, Miss), (8, Miss)

a. The block size is greater than 8 bytes

b. The block size is less or equal to 8 bytes

c. This cache has only two sets

d. This cache has more than 8 sets

e. This cache is 2-way set associative

f. The cache is 4-way set associative

g. Using an 8 bit address, the tag would be 4 bits

h. Using an 8 bit address, the tag would be greater than 4 bits

i. None of the above
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5. Processes – 10 pts
A) What is `exec()` used for? Give an example of when it is used.

B) On a context switch, circle all of the following that would be saved:

<table>
<thead>
<tr>
<th>TLB contents</th>
<th>Stack Pointer</th>
<th>Instruction Cache Contents</th>
<th>Heap Contents</th>
<th>Register Contents</th>
<th>Stack Contents</th>
<th>Condition Codes</th>
</tr>
</thead>
</table>

C) Given the following C program:

```c
void sunny() {
    int n = 1;
    if (fork() == 0) {
        n = n << 1
        printf("%d, ", n);
        n = n << 1
    }
    if(fork() == 0) {
        n = n + 700;
    }
    printf("%d", n);
}
```

Which of the following outputs are possible for this function (circle all that apply):

- a. 2, 4, 1, 701, 704,
- b. 1, 2, 4, 704, 701,
- c. 2, 704, 4, 701, 1,
- d. 701, 2, 704, 4, 1,
- e. 1, 704, 2, 4, 701,
- f. 2, 1, 704, 4, 701,
6. Programs, processes, and processors (oh my!) (25 pts)

(a) Consider the following C code on the left (running on Linux), then give one possible output of running it. Assume that `printf` flushes its output immediately.

```c
void oz() {
    char * name = "toto\n";
    printf("dorothy\n");
    if (fork() == 0) {
        name = "wizard\n";
        printf("scarecrow\n");
        fork();
        printf("tinman\n");
        exit(0);
        printf("witch\n");
    } else {
        printf("lion\n");
    }
    printf(name);
}
```

Possible output:

dorothy
lion
toto
scarecrow
tinman
tinman

(b) "Pay no attention to the man behind the curtain." We have seen several different mechanisms used to create illusions or abstractions for running programs:

A. Context switch
B. Virtual memory
C. Virtual method tables (vtables)
D. Caches
E. Timer interrupt
F. Stack discipline
G. None of the above, or impossible.

For each of the following, indicate which mechanism above (A-F) enables the behavior, or G if the behavior is impossible or untrue.

(i) ______ Allows operating system kernel to run to make scheduling decisions.

(ii) ______ Prevents buffer overflow exploits.

(iii) ______ Allows multiple instances of the same program to run concurrently.

(iv) ______ Lets programs use more memory than the machine has.

(v) ______ Makes recently accessed memory faster.

(vi) ______ Multiple processes appear to run concurrently on a single processor.

(vii) ______ Enables programs to run different code depending on an object’s type.

(viii)______ Allows an x86-64 machine to execute code for a different ISA.
Name: ________________________________

(c) Give an example of a synchronous exception, what could trigger it, and where the exception handler would return control to in the original program.

Page fault: triggered by access to virtual address not in memory, returns to the instruction that caused the fault.

Trap: used to for syscalls to do something protected by the kernel, returns to after the calling instruction.

(d) In what way does address translation (virtual memory) help make exec fast? Explain in less than 2 sentences. *Hint: it may help to write down what happens during exec.*

Address translation is a form of indirection, it allows us to implement fork without copying the whole process's memory, and exec without loading the whole program into memory at once.

(e) Which of the following can a running process determine, assuming it does not have access to a timer? *(check all that apply)*

- [ ] Its own process ID
- [ ] Size of physical memory
- [ ] Size of the virtual address space
- [ ] L1 cache associativity
- [ ] When context switches happen

(f) For each of the following, fill in what is responsible for making the decision: hardware ("HW"), operating system ("OS"), or program ("P").

(i) _______ Which physical page a virtual page is mapped to.

(ii) _______ Which cache line is evicted for a conflict in a set-associative cache.

(iii) _______ Which page is evicted from physical memory during a page fault.

(iv) _______ Translation from virtual address to physical address.

(v) _______ Whether data is stored in the stack or the heap.

(vi) _______ Data layout optimized for spatial locality
Assume we have a virtual memory detailed as follows:

- 256 MiB Physical Address Space
- 4 GiB Virtual Address Space
- 1 KiB page size
- A TLB with 4 sets that is 8-way associative with LRU replacement

For the following questions it is fine to leave your answers as powers of 2.

a) How many bits will be used for:

Page offset? __________

Virtual Page Number (VPN)? ________ Physical Page Number (PPN)? ________

TLB index? ________________ TLB tag? ________________

b) How many entries in this page table?

c) We run the following code with an empty TLB. Calculate the TLB miss rate for data (ignore instruction fetches). Assume \( i \) and \( \text{sum} \) are stored in registers and \( \text{cool} \) is page-aligned.

```c
#define LEAP 8
int cool[512];
... // Some code that assigns values into the array cool
... // Now flush the TLB. Start counting TLB miss rate from here.
int sum;
for (int i = 0; i < 512; i += LEAP) {
    sum += cool[i];
}
```

**TLB Miss Rate:** (fine to leave you answer as a fraction) ________________
Question F7: Virtual Memory  [10 pts]

Our system has the following setup:

- 24-bit virtual addresses and 512 KiB of RAM with 4 KiB pages
- A 4-entry TLB that is fully associative with LRU replacement
- A page table entry contains a valid bit and protection bits for read (R), write (W), execute (X)

(A) Compute the following values:  [2 pt]

- Page offset width _______  PPN width _______
- Entries in a page table _______  TLBT width _______

(B) Briefly explain why we make the page size so much larger than a cache block size.  [2 pt]

(C) Fill in the following blanks with “A” for always, “S” for sometimes, and “N” for never if the following get updated during a page fault.  [2 pt]

- Page table _______  Swap space _______  TLB _______  Cache _______

(D) The TLB is in the state shown when the following code is executed. Which iteration (value of i) will cause the protection fault (segfault)? Assume sum is stored in a register.  [4 pt]

Recall: the hex representations for TLBT/PPN are padded as necessary.

```c
long *p = 0x7F0000, sum = 0;
for (int i = 0; i < 1; i++) {
    if (i%2)
        *p = 0;
    else
        sum += *p;
    p++;
}
```

<table>
<thead>
<tr>
<th>TLBT</th>
<th>PPN</th>
<th>Valid</th>
<th>R</th>
<th>W</th>
<th>X</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x7F0</td>
<td>0x31</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>0x7F2</td>
<td>0x15</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0x004</td>
<td>0x1D</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0x7F1</td>
<td>0x2D</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

```c
i = _______
```

16au Final
18au Final

Question F10: Memory Allocation [18 pts]

(A) In the following code, briefly identify the TWO memory errors. They can be fixed by changing ONE line of code. [6 pt]

```c
int N = 64;
double *func(double A[][], double x[]) {
    double *z = (double *) malloc(N * sizeof(float));
    for (int i = 0; i < N; i++) {
        for (int j = 0; j < N; j++) {
            z[i] = A[i][j] + z[i] * x[j];
        }
    }
    return z;
}
```

Error 1:

Error 2:

Line of code with fixes:

(B) We are using a dynamic memory allocator on a 64-bit machine with an explicit free list, 4-byte boundary tags, and 16-byte alignment. Assume that a footer is always used. [6 pt]

<table>
<thead>
<tr>
<th>Request</th>
<th>return value</th>
<th>block addr</th>
<th>block size</th>
<th>internal fragmentation in this block</th>
</tr>
</thead>
<tbody>
<tr>
<td>p = malloc(12);</td>
<td>0x610</td>
<td>0x____</td>
<td>_____ bytes</td>
<td>_____ bytes</td>
</tr>
</tbody>
</table>

(C) Consider the C code shown here. Assume that the malloc call succeeds and that all variables are stored in memory (not registers). In the following groups of expressions, circle the one whose returned value (assume just before return 0) is the lowest/smallest. [6 pt]

```c
#include <stdlib.h>
int ZERO = 0;
char* str = "cse351";

int main(int argc, char *argv[]) {
    int *foo = malloc(888);
    int bar = 351;
    free(foo);
    return foo;
}
```

<table>
<thead>
<tr>
<th>Group 1:</th>
<th>&amp;bar</th>
<th>&amp;foo</th>
<th>foo</th>
</tr>
</thead>
<tbody>
<tr>
<td>Group 2:</td>
<td>&amp;foo</td>
<td>main</td>
<td>str</td>
</tr>
<tr>
<td>Group 3:</td>
<td>bar</td>
<td>&amp;str</td>
<td>&amp;ZERO</td>
</tr>
</tbody>
</table>
4. Memory Allocation (11 points total)

```c
#include <stdlib.h>
float pi = 3.14;

int main(int argc, char *argv[]) {
    int year = 2019;
    int* happy = malloc(sizeof(int*));
    happy++;
    free(happy);
    return 0;
}
```

a) [3 pts] Consider the C code shown above. Assume that the `malloc` call succeeds and `happy` and `year` are stored in memory (not in a register). Fill in the following blanks with “<” or “>” or “UNKNOWN” to compare the values returned by the following expressions just before `return 0`.

- `&year ________ &main`
- `happy ________ &happy`
- `&pi ________ happy`

b) [4 pts] The code above has two memory-related errors. Use the line numbers in the code to describe what the errors are and where they occur.

Error #1:

Error #2:

c) [2 pts] (Not related to code at top of page) Give one advantage that next fit placement policy has over a first fit placement policy in an implicit free list implementation.

d) [2 pts] List two reasons why it would be hard to write a garbage collector for the C programming language.

Reason #1:

Reason #2:
8. (11 points) (Java)

(a) For each course topic below that we studied using C, answer yes if the topic is directly relevant in Java as well (else answer no).

i. Floating-point operations often produce small rounding errors that can compound over many operations.

ii. Pointer arithmetic is scaled by the size of the pointed-to object.

iii. Using uninitialized data can lead to garbage results that depend on whatever happened to be in that memory previously.

iv. Keeping your working set small helps improve the performance of memory operations in general, without concern for the exact parameters of a machine’s memory hierarchy.

(b) Some of the safety checks that are performed in Java but not in C require extra data in memory, i.e., fields that are part of Java data but not part of the analogous C data. For each of the following, answer yes if Java needs such extra data to perform the operation (else answer no).

i. Throwing an ArrayIndexOutOfBoundsException if an array index is too large.

ii. Throwing a NullPointerException if the e in e.m() evaluates to null.

iii. Throwing a ClassCastException if the e in (Foo)e does not evaluate to an instance of Foo.

(c) Consider this Java code, which is part of a larger program.

```java
class Foo {
    int x;
    boolean sameAsX(int y) { return y == this.x; }
    boolean sameAs7(int y) { return y == 7; }
}

class Bar {
    boolean whyNotBoth(Foo f, int z) {
        return f.sameAsX(z) && f.sameAs7(z);
    }
}
```

Your friend suggests that when compiling the method call f.sameAs7(z) above, the compiler can optimize out the instruction that passes this as a procedure argument since the sameAs7 method in Foo does not use it. Explain in roughly 1–2 sentences why this “optimization” is wrong.