## Notation guide:

Parameter	Variable Name	$\operatorname{Units}$
Block size	K (B in book)	bytes
Cache size	$^{\mathrm{C}}$	bytes (C) or blocks (C/K)
Associativity	${f E}$	none ("ways")
Address width	$\mathbf{m}$	bits
Tag field width	t	bits
Index field width	S	bits
Offset field width	k (b in book)	bits
Number of sets	S	none

## Address translation:

Looking at the original address:

 $Most\text{-significant bit} \rightarrow \boxed{Tag \ bits} \ \boxed{Set \ index \ bits} \ \boxed{Block \ offset \ bits} \ \leftarrow Least\text{-significant bit}$  Things to think about:

**block offset bits:** If we have block size X, how many bits do we need to describe X things?

set index bits: If we have X sets, how many bits do we need to describe X things?

## Formulas to remember:

Average Memory Access Time (AMAT) = Hit Time + Miss Rate  $\times$  Miss Penalty

## General cache organization:

