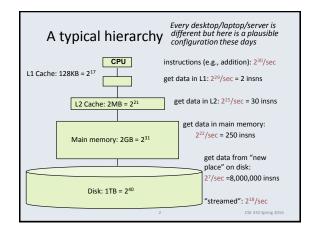
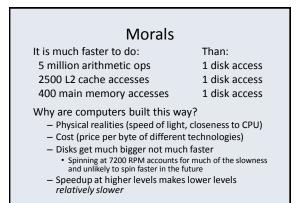
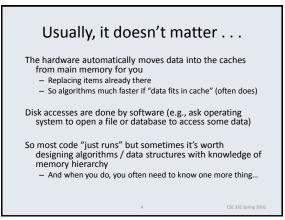
## CSE 332: Data Abstractions Memory Hierarchy

Richard Anderson Spring 2016



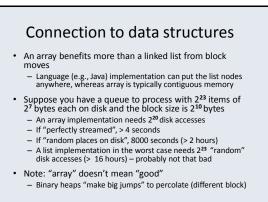


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## Block/line size Moving data up the memory hierarchy is slow because of *latency* (think distance-to-travel) May as well send more than just the one int/reference asked for (think "giving friends a car ride doesn't slow you down") Sends nearby memory because: it is easy Likely to be used soon (think fields/arrays) Mount of data moved from disk into memory called the "block" size or the "page" size Not under program control Amount of data moved from memory into cache called the "line" size Not under program control

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## BSTs?

- Looking things up in balanced binary search trees is  $O(\log n)$ , so even for  $n = 2^{39}$  (512GB) we need not worry about minutes or hours
- Still, number of disk accesses matters – AVL tree could have height of 55
  - So each find could take about 0.5 seconds or about 100 finds a minute
  - Most of the nodes will be on disk: the tree is shallow, but it is still many gigabytes big so the *tree* cannot fit in memory
    - Even if memory holds the first 25 nodes on our path, we still need 30 disk accesses

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## Note about numbers; moral

- All the numbers in this lecture are "ballpark" "back of the envelope" figures
- Even if they are off by, say, a factor of 5, the moral is the same: If your data structure is mostly on disk, you want to minimize disk accesses
- A better data structure in this setting would exploit the block size and relatively fast memory access to avoid disk accesses...

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