

CSE 311 Foundations of Computing I

Lecture 4, Boolean Logic
Autumn 2011

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Announcements

- Homework 2
 - Available for download
- Reading assignments
 - Boolean Algebra
 - 12.1 – 12.3 7th Edition
 - 11.1 – 11.3 6th Edition
 - 10.1 – 10.3 5th Edition
 - Predicates and Quantifiers
 - 1.4 7th Edition
 - 1.3 5th and 6th Edition

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Ugrad Autumn Career Events

We strongly urge all CSE undergrads to attend our first two CSE Autumn Career Events. These events feature vital employment information for all future CSE job seekers. (Note: for CSE majors only.)

Event 1: Employer Panel

Date/Time: Wednesday, October 5, 2010 5:30-6:30pm

Place: EE125

Our first career event of autumn will provide an overview of the job search process from both the CSE student and employer perspective.

Event 2: Resume Review Roundtable Workshop

Date/Time: Tuesday, October 11, 3:00-6:00 pm

Place: CSE Atrium

In this workshop, HR reps, recruiters and engineers sit with small groups of CSE students to critique resumes, offer suggestions, and help refine the way you present yourself on paper.

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Boolean logic

- Combinational logic
 - $\text{output}_i = F(\text{input}_i)$
- Sequential logic
 - $\text{output}_i = F(\text{output}_{i-1}, \text{input}_i)$
 - output dependent on history
 - concept of a time step (clock)
- An algebraic structure consists of
 - a set of elements $B = \{0, 1\}$
 - binary operations $\{+, \cdot\}$ (OR, AND)
 - and a unary operation $'$ (NOT)

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A quick combinational logic example

- Calendar subsystem: number of days in a month (to control watch display)
 - used in controlling the display of a wrist-watch LCD screen
 - inputs: month, leap year flag
 - outputs: number of days

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Implementation in software

```
integer number_of_days ( month, leap_year_flag ) {
    switch (month) {
        case 1: return (31);
        case 2: if (leap_year_flag == 1) then
                    return (29) else return (28);
        case 3: return (31);
        ...
        case 12: return (31);
        default: return (0);
    }
}
```

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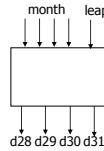
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Implementation as a combinational digital system

- Encoding:

- how many bits for each input/output?
- binary number for month
- four wires for 28, 29, 30, and 31



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month	leap		d28	d29	d30	d31
0000	-		-	-	-	-
0001	-		0	0	0	1
0010	0		1	0	0	0
0010	1		0	1	0	0
0011	-		0	0	0	1
0100	-		0	0	1	0
0101	-		0	0	0	1
0110	-		0	0	1	0
0111	-		0	0	0	1
1000	-		0	0	0	1
1001	-		0	0	1	0
1010	-		0	0	0	1
1011	-		0	0	1	0
1100	-		0	0	0	1
1101	-		-	-	-	-
1110	-		-	-	-	-
1111	-		-	-	-	-

Combinational example (cont'd)

- Truth-table to logic to switches to gates

- d28 = "1 when month=0010 and leap=0"

$$d28 = m8' \cdot m4' \cdot m2 \cdot m1' \cdot \text{leap}'$$

- d31 = "1 when month=0001 or month=0011 or ... month=1100"

$$d31 = (m8' \cdot m4' \cdot m2' \cdot m1) + (m8' \cdot m4' \cdot m2 \cdot m1) + \dots \\ (m8 \cdot m4 \cdot m2' \cdot m1')$$

- d31 = can we simplify more?

month	leap	d28	d29	d30	d31
0000	-	-	-	-	-
0001	-	0	0	0	1
0010	0	1	0	0	0
0010	1	0	1	0	0
0011	-	0	0	0	1
0100	-	0	0	1	0
0101	-	0	0	0	1
0110	-	0	0	1	0
0111	-	0	0	0	1
1000	-	0	0	0	1
1001	-	0	0	1	0
1010	-	0	0	0	1
1011	-	0	0	1	0
1100	-	0	0	0	1
1101	-	-	-	-	-
1110	-	-	-	-	-
1111	-	-	-	-	-

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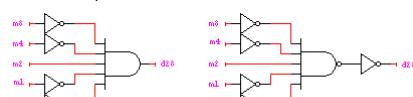
Combinational example (cont'd)

$$d28 = m8' \cdot m4' \cdot m2 \cdot m1' \cdot \text{leap}'$$

$$d29 = m8' \cdot m4' \cdot m2 \cdot m1' \cdot \text{leap}$$

$$d30 = (m8' \cdot m4 \cdot m2' \cdot m1') + (m8' \cdot m4' \cdot m2 \cdot m1') + \\ (m8 \cdot m4' \cdot m2' \cdot m1) + (m8 \cdot m4 \cdot m2 \cdot m1) \\ = (m8' \cdot m4 \cdot m1) + (m8 \cdot m4' \cdot m1)$$

$$d31 = (m8' \cdot m4 \cdot m2' \cdot m1) + (m8' \cdot m4' \cdot m2 \cdot m1) + \\ (m8' \cdot m4 \cdot m2' \cdot m1) + (m8' \cdot m4' \cdot m2 \cdot m1) + \\ (m8 \cdot m4' \cdot m2' \cdot m1) + (m8 \cdot m4' \cdot m2 \cdot m1') + \\ (m8 \cdot m4 \cdot m2' \cdot m1')$$



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Combinational logic

- Switches
- Basic logic and truth tables
- Logic functions
- Boolean algebra
- Proofs by re-writing and by perfect induction

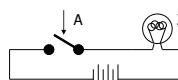
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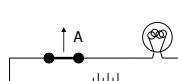
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Switches: basic element of physical implementations

- Implementing a simple circuit (arrow shows action if wire changes to "1"):



close switch (if A is "1" or asserted)
and turn on light bulb (Z)



open switch (if A is "0" or unasserted)
and turn off light bulb (Z)

$$Z \equiv A$$

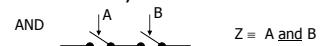
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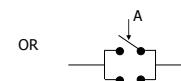
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Switches (cont'd)

- Compose switches into more complex ones (Boolean functions):



$$Z \equiv A \text{ and } B$$



$$Z \equiv A \text{ or } B$$

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Transistor networks

- Modern digital systems are designed in CMOS technology
 - MOS stands for Metal-Oxide on Semiconductor
 - C is for complementary because there are both normally-open and normally-closed switches
- MOS transistors act as voltage-controlled switches
 - similar, though easier to work with than relays.

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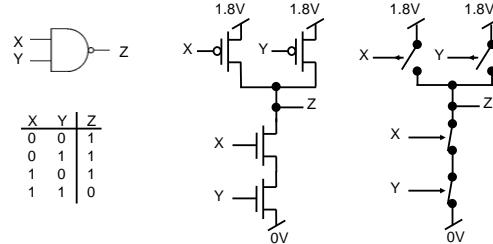
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Multi-input logic gate

- CMOS logic gates are inverting
 - Easy to implement NAND, NOR, NOT while AND, OR, and Buffer are harder



Claude Shannon – 1938



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Possible logic functions of two variables

- There are 16 possible functions of 2 input variables:
 - in general, there are $2^{**}(2^{**}n)$ functions of n inputs



		16 possible functions ($F_0 - F_{15}$)															
X	Y	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
0	1	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
1	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
1	1	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1

Annotations below the table:

- Row 0: X and Y
- Row 1: X
- Row 2: Y
- Row 3: X xor Y
- Row 4: X or Y
- Row 5: X nor Y
- Row 6: not Y
- Row 7: X = Y
- Row 8: not X
- Row 9: not Y
- Row 10: X and Y
- Row 11: not (X or Y)

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Boolean algebra



George Boole – 1854

- An algebraic structure consists of
 - a set of elements B
 - binary operations { +, • }
 - and a unary operation { ' }
 - such that the following axioms hold:

- the set B contains at least two elements: a, b
 - closure: $a + b$ is in B
 - commutativity: $a + b = b + a$
 - associativity: $a + (b + c) = (a + b) + c$
 - identity: $a + 0 = a$
 - distributivity: $a + (b * c) = (a + b) * (a + c)$
 - complementarity: $a + a' = 1$
- $a * b$ is in B
 $a * b = b * a$
 $a * (b * c) = (a * b) * c$
 $a * 1 = a$
 $a * (b + c) = (a * b) + (a * c)$
 $a * a' = 0$

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Logic functions and Boolean algebra

Any logic function that can be expressed as a truth table can be written as an expression in Boolean algebra using the operators: ', +, and •

X, Y are Boolean algebra variables

X	Y	$X + Y$	X	Y	X'	$X + Y$
0	0	0	0	0	1	0
0	1	1	0	1	1	1
1	0	1	1	0	0	1
1	1	1	1	1	0	1

X	Y	X'	Y'	$X + Y$	$X' + Y'$	$(X + Y) + (X' + Y')$
0	0	1	1	0	1	1
0	1	1	0	0	0	0
1	0	0	1	1	0	1
1	1	0	0	1	0	1

Boolean expression that is true when the variables X and Y have the same value and false, otherwise

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Axioms and theorems of Boolean algebra

- | | | |
|------------------|--------------------------------------|---------------------------------------|
| identity: | 1. $X + 0 = X$ | 1. $X * 1 = X$ |
| null: | 2. $X + 1 = 1$ | 2D. $X * 0 = 0$ |
| idempotency: | 3. $X + X = X$ | 3D. $X * X = X$ |
| involution: | 4. $(X')' = X$ | |
| complementarity: | 5. $X + X' = 1$ | 5D. $X * X' = 0$ |
| commutatively: | 6. $X + Y = Y + X$ | 6D. $X * Y = Y * X$ |
| associativity: | 7. $(X + Y) + Z = X + (Y + Z)$ | 7D. $(X * Y) * Z = X * (Y * Z)$ |
| distributivity: | 8. $X * (Y + Z) = (X * Y) + (X * Z)$ | 8D. $X + (Y * Z) = (X + Y) * (X + Z)$ |

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Axioms and theorems of Boolean algebra (cont'd)

uniting:

$$9. X \bullet Y + X \bullet Y' = X$$

absorption:

$$10. X + X \bullet Y = X$$

$$11. (X + Y') \bullet Y = X \bullet Y$$

factoring:

$$12. (X + Y) \bullet (X' + Z) = X + Z + X' \bullet Y$$

$$9D. (X + Y) \bullet (X + Y') = X$$

$$10D. X \bullet (X + Y) = X$$

$$11D. (X \bullet Y') + Y = X + Y$$

$$12D. X \bullet Y + X' \bullet Z = (X + Z) \bullet (X' + Y)$$

consensus:

$$13. (X \bullet Y) + (Y \bullet Z) + (X' \bullet Z) = X \bullet Y + X' \bullet Z$$

$$13D. (X + Y) \bullet (Y + Z) + (X' + Z) = (X + Y) \bullet (X' + Z)$$

de Morgan's:

$$14. (X + Y + \dots)' = X' \bullet Y' \bullet \dots$$

$$14D. (X \bullet Y \bullet \dots)' = X' + Y' + \dots$$

generalized de Morgan's:

$$15. f'(X_1, X_2, \dots, X_n, 0, 1, +, \bullet) = f(X_1', X_2', \dots, X_n', 1, 0, \bullet, +)$$

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Proving theorems (rewriting)

- Using the laws of Boolean algebra:

e.g., prove the theorem:

$$X \bullet Y + X \bullet Y' = X$$

distributivity (8)
complementarity (5)
identity (1D)

$$\begin{aligned} X \bullet Y + X \bullet Y' &= X \bullet (Y + Y') \\ &= X \bullet (1) \\ &= X \end{aligned}$$

e.g., prove the theorem: $X + X \bullet Y = X$

identity (1D)
distributivity (8)
identity (2)
identity (1D)

$$\begin{aligned} X + X \bullet Y &= X \bullet 1 + X \bullet Y \\ &= X \bullet (1 + Y) \\ &= X \bullet (1) \\ &= X \end{aligned}$$

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Proving theorems (perfect induction)

- Using perfect induction (complete truth table):

e.g., de Morgan's:

$$(X + Y)' = X' \bullet Y'$$

NOR is equivalent to AND
with inputs complemented

X	Y	X'	Y'	(X + Y)'	X' \bullet Y'
0	0	1	1	0	0
0	1	1	0	1	0
1	0	0	1	1	0
1	1	0	0	0	1

$$(X \bullet Y)' = X' + Y'$$

NAND is equivalent to OR
with inputs complemented

X	Y	X'	Y'	(X \bullet Y)'	X' + Y'
0	0	1	1	0	1
0	1	1	0	1	0
1	0	0	1	1	0
1	1	0	0	0	1

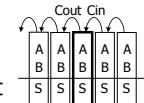
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A simple example: 1-bit binary adder

- Inputs: A, B, Carry-in
- Outputs: Sum, Carry-out



A	B	Cin	Cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

$$\begin{aligned} S &= A' B' Cin + A' B Cin' + A B' Cin' + A B Cin \\ Cout &= A' B Cin + A B' Cin + A B Cin' + A B Cin \end{aligned}$$

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Apply the theorems to simplify expressions

- The theorems of Boolean algebra can simplify expressions
 - e.g., full adder's carry-out function

$$\begin{aligned} Cout &= A' B Cin + A B' Cin + A B Cin' + A B Cin \\ &= A' B Cin + A B' Cin + A B Cin' + \boxed{A B Cin + A B Cin} \\ &= A' B Cin + A B Cin + A B' Cin + A B Cin' + A B Cin \\ &= (A' + A) B Cin + A B' Cin + A B Cin' + A B Cin \\ &= (1) B Cin + A B' Cin + A B Cin' + A B Cin \\ &= B Cin + A B' Cin + A B Cin + \boxed{A B Cin + A B Cin} \\ &= B Cin + A B' Cin + A B Cin + A B Cin' + A B Cin \\ &= B Cin + A (B' + B) Cin + A B Cin' + A B Cin \\ &= B Cin + A (1) Cin + A B Cin' + A B Cin \\ &= B Cin + A Cin + A B (Cin' + Cin) \\ &= B Cin + A Cin + A B (1) \\ &= B Cin + A Cin + A B \end{aligned}$$

adding extra terms creates new factoring opportunities

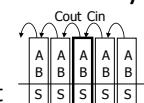
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A simple example: 1-bit binary adder

- Inputs: A, B, Carry-in
- Outputs: Sum, Carry-out



A	B	Cin	Cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

$$\begin{aligned} Cout &= B Cin + A Cin + A B \\ S &= A' B' Cin + A' B Cin' + A B' Cin' + A B Cin \\ &= A' (B' Cin + B Cin') + A (B' Cin' + B Cin) \\ &= A' Z + A Z' \\ &= A \text{ xor } Z = B \text{ xor } Cin \end{aligned}$$

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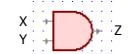
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From Boolean expressions to logic gates

- NOT X' \bar{X} $\sim X$ $X/$ 

X	Y	Z
0	0	1
0	1	0
1	0	1
1	1	0

- AND $X \bullet Y$ XY $X \wedge Y$



X	Y	Z
0	0	0
0	1	0
1	0	0
1	1	1

- OR $X + Y$ $X \vee Y$



X	Y	Z
0	0	0
0	1	1
1	0	1
1	1	1

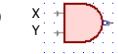
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From Boolean expressions to logic gates (cont'd)

- NAND



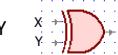
X	Y	Z
0	0	1
0	1	0
1	0	0
1	1	0

- NOR



X	Y	Z
0	0	1
0	1	0
1	0	0
1	1	0

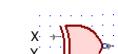
- XOR



X	Y	Z
0	0	0
0	1	1
1	0	1
1	1	0

$X \text{xor } Y = X'Y + XY'$
X or Y but not both
(“inequality”, “difference”)

- XNOR



X	Y	Z
0	0	1
0	1	0
1	0	0
1	1	1

$X \text{xnor } Y = XY + X'Y'$
X and Y are the same
(“equality”, “coincidence”)

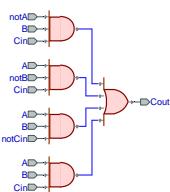
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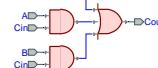
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Full adder: Carry-out

$$\text{Before Boolean minimization: } Cout = A'BCin + AB'Cin + ABCin' + ABCin$$



$$\text{After Boolean minimization: } Cout = BCin + ACin + AB$$



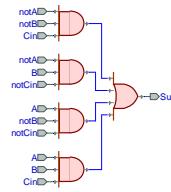
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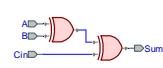
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Full adder: Sum

$$\text{Before Boolean minimization: } Sum = A'B'Cin + A'BCin' + AB'Cin + ABCin$$



$$\text{After Boolean minimization: } Sum = (A \oplus B) \oplus Cin$$

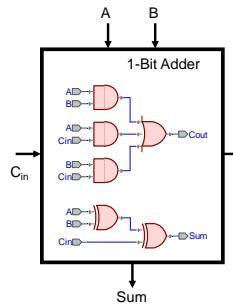


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Preview: A 2-bit ripple-carry adder



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Mapping truth tables to logic gates

- Given a truth table:

- Write the Boolean expression
- Minimize the Boolean expression
- Draw as gates
- Map to available gates

$$\begin{aligned} F &= A'BC' + A'BC + AB'C + ABC \\ &= A'B(C+C') + AC(B+B') \\ &= A'B + AC \end{aligned}$$



A	B	C	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

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Canonical forms

- Truth table is the unique signature of a Boolean function
- The same truth table can have many gate realizations
 - we've seen this already
 - depends on how good we are at Boolean simplification
- Canonical forms
 - standard forms for a Boolean expression
 - we all come up with the same expression

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Sum-of-products canonical forms

- Also known as disjunctive normal form
- Also known as minterm expansion

$F = 001$	011	101	110	111
$F = A'B'C + A'BC + AB'C + ABC' + ABC$				
A	B	C	F	F'
0	0	0	0	1
0	0	1	1	0
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	0

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Sum-of-products canonical form (cont'd)

- Product term (or minterm)
 - ANDed product of literals – input combination for which output is true
 - each variable appears exactly once, true or inverted (but not both)

A	B	C	minterms
0	0	0	$A'B'C'$ m0
0	0	1	$A'B'C$ m1
0	1	0	$A'B'C'$ m2
0	1	1	$A'BC$ m3
1	0	0	ABC' m4
1	0	1	ABC m5
1	1	0	ABC' m6
1	1	1	ABC m7

short-hand notation for minterms of 3 variables

F in canonical form:
 $F(A, B, C) = \Sigma m(1,3,5,7)$
 $= m1 + m3 + m5 + m6 + m7$
 $= A'B'C + A'BC + AB'C + ABC' + ABC$

canonical form ≠ minimal form
 $F(A, B, C) = A'B'C + A'BC + AB'C + ABC + ABC'$
 $= (A'B' + AB' + AB'C + ABC)C + ABC'$
 $= ((A' + A)(B' + B))C + ABC'$
 $= C + ABC'$
 $= ABC' + C$
 $= AB + C$

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Product-of-sums canonical form

- Also known as conjunctive normal form
- Also known as maxterm expansion

$F = 000$	010	100
$F = (A + B + C) (A + B' + C) (A' + B + C)$		
A	B	C
0	0	0
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1
1	1	0
1	1	1

$$F' = (A + B + C') (A + B' + C') (A' + B + C') (A' + B' + C')$$

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Product-of-sums canonical form (cont'd)

- Sum term (or maxterm)
 - ORed sum of literals – input combination for which output is false
 - each variable appears exactly once, true or inverted (but not both)

A	B	C	maxterms
0	0	0	$A+B+C$ M0
0	0	1	$A+B+C'$ M1
0	1	0	$A+B'+C$ M2
0	1	1	$A+B'+C'$ M3
1	0	0	$A+B+C$ M4
1	0	1	$A+B+C'$ M5
1	1	0	$A+B'+C$ M6
1	1	1	$A+B'+C'$ M7

short-hand notation for maxterms of 3 variables

F in canonical form:
 $F(A, B, C) = \Pi M(0,2,4)$
 $= M0 + M2 + M4$
 $= (A + B + C) (A + B' + C) (A' + B + C)$

canonical form ≠ minimal form
 $F(A, B, C) = (A + B + C) (A + B' + C) (A' + B + C) (A' + B' + C)$
 $= (A + B + C) (A + B' + C)$
 $= (A + B + C) (A + B + C)$
 $= (A + C) (B + C)$

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S-o-P, P-o-S, and de Morgan's theorem

- Complement of function in sum-of-products form
 - $F' = A'B'C' + A'BC + AB'C'$
- Complement again and apply de Morgan's and get the product-of-sums form
 - $(F')' = (A'B'C' + A'BC + AB'C)'$
 - $F = (A + B + C) (A + B' + C) (A' + B + C)$
- Complement of function in product-of-sums form
 - $F' = (A + B + C) (A + B' + C') (A' + B + C) (A' + B' + C')$
- Complement again and apply de Morgan's and get the sum-of-product form
 - $(F')' = ((A + B + C)(A + B' + C')(A' + B + C)(A' + B' + C'))'$
 - $F = A'B'C + A'BC + AB'C + ABC'$

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