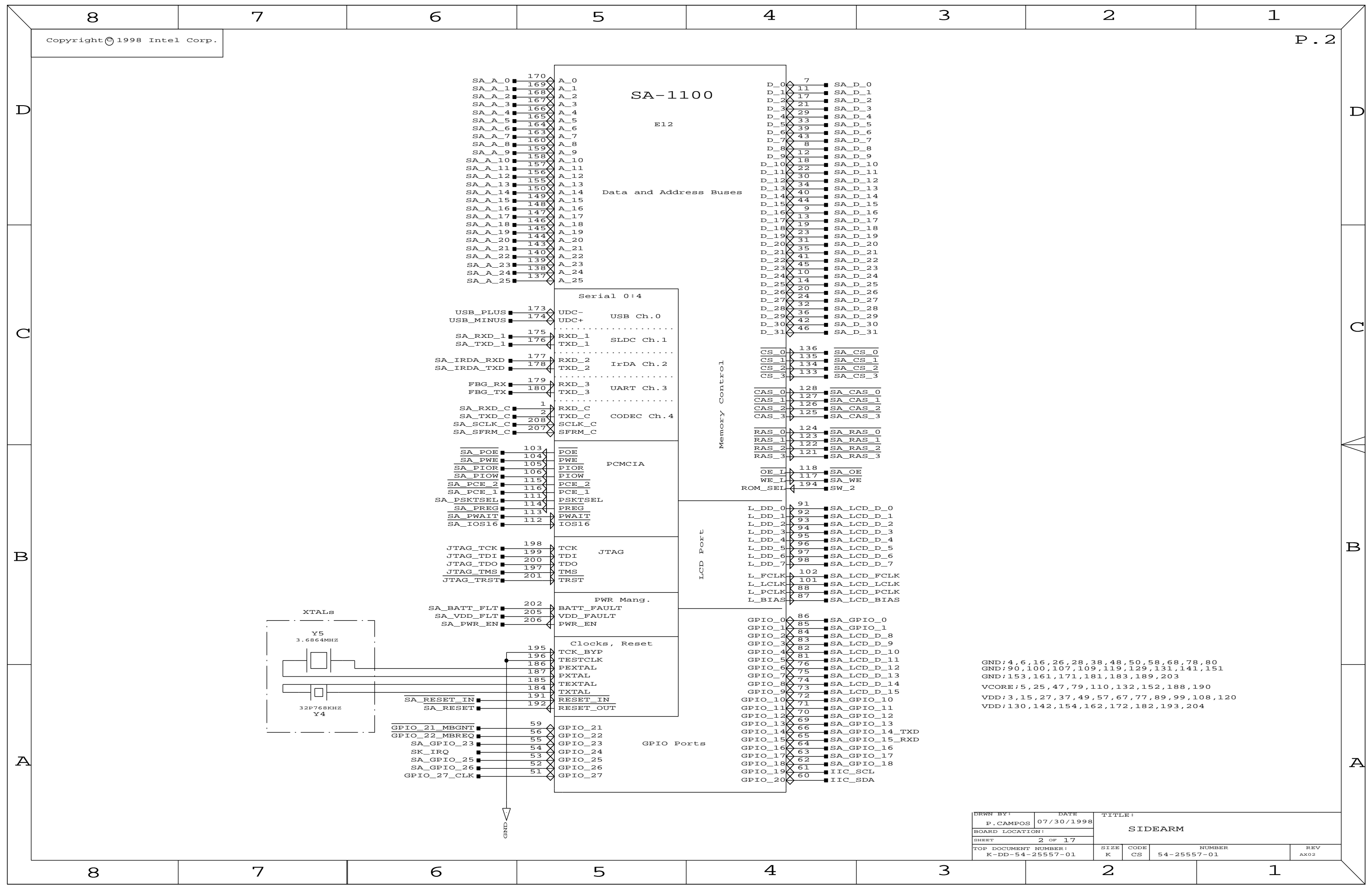


SideARM

SA-1100 Video Module

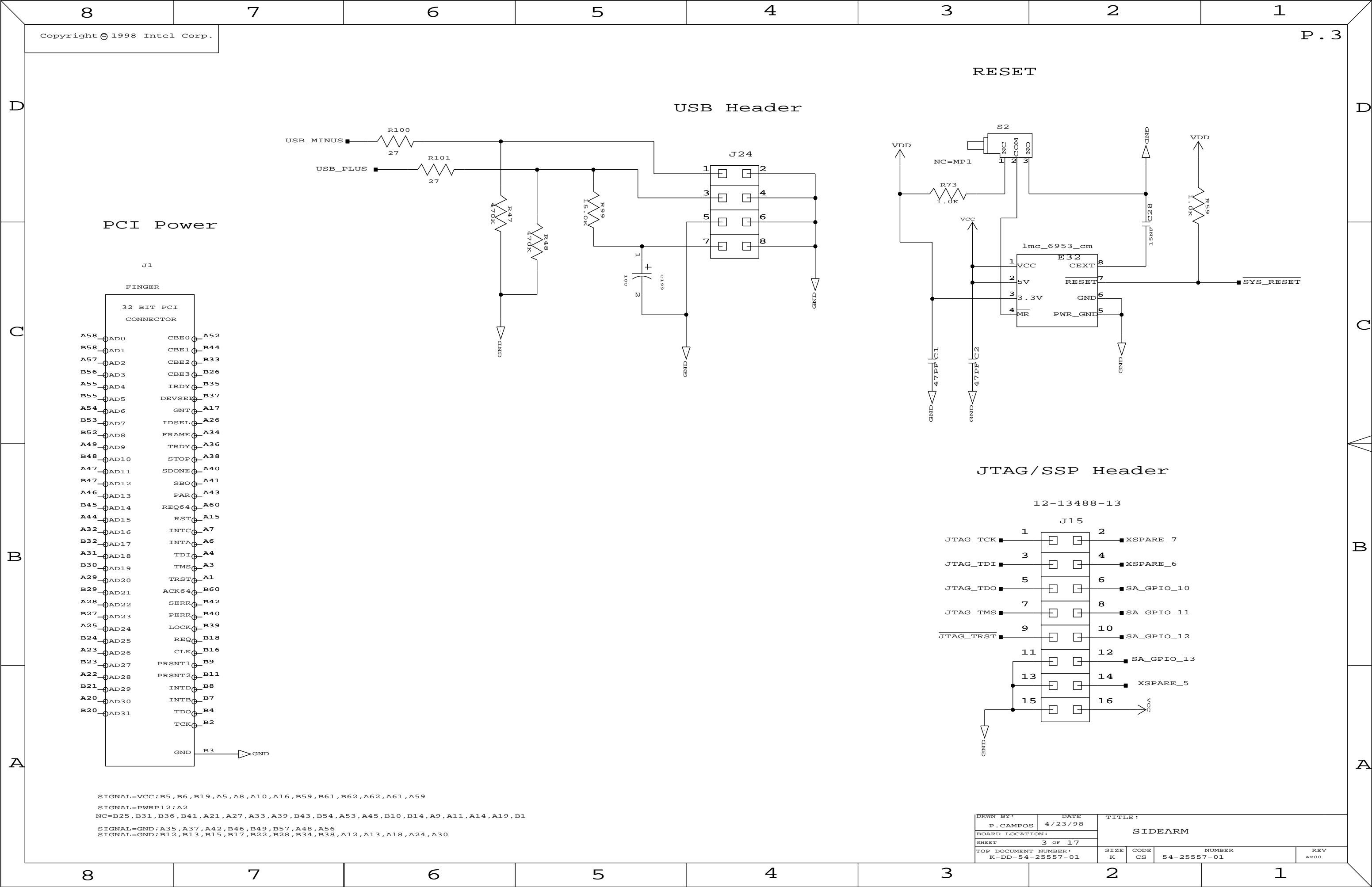
1. Contents
2. SA-1100, SA-1100 CLOCKS
3. SA-1100 Support (reset, jtag, pullups) PCI Connector
4. Flash, DRAM
5. Data Xcvrs, Address Buffers, XBUS Headers
6. 128 Pin Bus, XBUS Control, SysRegA, SysregB
7. RS-232 Port, IRDA
8. DSP CT8020, SRAM
9. UCB1200, DDA and Audio
10. BT Video Decoder Lower 16
11. BT Video Encoder Upper 8
12. ISP 1032 Datapath
13. FIFO, Datapath Control
14. Video Output 7176/7170
15. Misc LEDs, Switches
16. Voltage Regulators, Analog Supplies
17. Decoupling Capacitors

DRWN BY:	DATE	TITLE:		
P. CAMPOS	07/30/1998	SIDEARM		
BOARD LOCATION:				
SHEET		1 OF 17		
TOP DOCUMENT NUMBER:	SIZE	CODE	NUMBER	REV
K-DD-54-25557-01	B	CS	54-25557-01	AX02

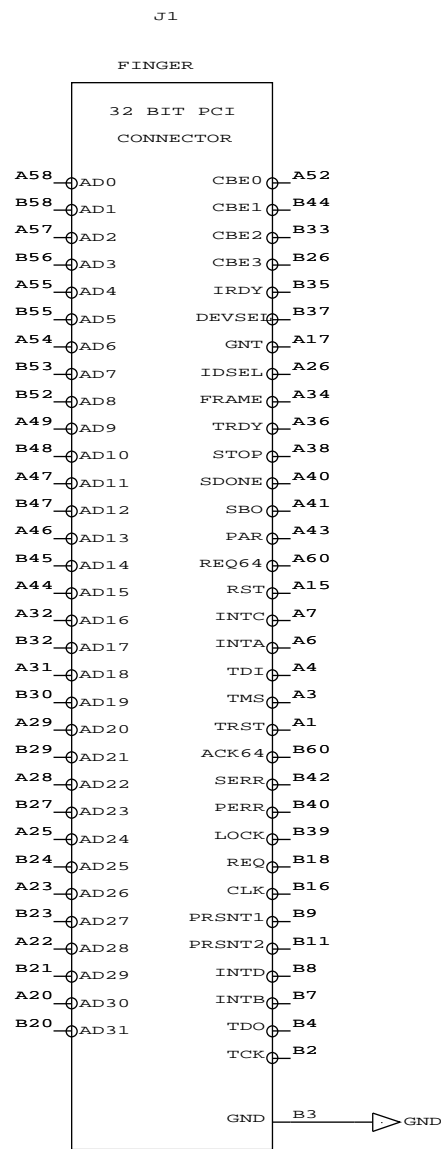


GND: 4, 6, 16, 26, 28, 38, 48, 50, 58, 68, 78, 80
 GND: 90, 100, 107, 109, 119, 129, 131, 141, 151
 GND: 153, 161, 171, 181, 183, 189, 203
 VCORE: 5, 25, 47, 79, 110, 132, 152, 188, 190
 VDD: 3, 15, 27, 37, 49, 57, 67, 77, 89, 99, 108, 120
 VDD: 130, 142, 154, 162, 172, 182, 193, 204

DRWN BY:	DATE	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET	2 OF 17	
TOP DOCUMENT NUMBER:	SIZE	CODE
K-DD-54-25557-01	K	CS
	NUMBER	REV
	54-25557-01	AX02

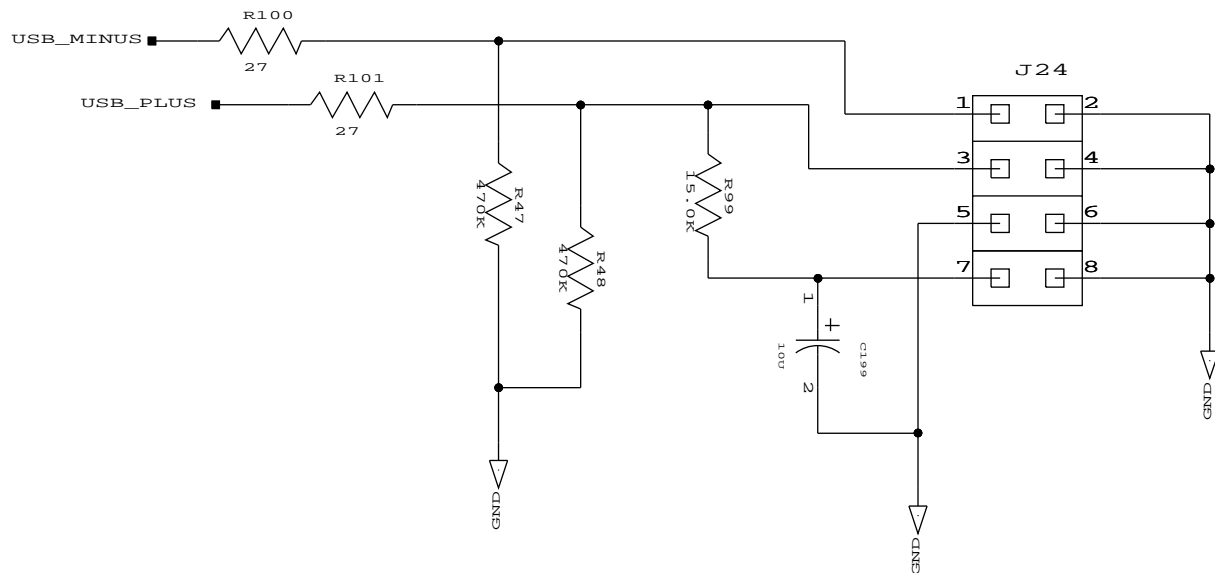


PCI Power

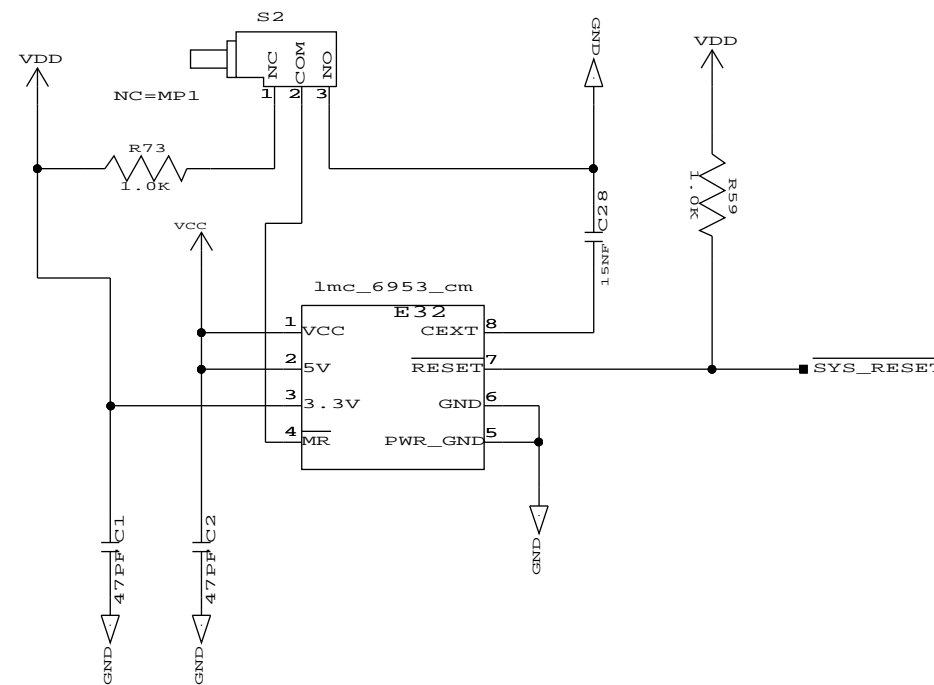


SIGNAL=VCC; B5, B6, B19, A5, A8, A10, A16, B59, B61, B62, A62, A61, A59
 SIGNAL=PWRP12; A2
 NC=B25, B31, B36, B41, A21, A27, A33, A39, B43, B54, A53, A45, B10, B14, A9, A11, A14, A19, B1
 SIGNAL=GND; A35, A37, A42, B46, B49, B57, A48, A56
 SIGNAL=GND; B12, B13, B15, B17, B22, B28, B34, B38, A12, A13, A18, A24, A30

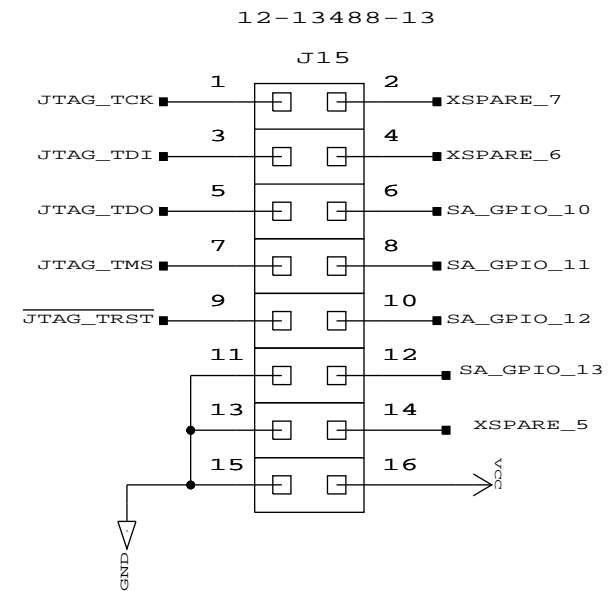
USB Header



RESET



JTAG/SSP Header

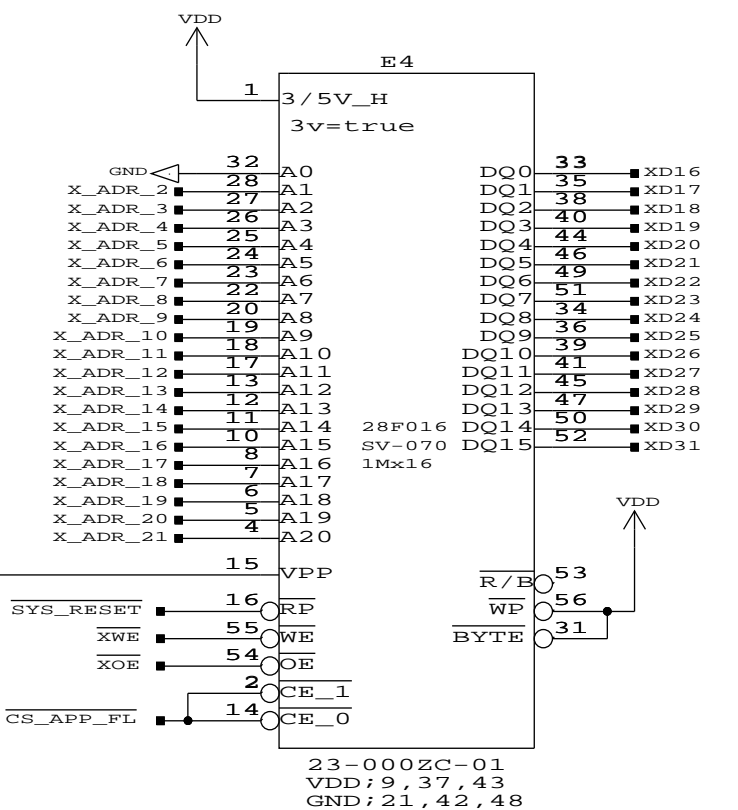
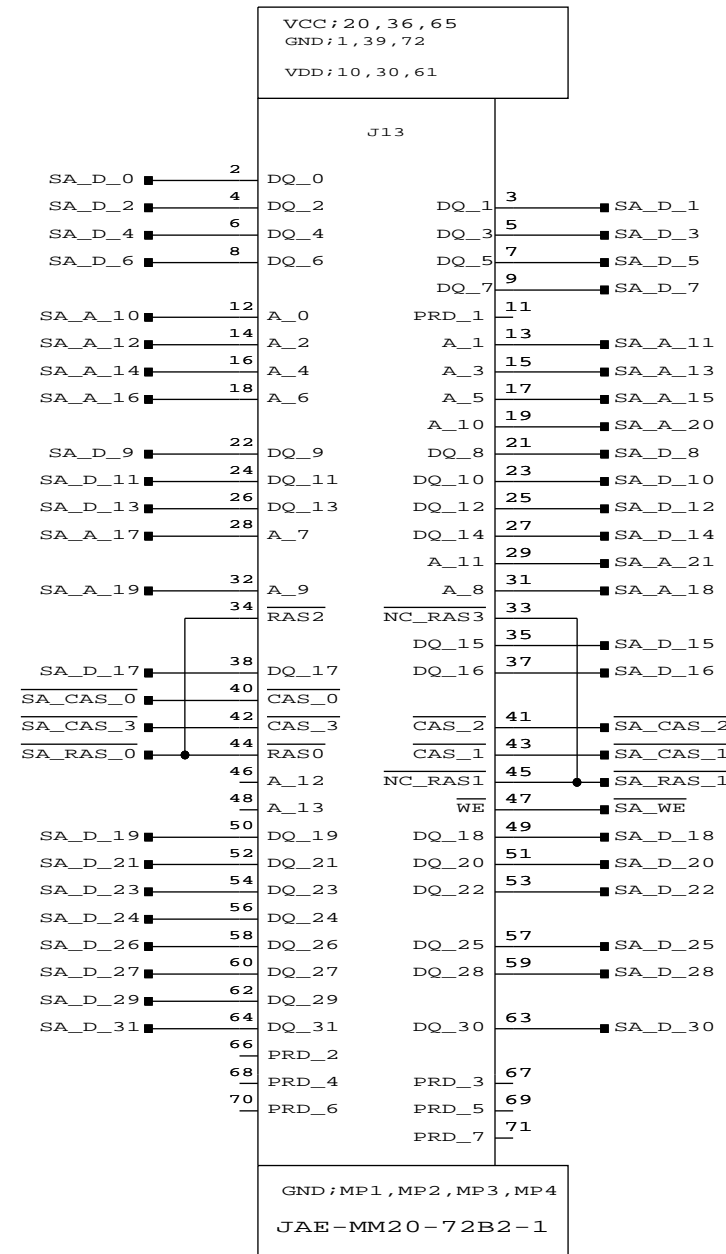
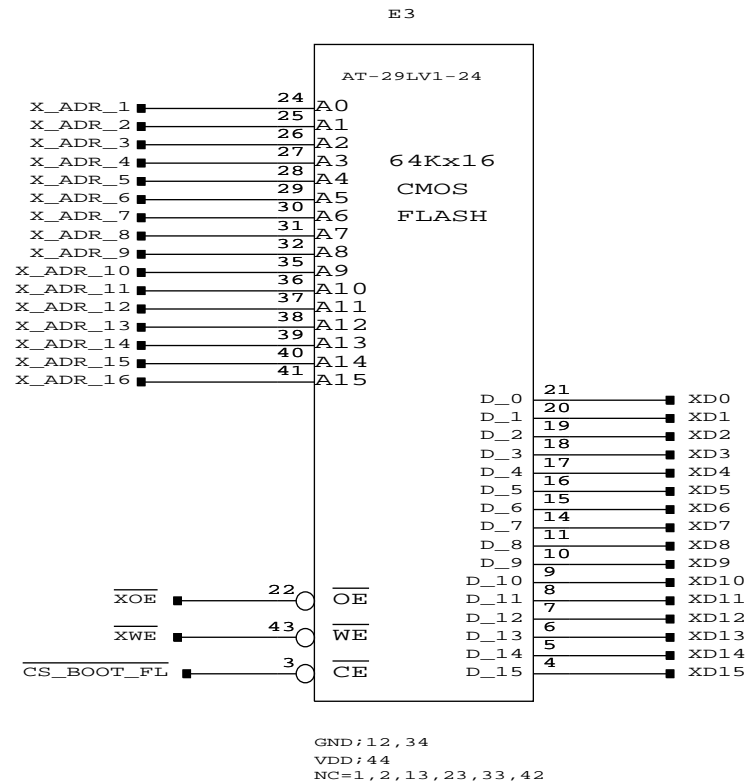
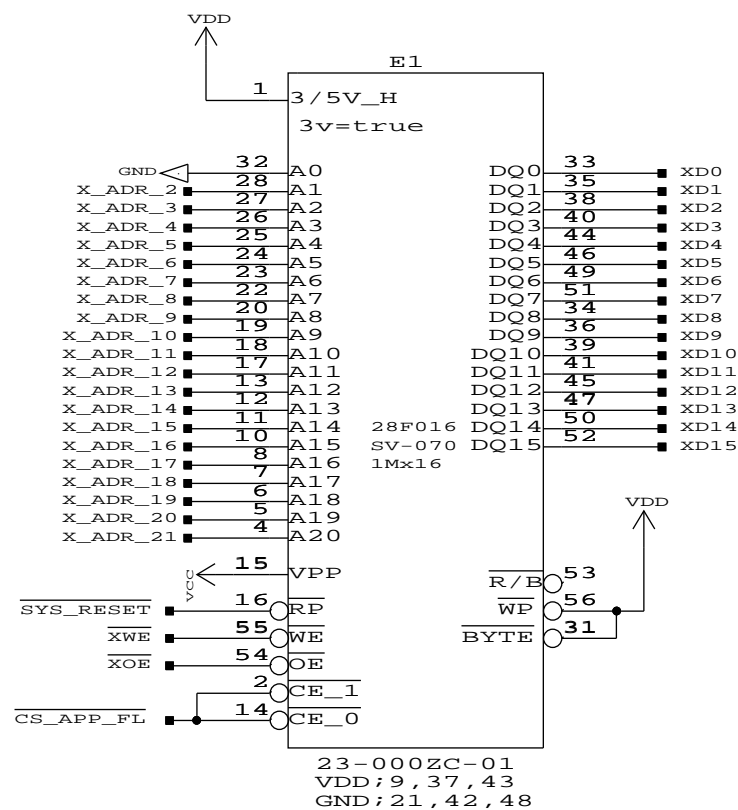


DRWN BY:	DATE:	TITLE:	
P. CAMPOS	4/23/98	SIDEARM	
BOARD LOCATION:	SHEET		
	3 OF 17		
TOP DOCUMENT NUMBER:	SIZE	CODE	NUMBER
K-DD-54-25557-01	K	CS	54-25557-01
			REV
			AK00

Application Flash

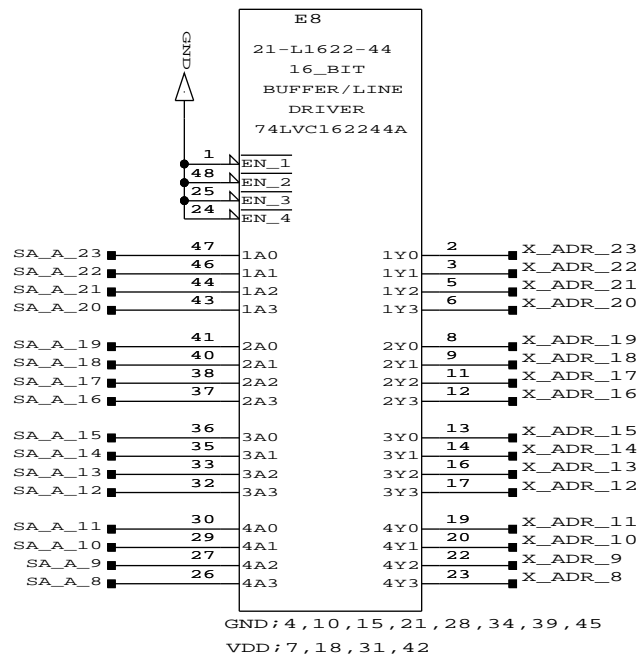
Boot Flash

DRAM SODIMM

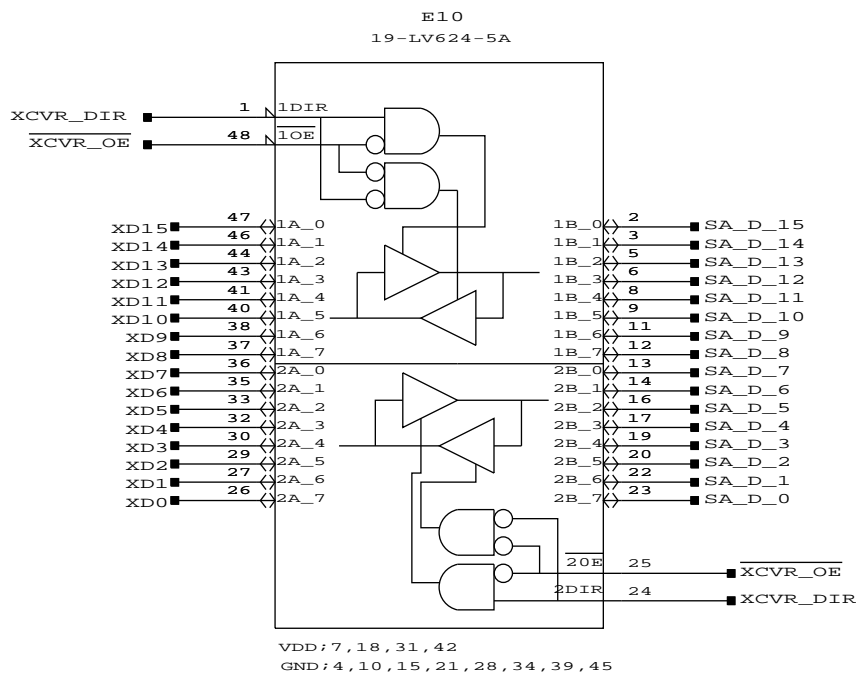


DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET 4 OF 17		
TOP DOCUMENT NUMBER:	SIZE:	CODE:
K-DD-54-25557-01	K	CS
NUMBER:	REV:	
54-25557-01	AK02	

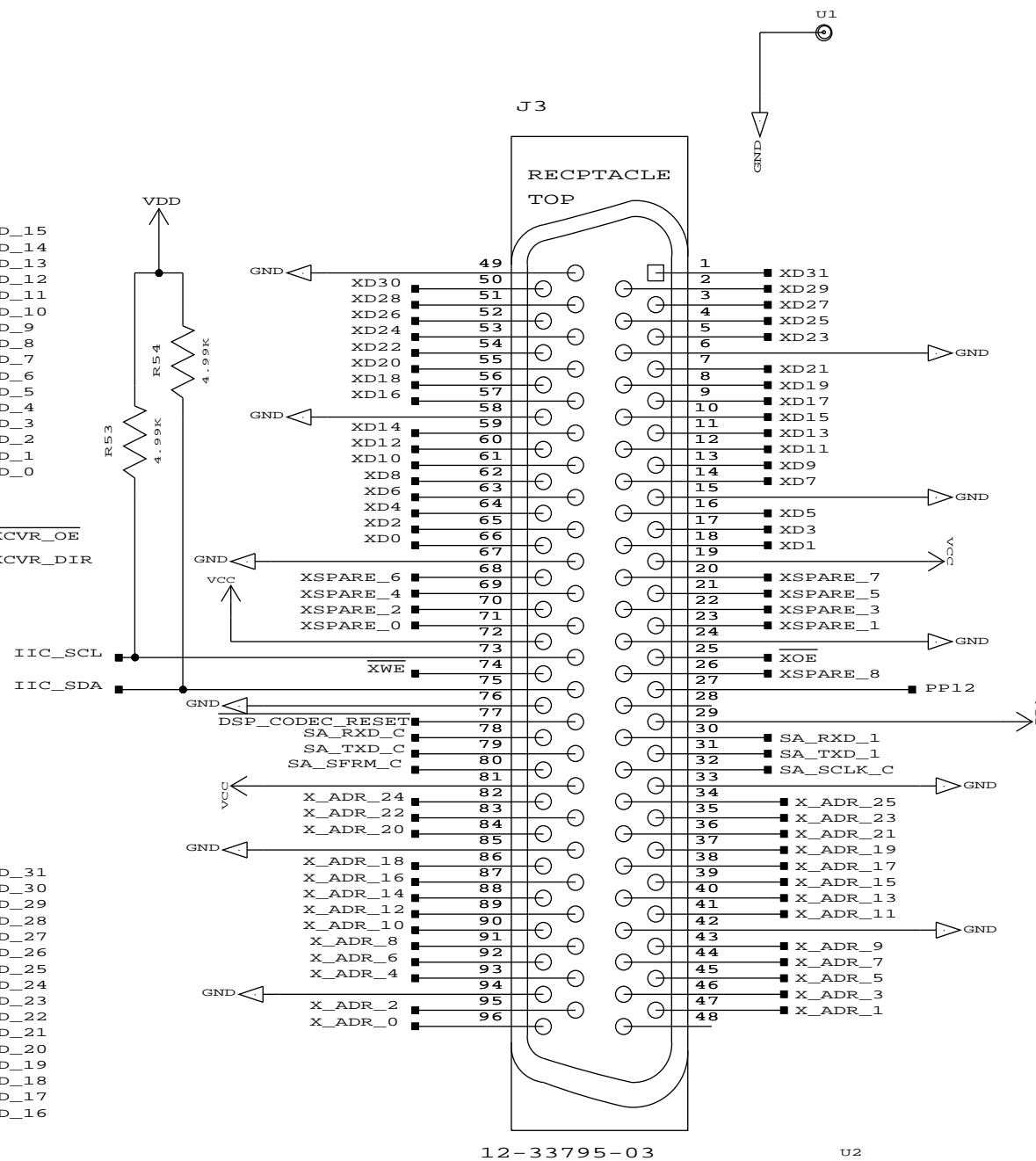
Drivers



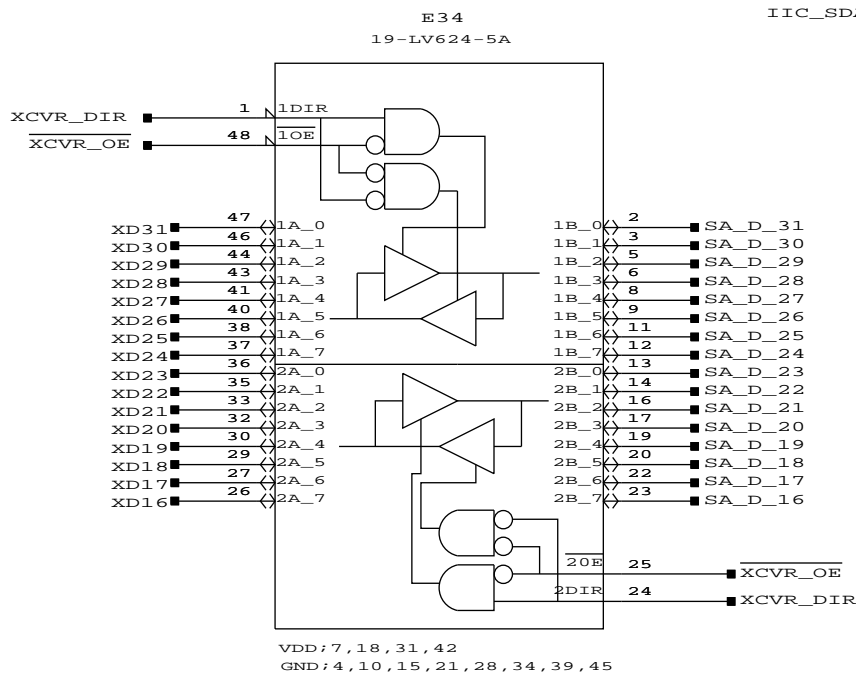
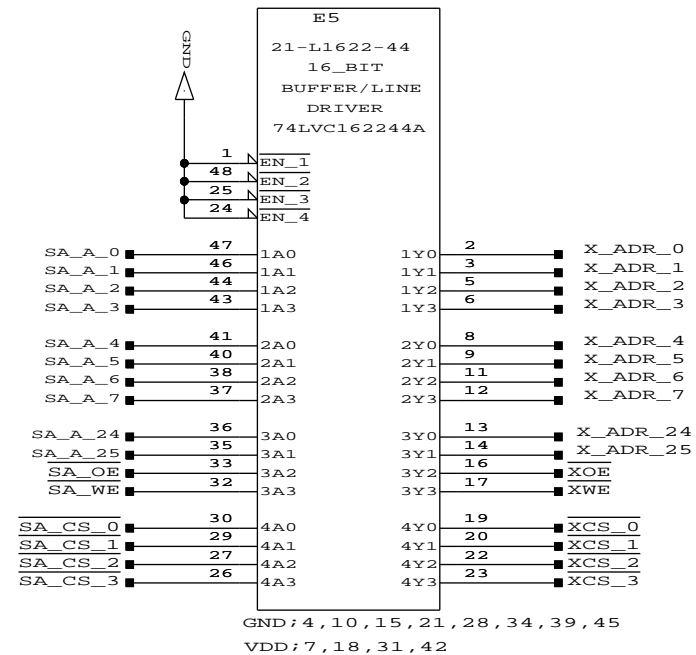
Tranceivers



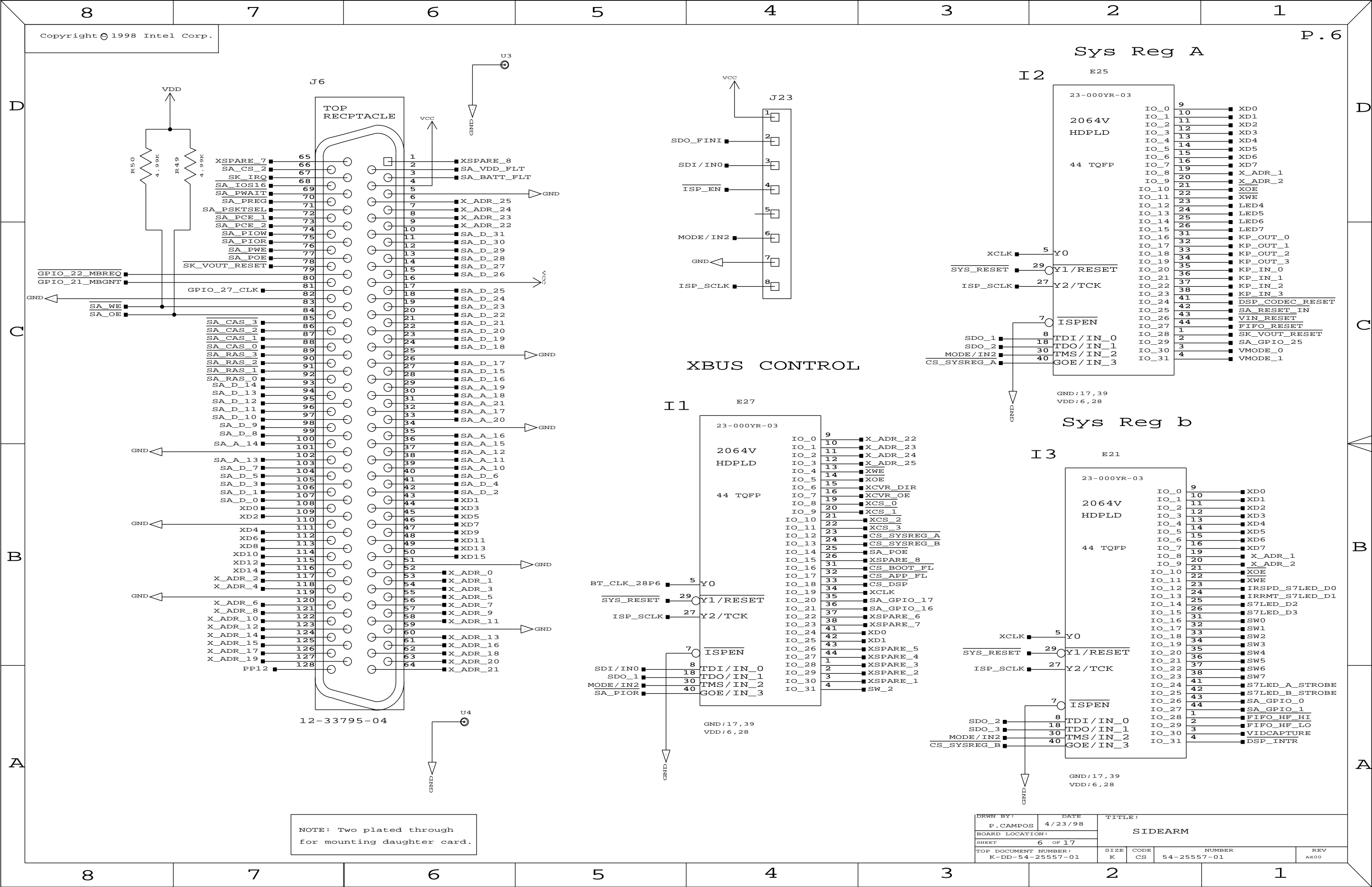
XBUS Header



NOTE: Two plated through for mounting daughter card.



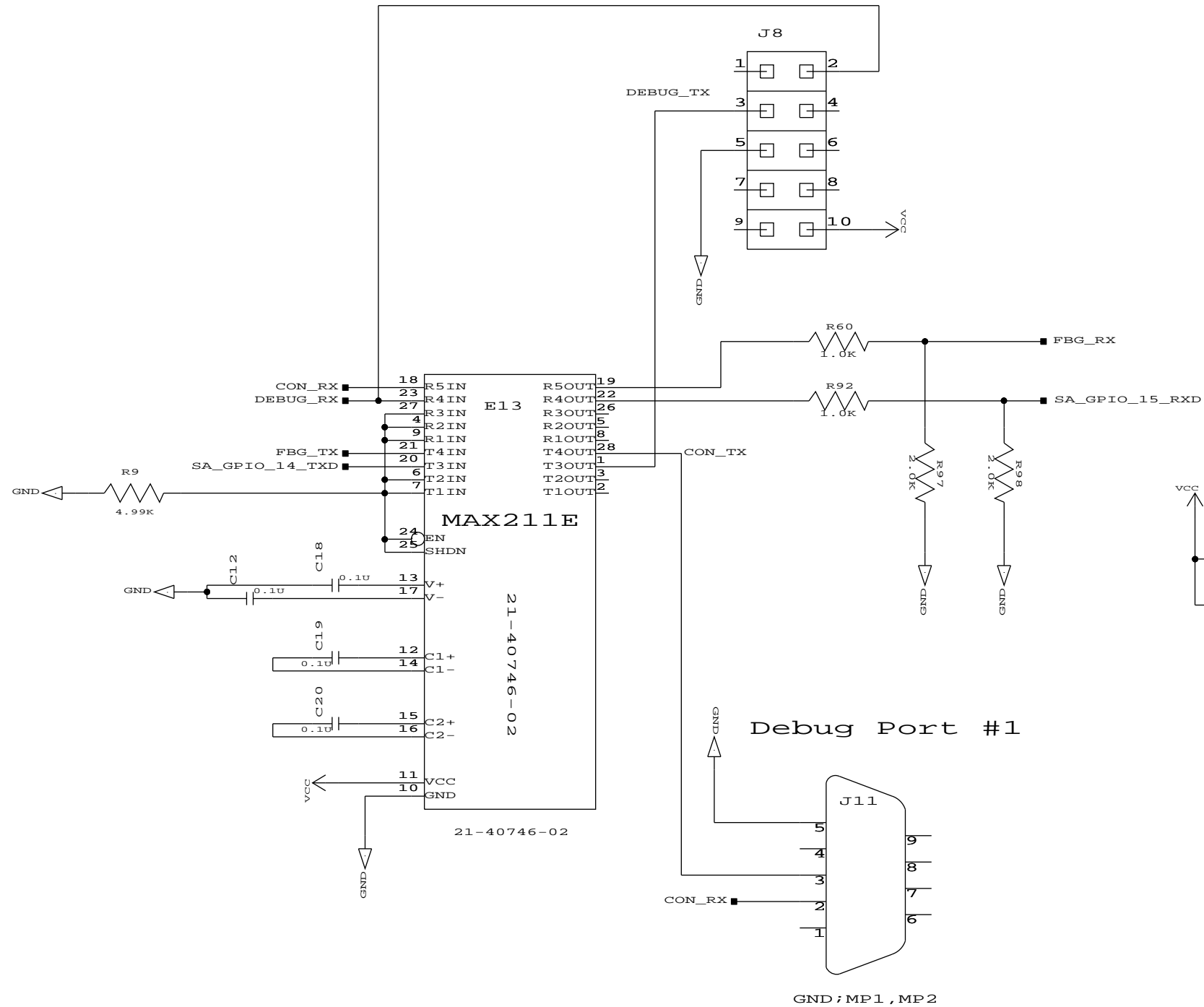
DRWN BY:	DATE	TITLE:		
P. CAMPOS	07/30/1998	SIDEARM		
BOARD LOCATION:				
SHEET	5 OF 17			
TOP DOCUMENT NUMBER:	SIZE	CODE	NUMBER	REV
K-DD-54-25557-01	K	CS	54-25557-01	AX02



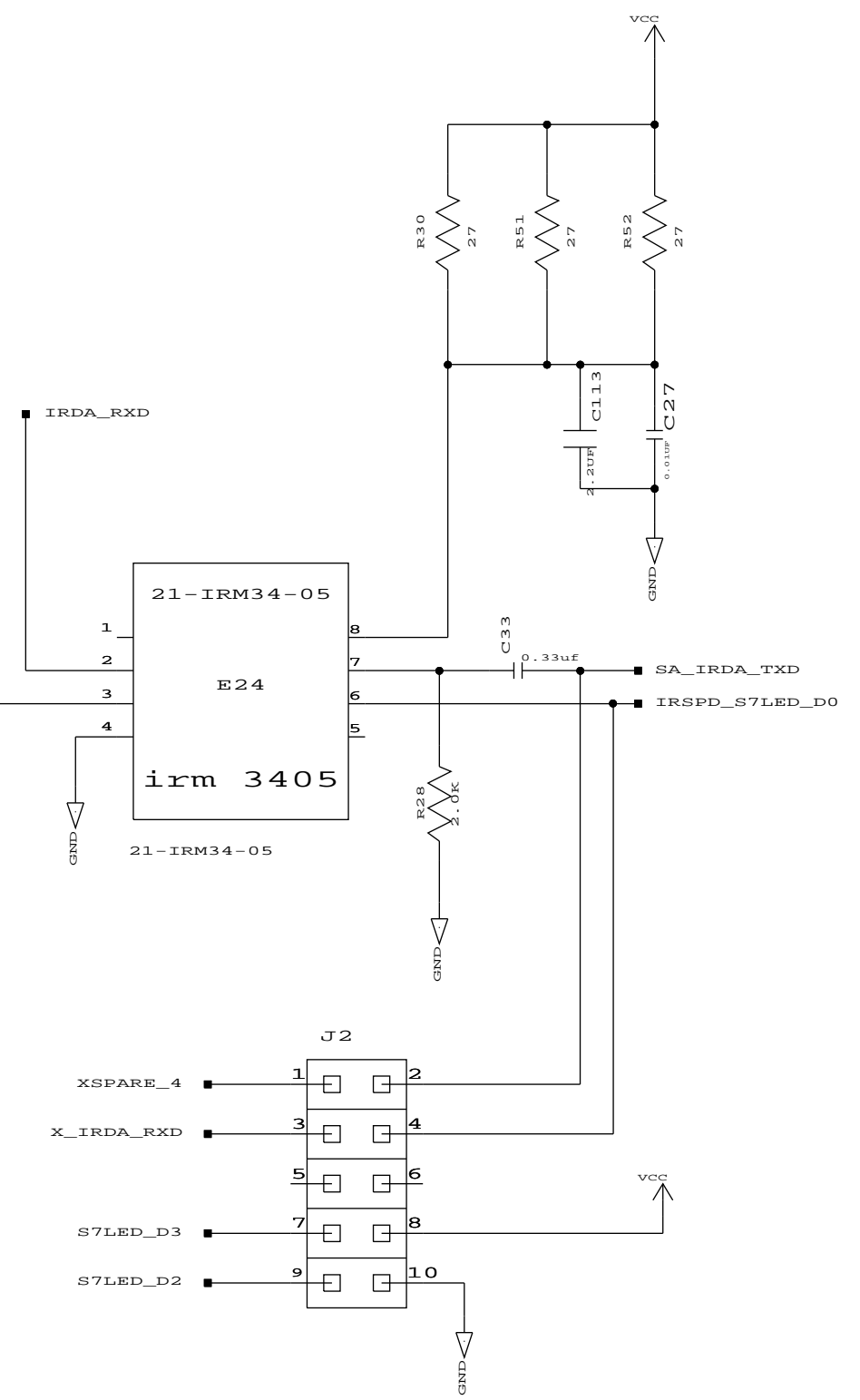
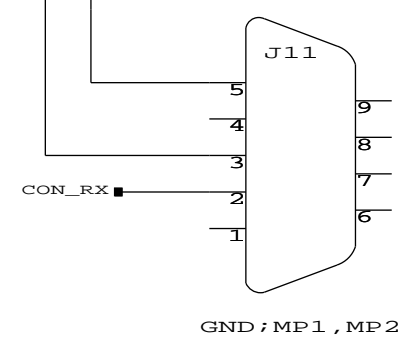
NOTE: Two plated through
for mounting daughter card.

DRWN BY:	DATE:	TITLE:
P. CAMPOS	4/23/98	SIDEARM
BOARD LOCATION:		
SHEET:	6 OF 17	
TOP DOCUMENT NUMBER:	SIZE:	CODE:
K-DD-54-25557-01	K	CS
	NUMBER:	REV:
	54-25557-01	AX00

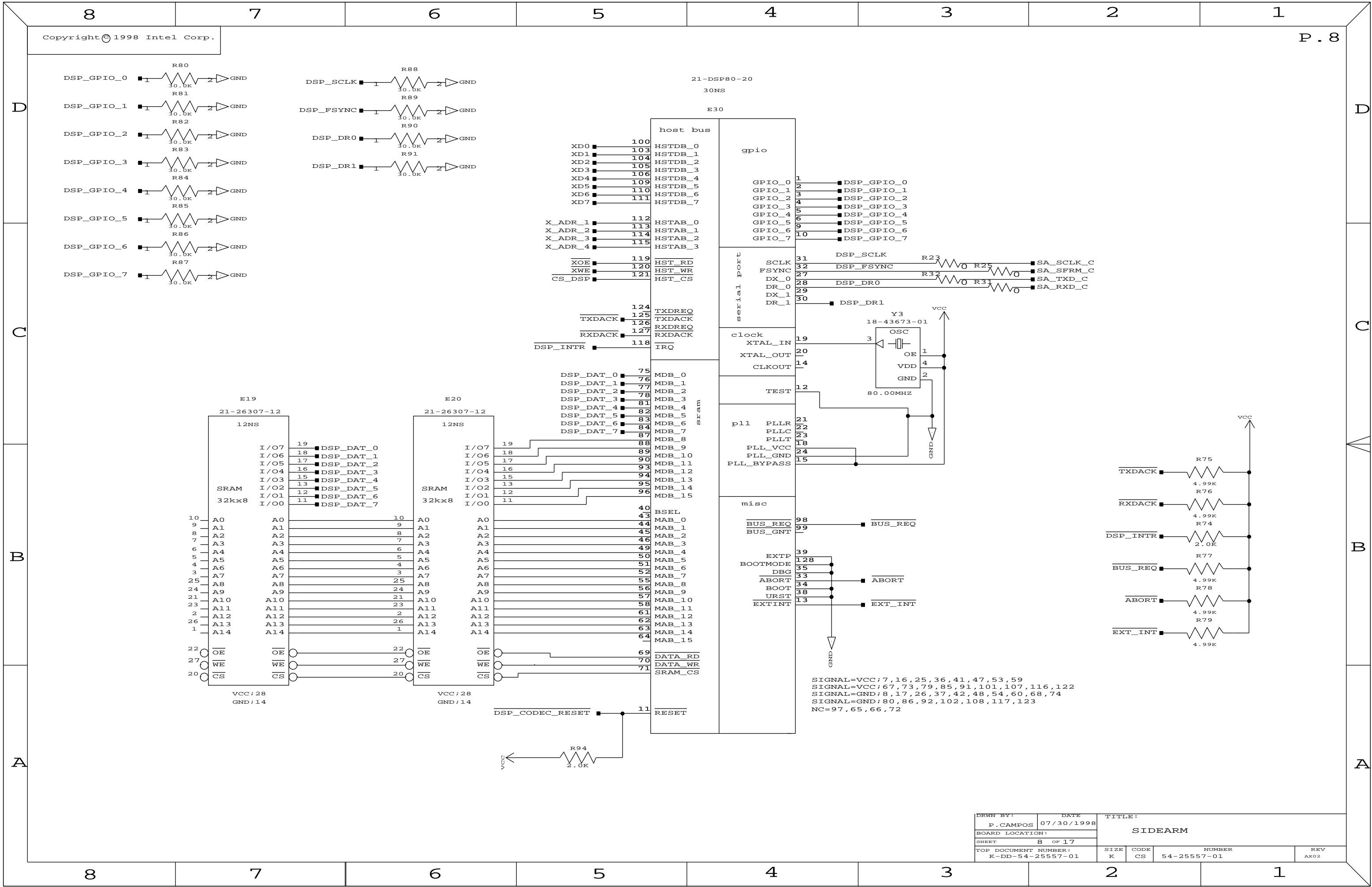
Debug Port #2



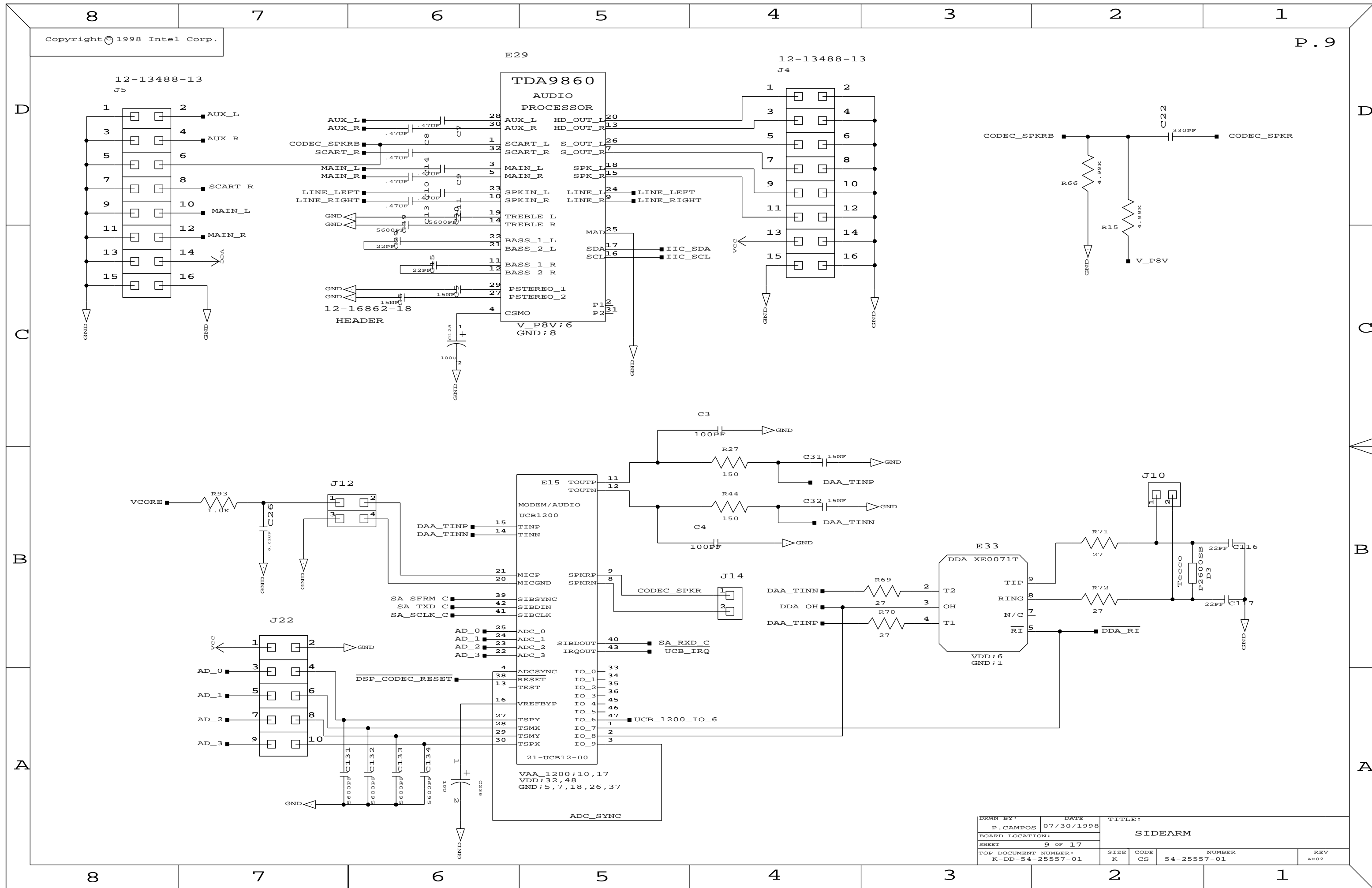
Debug Port #1



DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET	7 OF 17	
TOP DOCUMENT NUMBER:	SIZE	CODE
K-DD-54-25557-01	K	CS
	NUMBER	REV
	54-25557-01	AX02



DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET 8 OF 17		
TOP DOCUMENT NUMBER:	SIZE:	CODE:
K-DD-54-25557-01	K	CS
NUMBER:	REV:	
54-25557-01	AX02	



DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET:	9 OF 17	
TOP DOCUMENT NUMBER:	SIZE:	CODE:
K-DD-54-25557-01	K	CS
	NUMBER:	REV:
	54-25557-01	AK02

PIP DECODER VIDEO

D

D

C

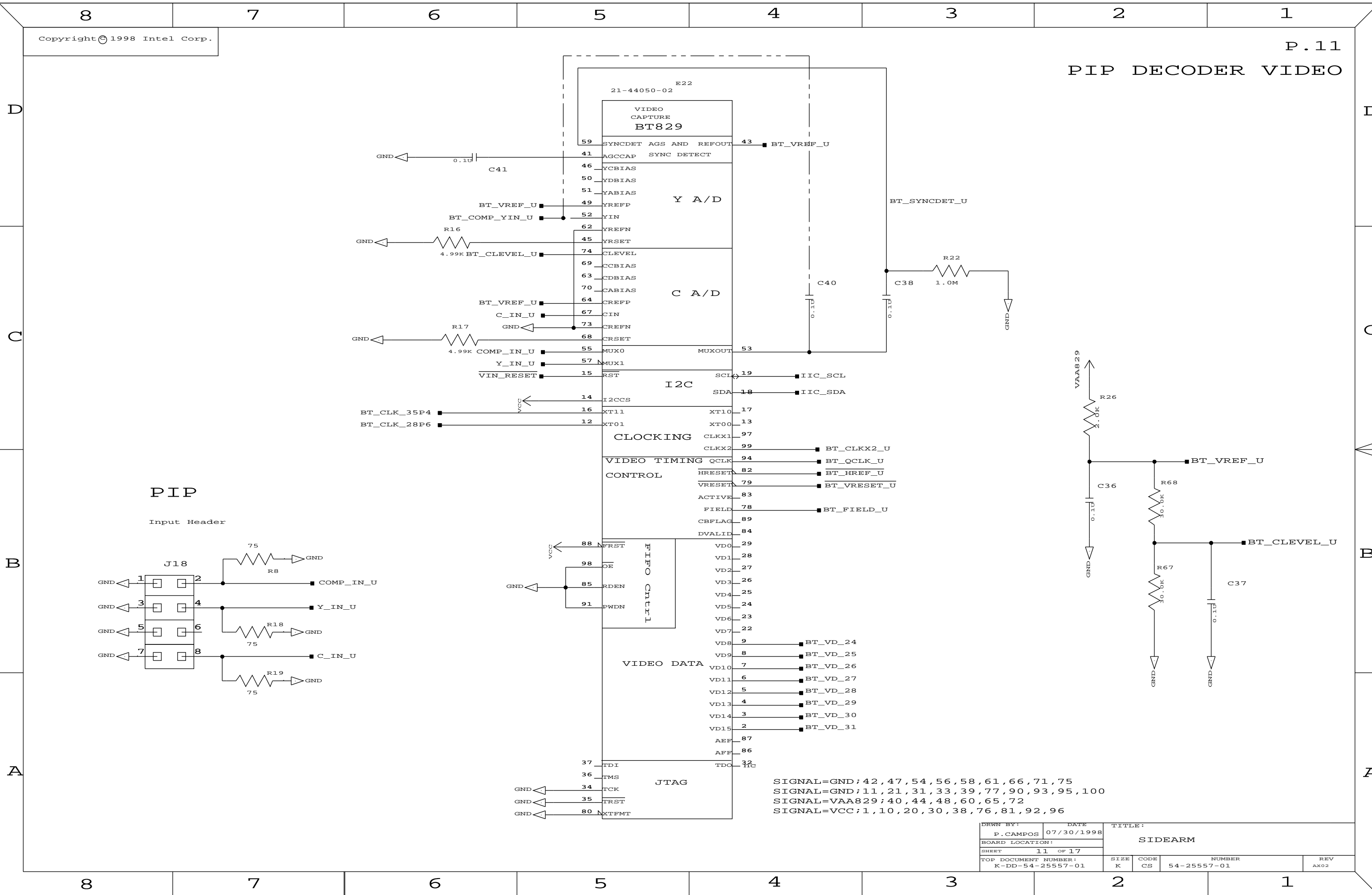
C

B

B

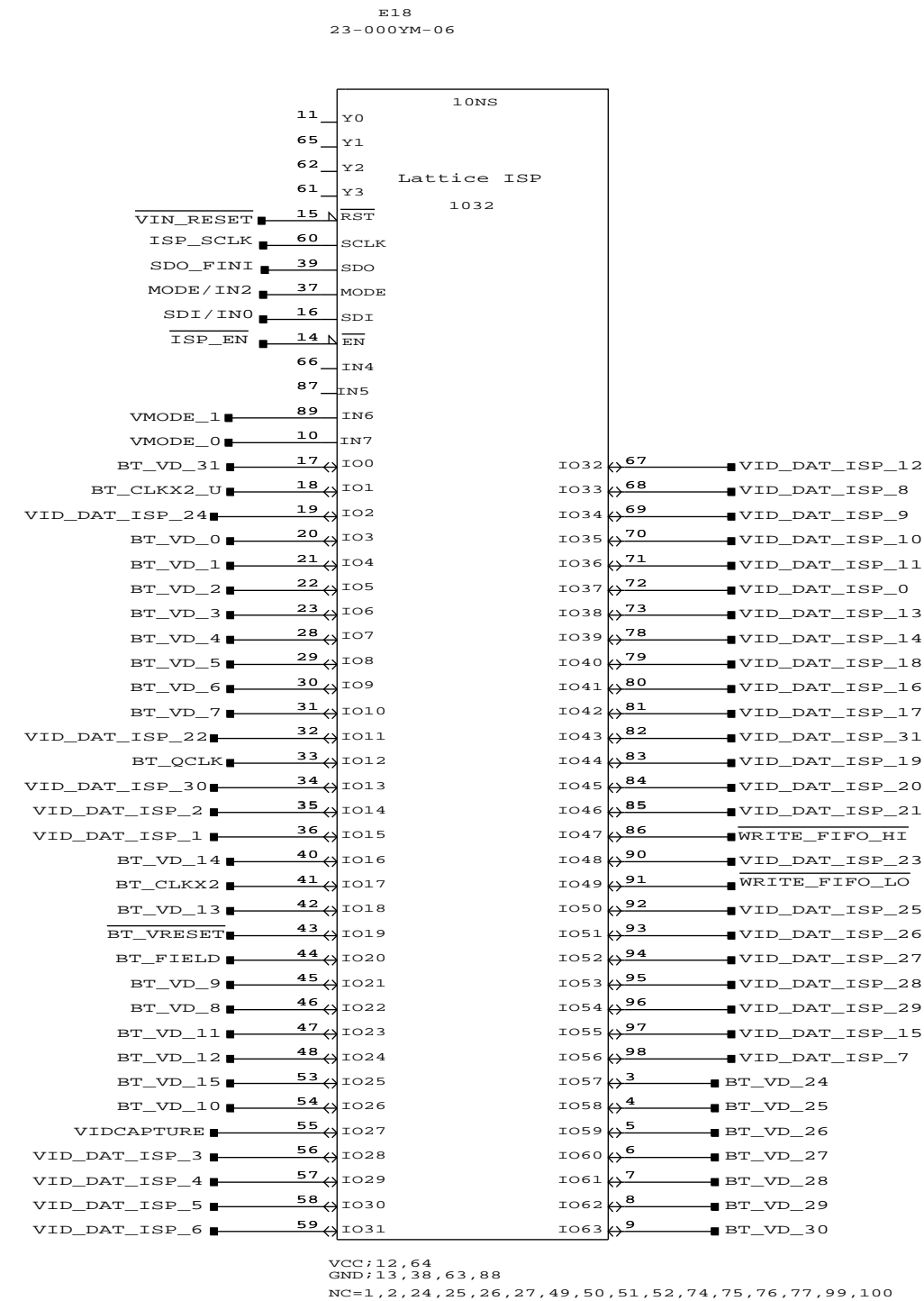
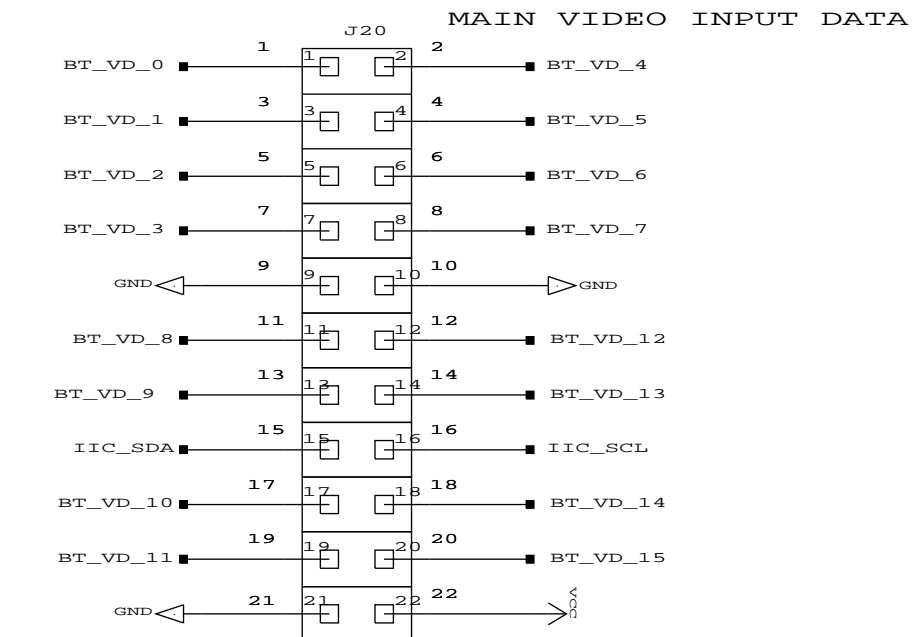
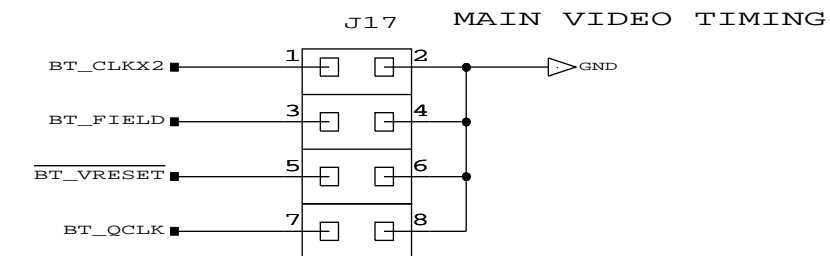
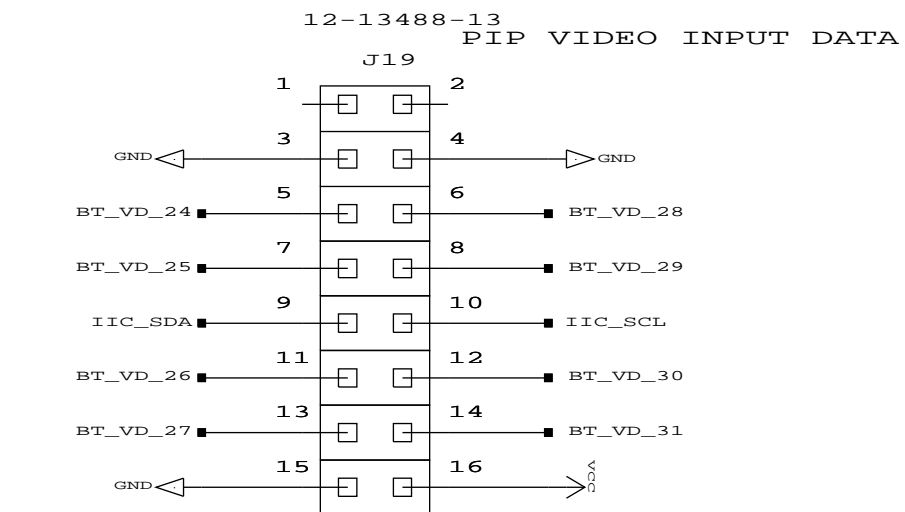
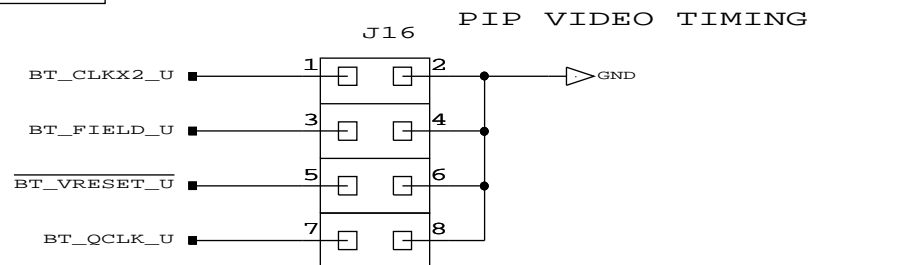
A

A



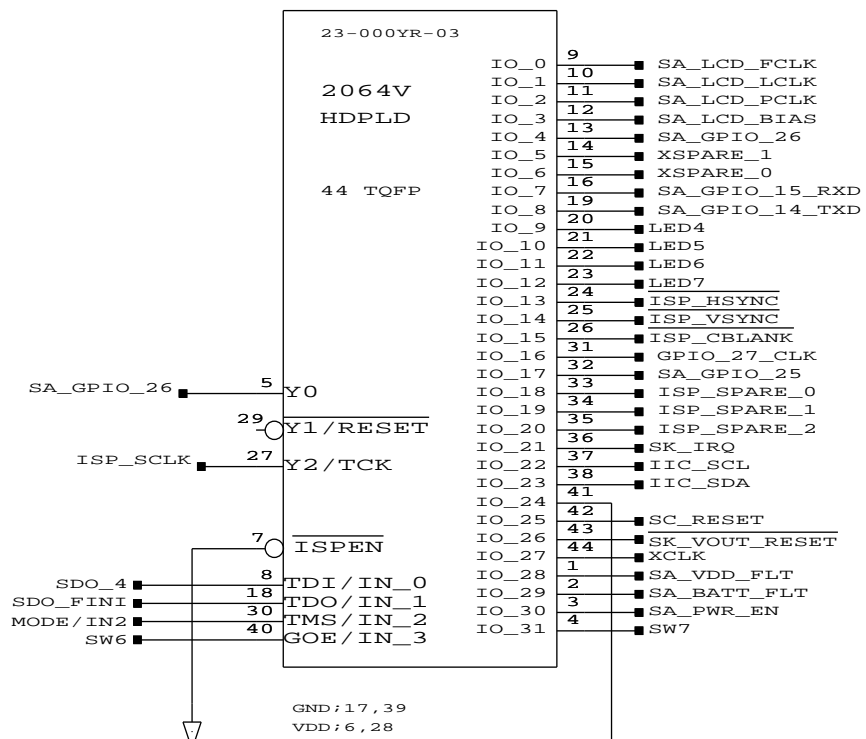
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 SIGNAL=GND; 11, 21, 31, 33, 39, 77, 90, 93, 95, 100
 SIGNAL=VAA829; 40, 44, 48, 60, 65, 72
 SIGNAL=VCC; 1, 10, 20, 30, 38, 76, 81, 92, 96

DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET 11 OF 17	SIZE K	CODE CS
TOP DOCUMENT NUMBER: K-DD-54-25557-01	NUMBER 54-25557-01	REV AX02

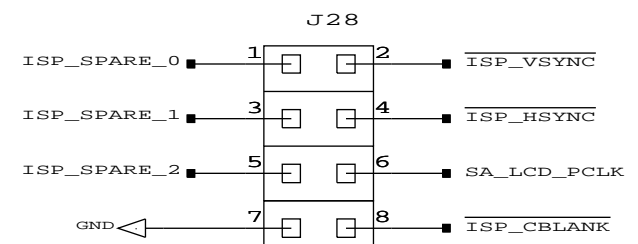


DRWN BY:	DATE:	TITLE:			
P. CAMPOS	07/30/1998	SIDEARM			
BOARD LOCATION:					
SHEET 12 OF 17					
TOP DOCUMENT NUMBER:	SIZE:	CODE:	NUMBER:	REV:	
K-DD-54-25557-01	K	CS	54-25557-01	AX02	

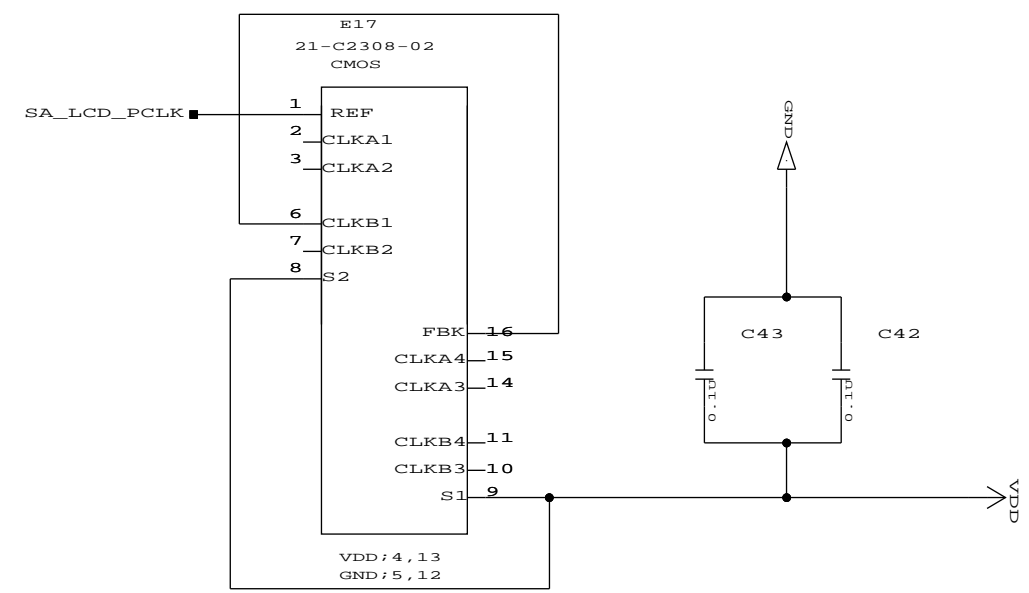
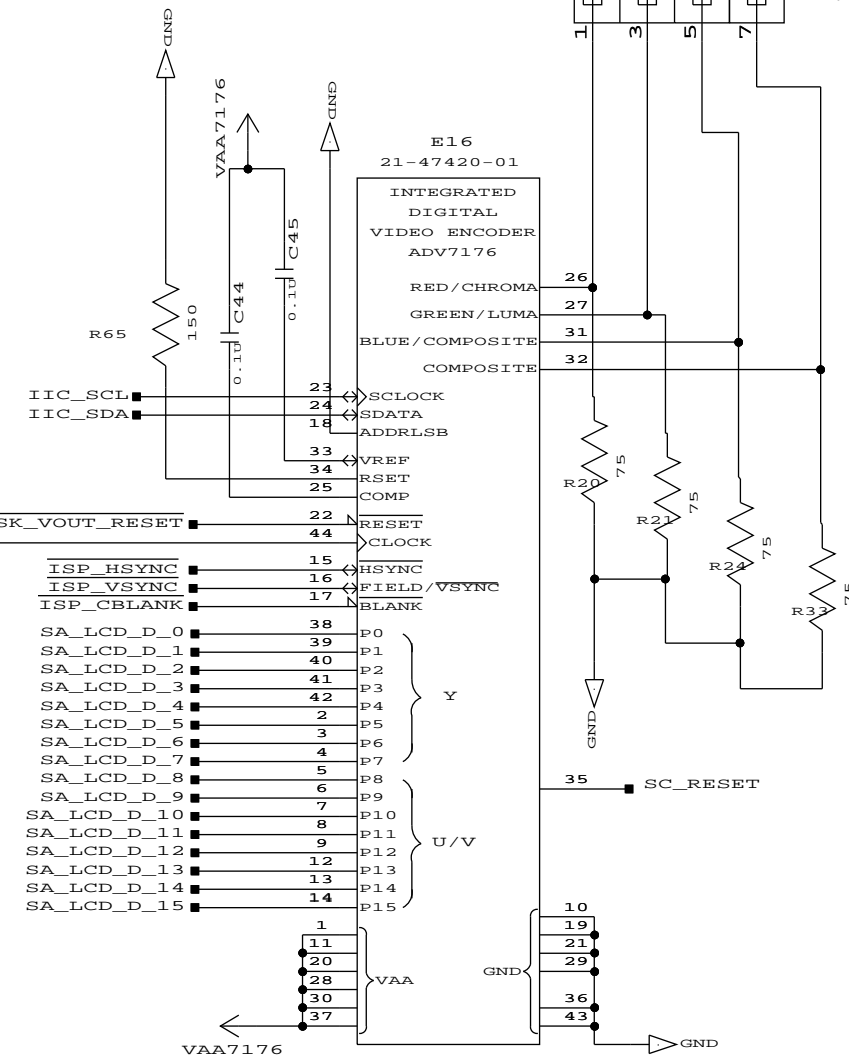
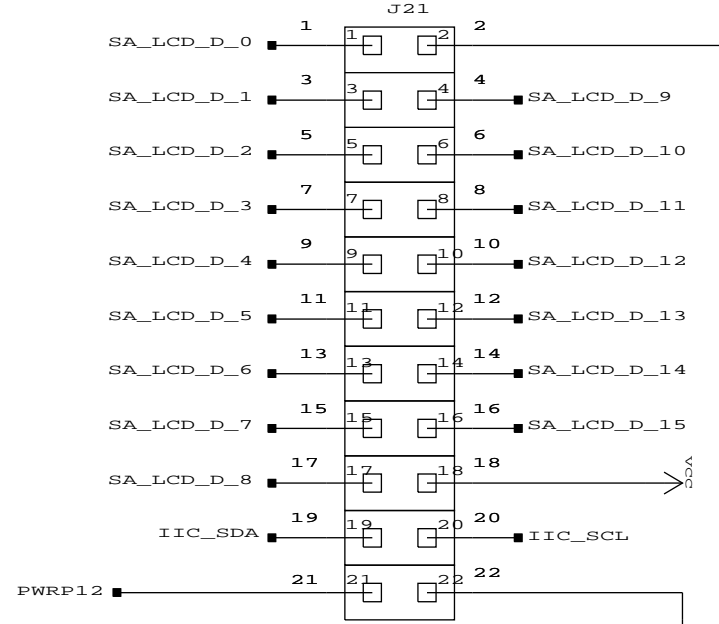
E31



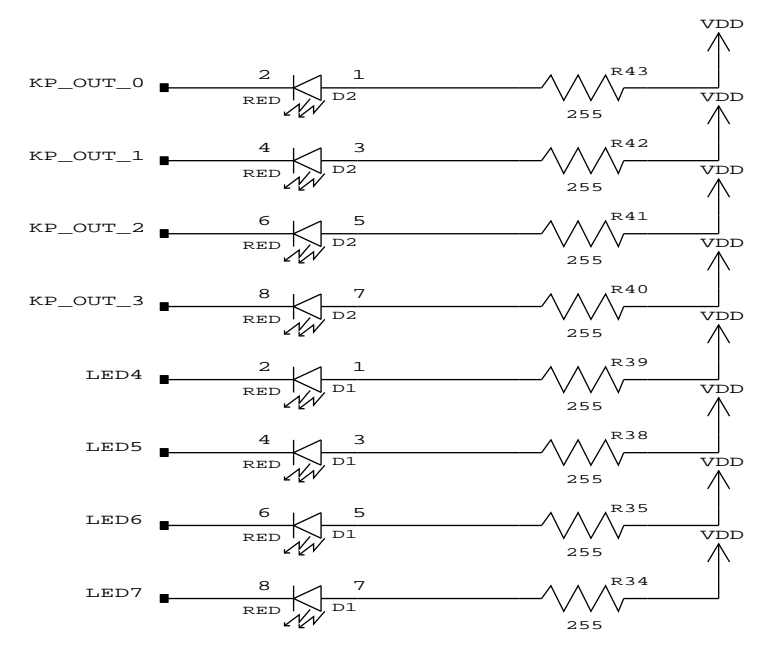
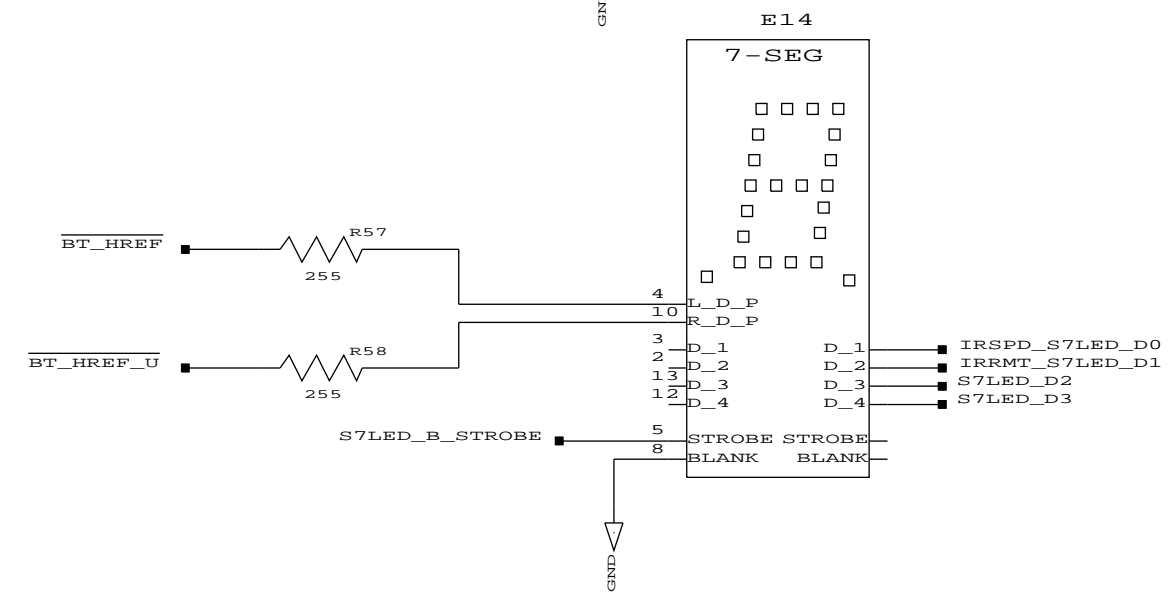
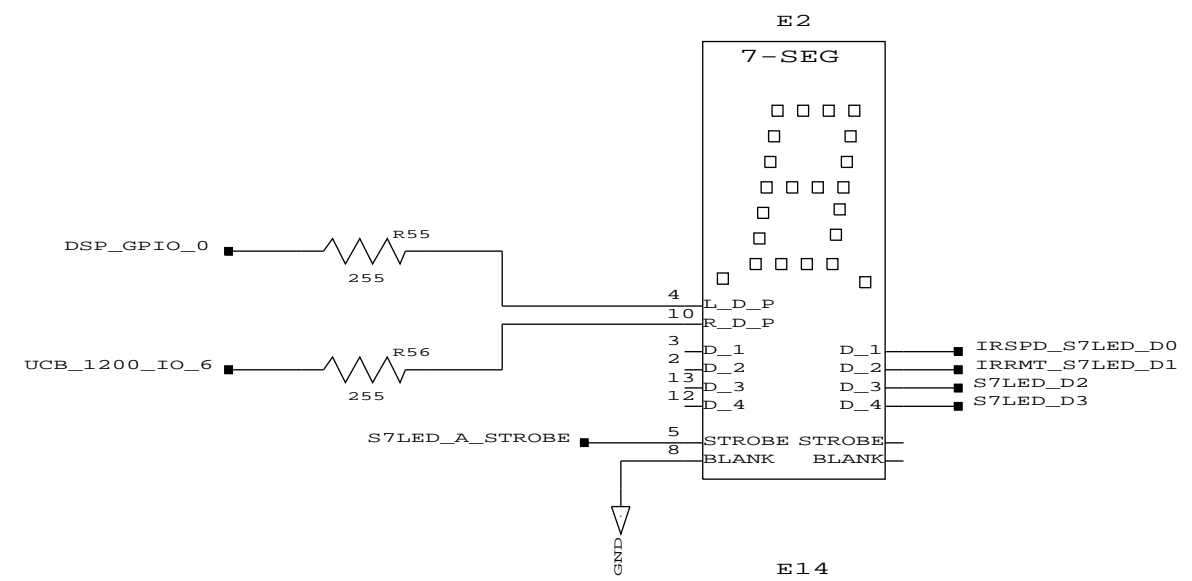
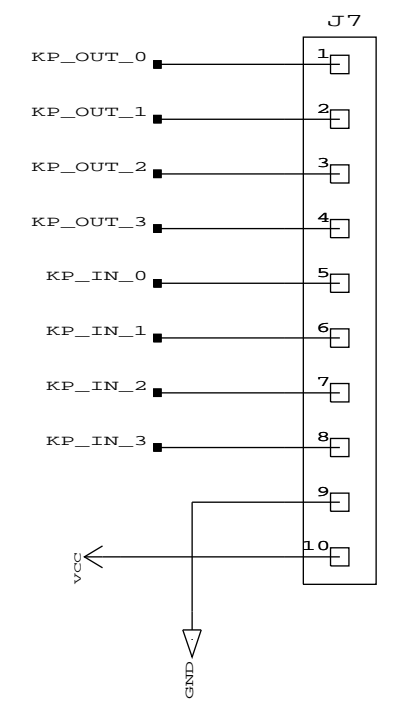
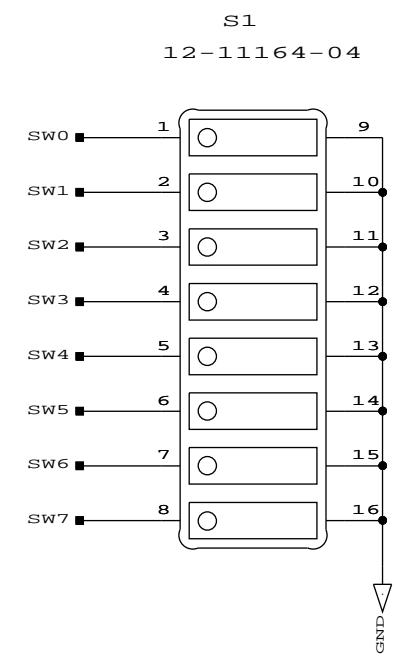
LCD TIMING



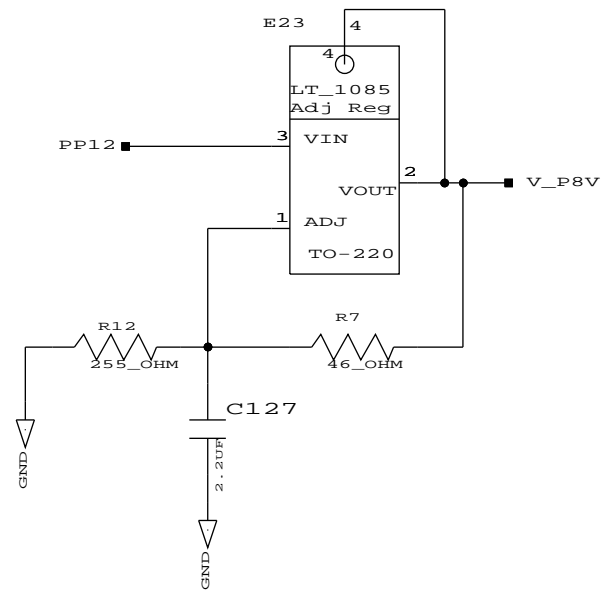
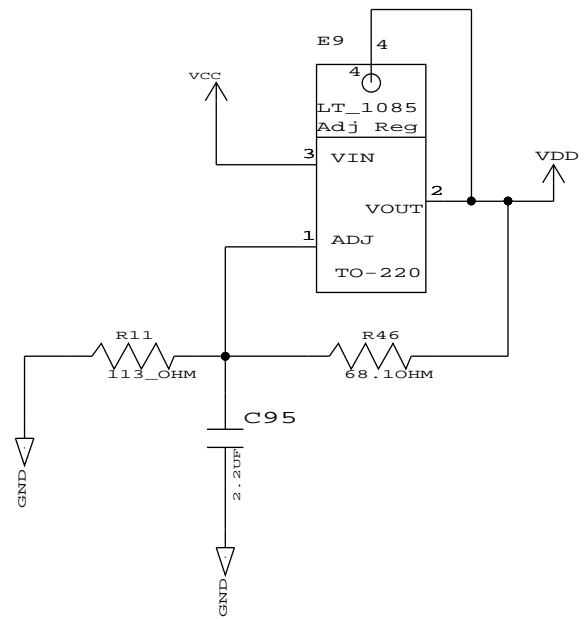
LCD OUTPUT DATA



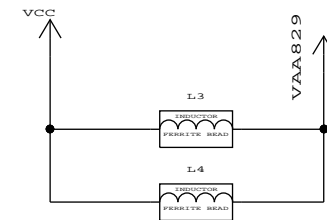
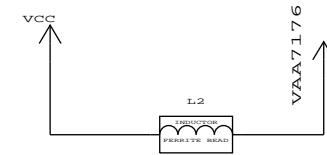
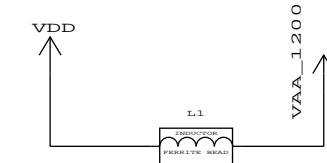
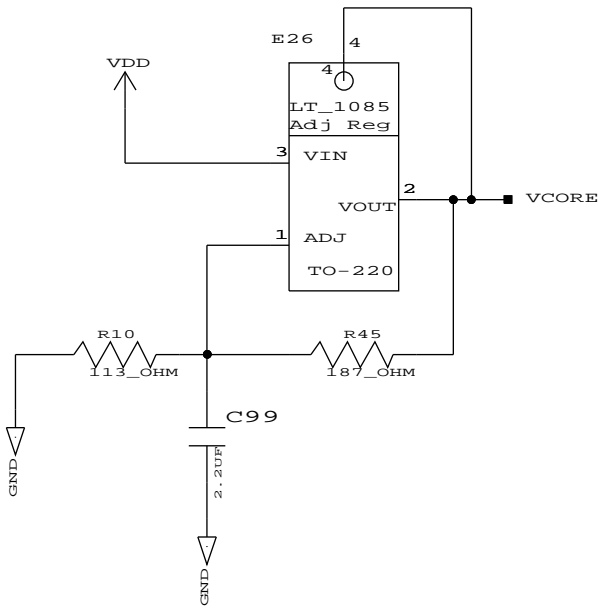
DRWN BY:	DATE	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:	4	4
SHEET	14 OF 17	NUMBER
TOP DOCUMENT NUMBER:	SIZE	CODE
K-DD-54-25557-01	B	CS
	NUMBER	REV
	54-25557-01	AX02



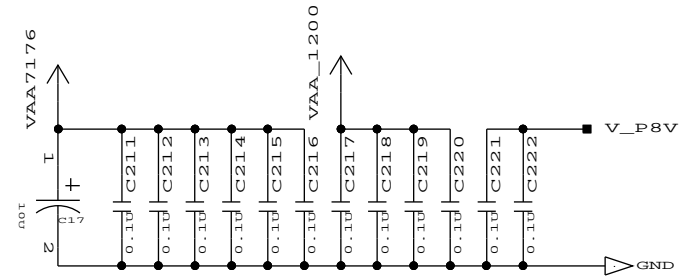
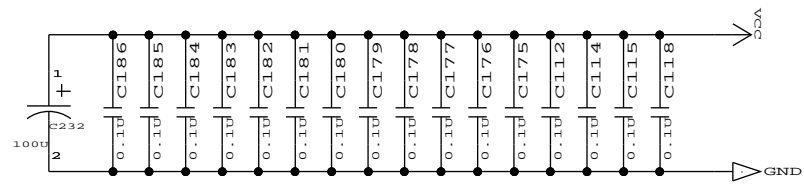
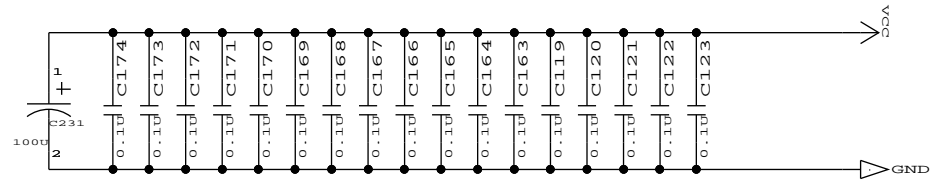
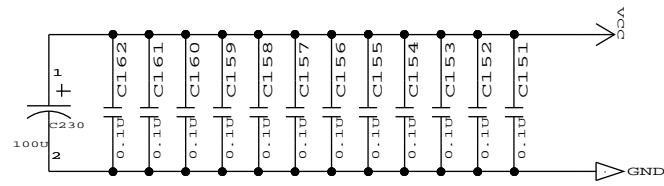
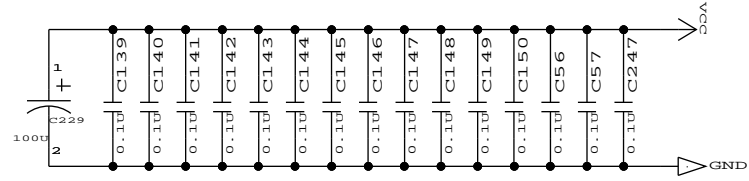
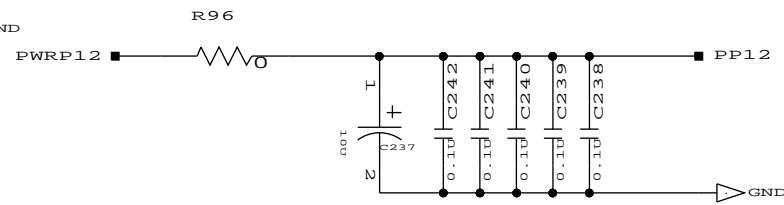
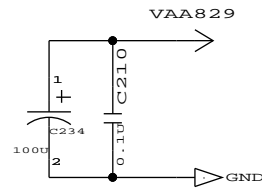
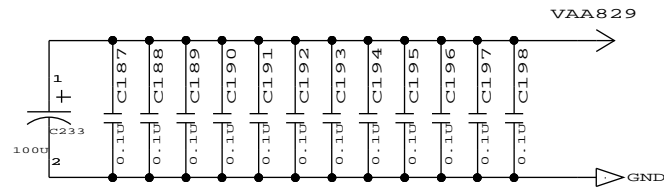
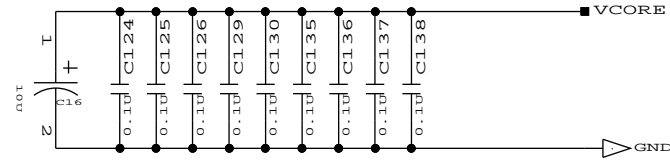
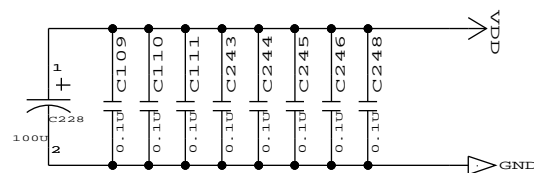
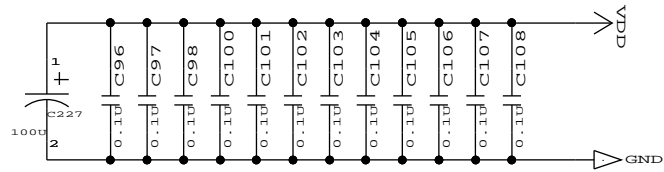
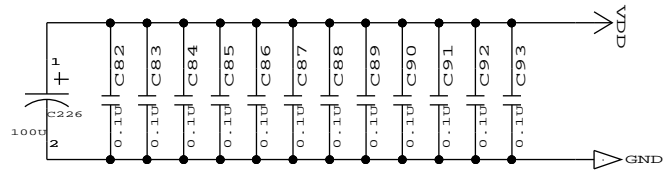
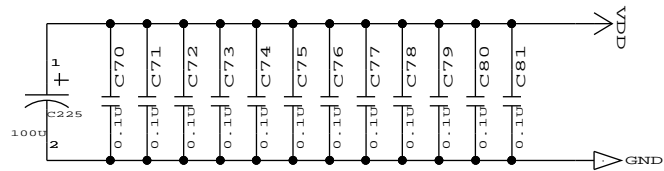
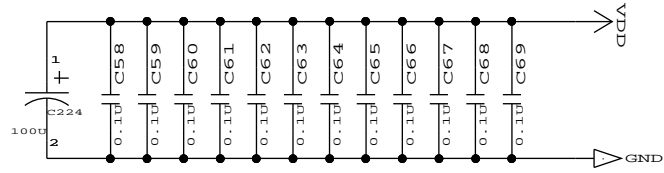
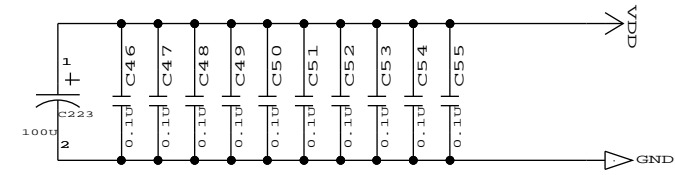
DRWN BY:	DATE:	TITLE:
P. CAMPOS	07/30/1998	SIDEARM
BOARD LOCATION:		
SHEET 15 OF 17		
TOP DOCUMENT NUMBER:	SIZE:	CODE:
K-DD-54-25557-01	B	CS
NUMBER:	REV:	
54-25557-01	AX02	



Note: Formula for VOUT is
 $V_{out} = 1.25 * (1 + R_{bot} / R_{top})$



DRWN BY:	DATE:	TITLE:			
P. CAMPOS	07/30/1998	SIDEARM			
BOARD LOCATION:					
SHEET 16 OF 17					
TOP DOCUMENT NUMBER:	SIZE:	CODE:	NUMBER:	REV:	
K-DD-54-25557-01	K	CS	54-25557-01	AX02	



DRWN BY: P. CAMPOS	DATE 07/30/1998	TITLE: SIDEARM		
BOARD LOCATION:		NUMBER 54-25557-01		
SHEET 17 OF 17		SIZE K	CODE CS	REV AX02
TOP DOCUMENT NUMBER: K-DD-54-25557-01				