

## **PGAS** Programming and Chapel

Brad Chamberlain, Chapel Team, Cray Inc. UW CSEP 524, Spring 2015

**April 28th, 2015** 



#### **Safe Harbor Statement**



This presentation may contain forward-looking statements that are based on our current expectations. Forward looking statements may include statements about our financial guidance and expected operating results, our opportunities and future potential, our product development and new product introduction plans, our ability to expand and penetrate our addressable markets and other statements that are not historical facts. These statements are only predictions and actual results may materially vary from those projected. Please refer to Cray's documents filed with the SEC from time to time concerning factors that could affect the Company and these forward-looking statements.



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#### "Who is this guy Mike dumped on us?"



#### 2001: graduated from UW CSE with a PhD

- worked on the ZPL parallel programming language
- advisor: Larry Snyder (now emeritus)

#### 2001-2002: spent a lost/instructive year at a startup

#### 2002-present: have been working at Cray Inc.

- Hired to help with the HPCS program
- Convinced execs/customers that we should do a language
- Have been working on Chapel ever since



#### Also a UW CSE affiliate faculty member

taught this class last time around Winter 2013



#### **Chapel's Origins: HPCS**



#### **DARPA HPCS: High Productivity Computing Systems**

- Goal: improve productivity by a factor of 10x
- Timeframe: Summer 2002 Fall 2012
- Cray developed a new system architecture, network, software stack...
  - this became the very successful Cray XC30™ Supercomputer Series



...and a new programming language: Chapel

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#### What is Chapel?



- An emerging parallel programming language
  - Design and development led by Cray Inc.

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- in collaboration with academia, labs, industry; domestically & internationally
- A work-in-progress
- Goal: Improve productivity of parallel programming



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#### **Recent Graduates:**

"something similar to what I used in school: Python, Matlab, Java, ..."





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#### **Seasoned HPC Programmers:**

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"that sugary stuff that I don't need because I was born to suffer"



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#### **Computational Scientists:**

"something that lets me express my parallel computations without having to wrestle with architecture-specific details"





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#### **Computational Scientists:**

"something that lets me express my parallel computations without having to wrestle with architecture-specific details"

#### **Chapel Team:**

"something that lets computational scientists express what they want, without taking away the control that HPC programmers want, implemented in a language as attractive as recent graduates want."



#### **Chapel's Implementation**



- Being developed as open source at GitHub
  - Uses Apache v2.0 license
- Portable design and implementation, targeting:
  - multicore desktops and laptops
  - commodity clusters and the cloud
  - HPC systems from Cray and other vendors
  - in-progress: manycore processors, CPU+accelerator hybrids, ...



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#### **Game Plan for Tonight**



- Rough outline:
  - a bit of context: PGAS programming languages
  - lots of Chapel

- Please feel free to ask questions as we go
  - I can throttle as necessary

- optionally: "Happy Office Hour" afterwards
  - or: go catch Lightning Bolt at Neumo's





#### **Terminology Check**

+

**Introduction to PGAS\* Programming** 

(\* Partitioned Global Address Space)

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#### **Shared vs. Distributed Memory**



Shared Memory Architectures:

Distributed Memory Architectures:

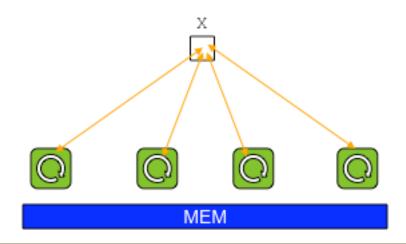


## Global Address Space Programming Models (Shared Memory)



#### e.g., OpenMP, Pthreads

- + support dynamic, fine-grain parallelism
- + considered simpler, more like traditional programming
  - "if you want to access something, simply name it"
- no support for expressing locality/affinity; limits scalability
- bugs can be subtle, difficult to track down (race conditions)
- tend to require complex memory consistency models





## **SPMD Programming/Execution Models**



SPMD =



#### **SPMD Programming/Execution Models**



#### **SPMD** = Single Program, Multiple Data

- the dominant model for distributed memory programming
- Concept:
  - write one copy of a program
  - execute multiple copies of it simultaneously
    - various terms: images, processes, PEs (Processing Elements), ranks, ...
    - one per compute node? one per core?
  - in a pure SPMD model, this is the only source of parallelism
    - i.e., run p copies of my program in parallel
    - our parallel tasks are essentially the program images
  - in practice, each program can also contain parallelism
    - typically achieved by mixing two notations (e.g., MPI + OpenMP)



#### **How Do SPMD Program Images Interact?**



- Message Passing (the most common HPC paradigm):
  - "messages": essentially buffers of data
  - primitive message passing operations: send/receive
  - primary example: MPI

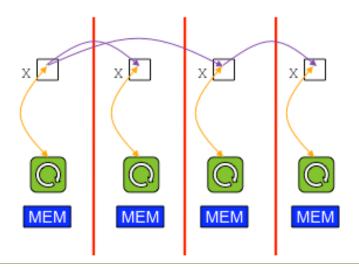


## Message Passing Programming Models (Distributed Memory)



#### e.g., MPI

- + a more constrained model; can only access local data
- + runs on most large-scale parallel platforms
  - and for many of them, can achieve near-optimal performance
- + is *relatively* easy to implement
- + can serve as a strong foundation for higher-level models
- + users have been able to get real work done with it



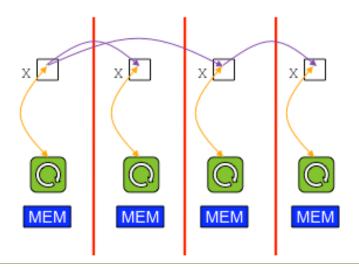


## Message Passing Programming Models (Distributed Memory)



#### e.g., MPI

- communication must be used to get copies of remote data
  - tends to reveal too much about how to transfer data, not simply what
- only supports "cooperating executable"-level parallelism
- couples data transfer and synchronization
- has frustrating classes of bugs of its own
  - e.g., mismatches between sends/recvs, buffer overflows, etc.





#### **How Do SPMD Program Images Interact?**



#### Message Passing (the most common HPC paradigm):

- "messages": essentially buffers of data
- primitive message passing operations: send/receive
- primary example: MPI

#### Other alternatives:

- Single-Sided Communication
- Partitioned Global Address Spaces
- Active Messages
- . . .



## Partitioned Global Address Space (PGAS) Languages



#### (Or perhaps: partitioned global namespace languages)

- abstract concept:
  - support a shared namespace on distributed memory
    - permit parallel tasks to access remote variables by naming them

	partitioned sh	nared name-/a	ddress space	
private	private	private	private	private
space 0	space 1	space 2	space 3	space 4



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# Partitioned Global Address Space (PGAS) Languages



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- abstract concept:
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  - establish a strong sense of ownership
    - every variable has a well-defined location
    - local variables are cheaper to access than remote ones

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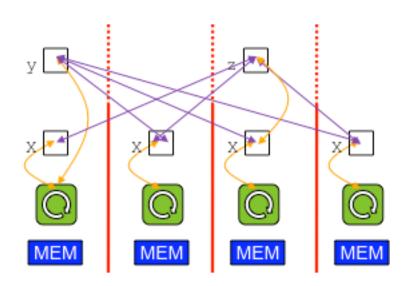
- abstract concept:
  - support a shared namespace on distributed memory
    - permit parallel tasks to access remote variables by naming them
  - establish a strong sense of ownership
    - every variable has a well-defined location
    - local variables are cheaper to access than remote ones
- traditional PGAS languages have been SPMD in nature
  - best-known examples: Co-Array Fortran, UPC

	partitioned sh	nared name-/a	ddress space	
private	private	private	private	private
space 0	space 1	space 2	space 3	space 4

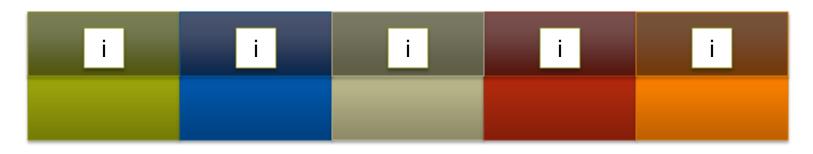


## **Traditional PGAS Languages**

## e.g., Co-Array Fortran, UPC







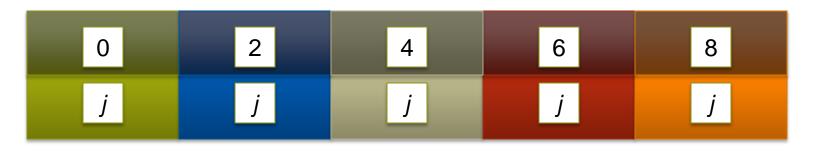






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```
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```





```
proc main() {
 var i(*): int;  // declare a shared variable i
  i = 2*this image(); // each image initializes its
                 // declare a private variable j
  var j: int;
  j = i( (this image()+1) % num images() );
    // ^^ access our neighbor's copy of i
    // communication implemented by compiler + runtime
    // How did we know our neighbor had an i?
    // Because it's SPMD - we're all running the same
    // program. (Simple, but restrictive)
                         4
                         6
```



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### **Traditional PGAS Languages**



#### founding PGAS members: Co-Array Fortran, UPC, Titanium

- extensions to Fortran, C, and Java, respectively
- details vary, but potential for:
  - arrays that are decomposed across compute nodes
  - pointers that refer to remote objects
- note that earlier languages could also be considered PGAS, but the term hadn't been coined yet



#### **UPC: Unified Parallel C**



#### **UPC:** A "traditional" PGAS language

- developed ~1999
- "unified" in the sense that it combined 3 distinct parallel C's:
  - AC, Split-C, Parallel C Preprocessor
- though a sibling to CAF, philosophically quite different

#### **Motivating Philosophy:**

- extend C concepts logically to support SPMD execution
  - 1D arrays
  - for loops
  - pointers (and pointer/array equivalence)



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#### **UPC** is also SPMD



- SPMD programming/execution model
  - program copies are referred to as 'threads'
- Built-in constants provide the basics:

```
int p, me;
p = THREADS; // returns number of processes
me = MYTHREAD; // returns a value in 0..THREADS-1
```

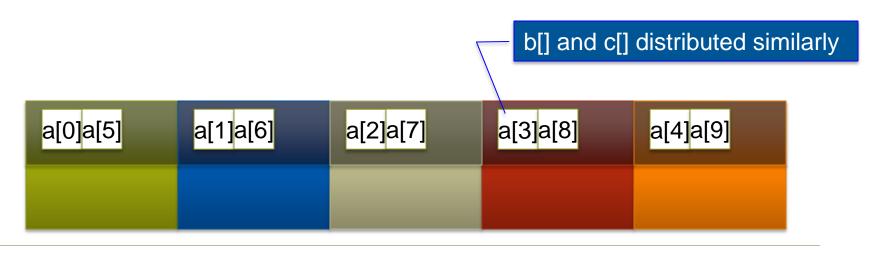
Barrier synch statement:

```
upc barrier; // wait for all processes/threads
```





- Arrays declared with the 'shared' keyword are distributed within the shared space
  - uses a cyclic distribution by default #define N 10
     shared float a[N], b[N], c[N];

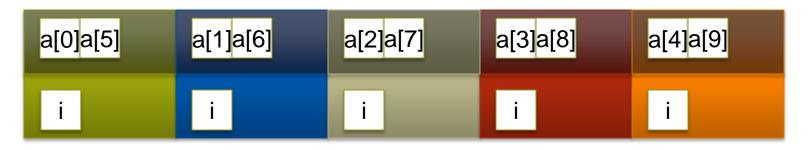






- Arrays declared with the 'shared' keyword are distributed within the shared space
  - uses a cyclic distribution by default
     #define N 10

```
shared float a[N], b[N], c[N];
int i=0; // no "shared" keyword => stored privately
```







- Arrays declared with the 'shared' keyword are distributed within the shared space
  - uses a cyclic distribution by default

```
#define N 10
shared float a[N], b[N], c[N];
for (int i=0; i<N; i++) { // dumb loop: O(N)
   if (i%THREADS == MYTHREAD) {
     c[i] = a[i] + alpha * b[i];
   }
}</pre>
```

a[0] <mark>a[5]</mark>	a[1] <mark>a[6]</mark>	a[2] <mark>a[7]</mark>	a[3] <mark>a[8]</mark>	a[4] <mark>a[9]</mark>
i	i	i	i	i



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- Arrays declared with the 'shared' keyword are distributed within the shared space
  - uses a cyclic distribution by default

```
#define N 10
shared float a[N], b[N], c[N];
// smarter loop: O(N/THREADS)
for (int i=MYTHREAD; i<N; i+=THREADS) {
  c[i] = a[i] + alpha * b[i];
}</pre>
```

a[0] <mark>a[5]</mark>	a[1] <mark>a[6]</mark>	a[2] <mark>a[7]</mark>	a[3] <mark>a[8]</mark>	a[4] <mark>a[9]</mark>
i	i	i	i	i



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- Arrays declared with the 'shared' keyword are distributed within the shared space
  - uses a cyclic distribution by default

```
#define N 10
shared float a[N], b[N], c[N];
upc_forall (int i=0; i<N; i++; i) {
  c[i] = a[i] + alpha * b[i];
}</pre>
```

Affinity field: Which thread should execute this iteration? (if int, %THREADS to get ID)

a[0] <mark>a[5]</mark>	a[1] <mark>a[6]</mark>	a[2] <mark>a[7]</mark>	a[3] <mark>a[8]</mark>	a[4] <mark>a[9]</mark>
i	i	i	i	i





- Arrays declared with the 'shared' keyword are distributed within the shared space
  - can specify a block-cyclic distribution as well

```
#define N 10
shared [2] float a[N], b[N], c[N];
upc_forall (int i=0; i<N; i++; &c[i]) {
   c[i] = a[i] + alpha * b[i];
}</pre>
Affinity
```

Affinity field: Which thread should execute this iteration? (if ptr-to-shared, owner does)

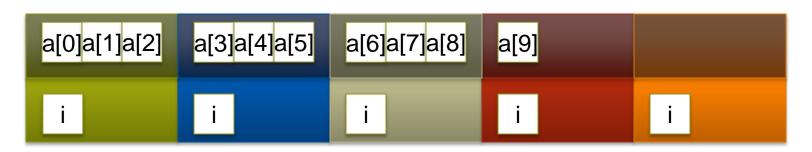
a[0] <mark>a[1]</mark>	a[2] <mark>a[3]</mark>	a[4] <mark>a[5]</mark>	a[6] <mark>a[7]</mark>	a[8] <mark>a[9]</mark>
i	i	i	i	i





- Arrays declared with the 'shared' keyword are distributed within the shared space
  - can specify a block-cyclic distribution as well

```
#define N 10
shared [3] float a[N], b[N], c[N];
upc_forall (int i=0; i<N; i++; &c[i]) {
  c[i] = a[i] + alpha * b[i];
}</pre>
```



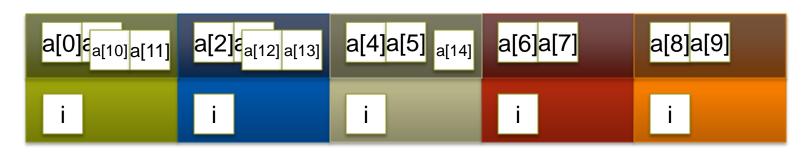


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- Arrays declared with the 'shared' keyword are distributed within the shared space
  - can specify a block-cyclic distribution as well

```
#define N 15
shared [2] float a[N], b[N], c[N];
upc_forall (int i=0; i<N; i++; &c[i]) {
  c[i] = a[i] + alpha * b[i];
}</pre>
```





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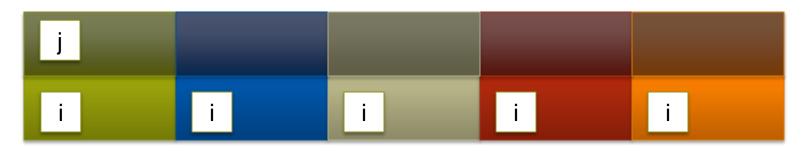
#### **Scalars in UPC**



 Somewhat confusingly (to me anyway\*), shared scalars in UPC result in a single copy on thread 0

```
int i;
shared int j;
```

\* = because it seems contrary to SPMD programming



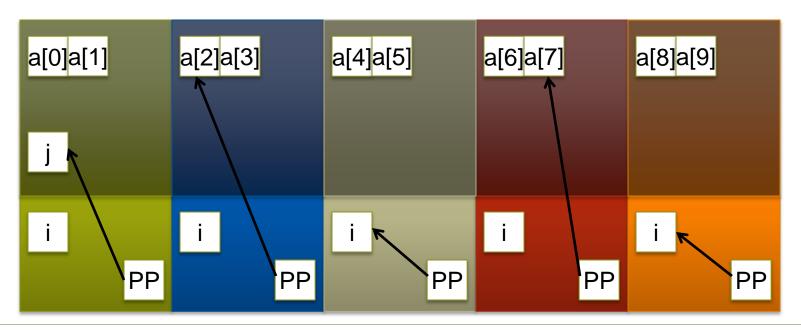


#### **Pointers in UPC**



 UPC Pointers may be private/shared and may point to private/shared

int\* PP; // private pointer to local data



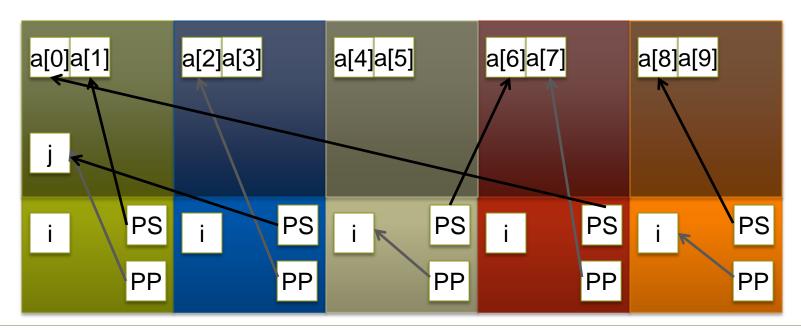


#### **Pointers in UPC**



 UPC Pointers may be private/shared and may point to private/shared

int\* PP; // private pointer to local data
shared int\* PS; // private pointer to shared data



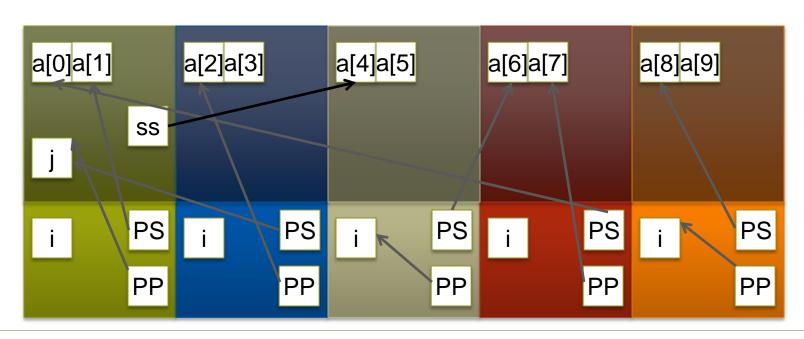


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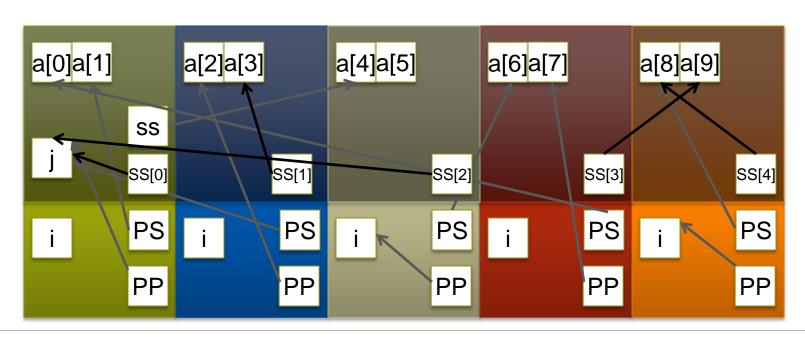
```
int* PP; // private pointer to local data
shared int* PS; // private pointer to shared data
shared int* shared ss; // shared pointer to shared data
```





### **Arrays of Pointers in UPC**

- CRAY
- Of course, one can also create arrays of pointers
   // array of shared pointer to shared data
   shared int\* shared SS[THREADS];
- As you can imagine, one UPC's strengths is its ability to create fairly arbitrary distributed data structures



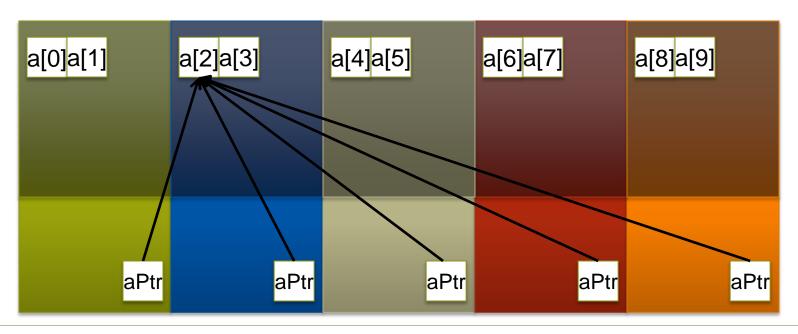


### **Array/Pointer Equivalence in UPC**



As in C, pointers can be walked through memory

```
shared [2] float a[N];
shared float* aPtr[THREADS] = &(a[2]);
```



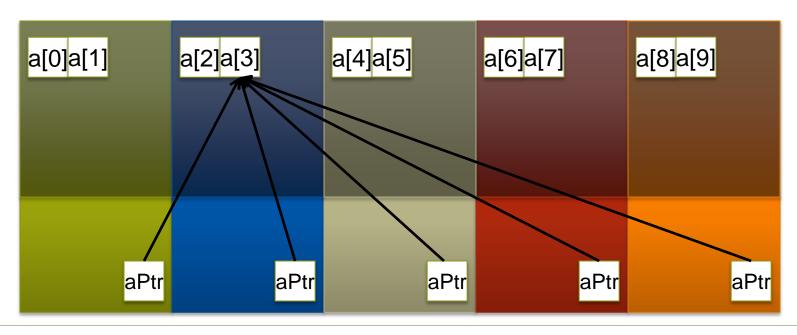


### **Array/Pointer Equivalence in UPC**



As in C, pointers can be walked through memory

```
shared [2] float a[N];
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aPtr++;
```



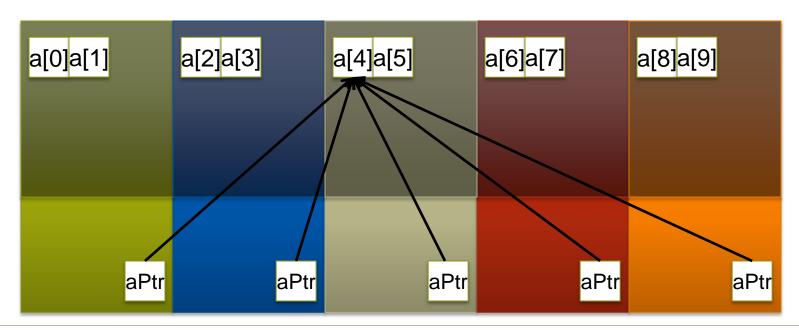


#### **Array/Pointer Equivalence in UPC**



As in C, pointers can be walked through memory

```
shared [2] float a[N];
shared float* aPtr[THREADS] = &(a[2]);
aPtr++;
aPtr++;
```





#### **How are UPC Pointers Implemented?**



# Local pointers to local: just an address, as always Pointers to shared: 3 parts

- thread ID
- base address of block within the thread
- phase/offset within the block (0..blocksize-1)
- UPC supports a number of utility functions that permit you to query this information from pointers
- Casting between pointer types is permitted
  - but can be dangerous (as in C) and/or lossy



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#### Other Features in UPC



- Collectives Library
- Memory Consistency Model
  - among the first/foremost memory models in HPC
  - ability to move between strict and relaxed models
  - fence operations
- Dynamic Memory Management
- Locks
- Parallel I/O
- ...



### **UPC Summary**

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- Program in SPMD style
- Communicate via shared arrays/pointers
  - cyclic and block-cyclic arrays
  - pointers to shared and private data
  - array-pointer equivalence
- Other stuff too, but this gives you the main idea
- For more information, see <a href="https://upc-lang.org/upc/">https://upc-lang.org/upc/</a>



#### Other Notable SPMD PGAS Languages



#### **Founding Fathers:**

- Co-Array Fortran (CAF): A Fortran-based PGAS language
  - Remote accesses are much more explicit than in UPC
  - Distributed arrays are better (multidimensional, like Fortran's)
    - ...but also worse (must declare in terms of per-image chunks)
  - Adopted into the Fortran 2008 standard
- Titanium: A Java-based PGAS language
  - my favorite of the three

#### **New Kids on the Block:**

- UPC++
- Co-Array C++

C++ PGAS languages designed using template meta-programming (so, no compiler required)

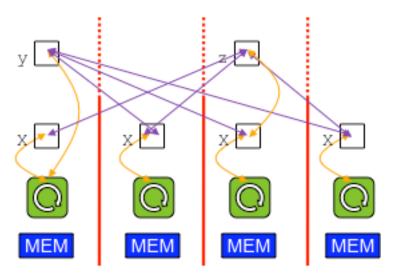


### **Traditional PGAS Languages**



### e.g., Co-Array Fortran, UPC

- + support a shared namespace, like shared-memory
- + support a strong sense of ownership and locality
  - each variable is stored in a particular memory segment
  - tasks can access any visible variable, local or remote
  - local variables are cheaper to access than remote ones
- + implicit communication eases user burden; permits compiler to use best mechanisms available



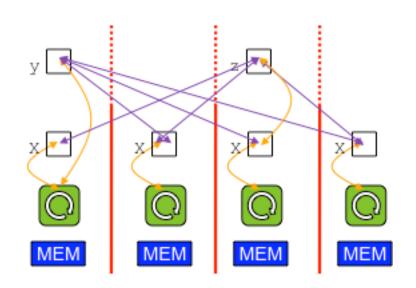


### **Traditional PGAS Languages**



### e.g., Co-Array Fortran, UPC

- restricted to SPMD programming and execution models
- data structures not as flexible/rich as one might like
- retain many of the downsides of shared-memory
  - error cases, memory consistency models



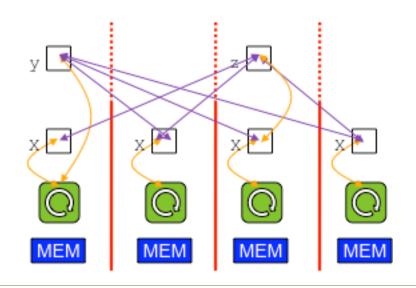


### **Next-Generation PGAS Languages**



# e.g., Chapel (also Charm++, X10, Fortress, ...)

- + breaks out of SPMD mold via global multithreading
- + richer set of distributed data structures
- retains many of the downsides of shared-memory
  - error cases, memory consistency models







## **Chapel Motivation**

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### **Sustained Performance Milestones**



#### 1 GF - 1988: Cray Y-MP; 8 Processors

Static finite element analysis





#### 1 TF - 1998: Cray T3E; 1,024 Processors

Modeling of metallic magnet atoms





#### 1 PF - 2008: Cray XT5; 150,000 Processors

Superconductive materials





### 1 EF - ~2018: Cray \_\_\_\_; ~10,000,000

**Processors** 

• TBD



### **Sustained Performance Milestones**



#### 1 GF - 1988: Cray Y-MP; 8 Processors

- Static finite element analysis
- Fortran77 + Cray autotasking + vectorization





#### 1 TF - 1998: Cray T3E; 1,024 Processors

- Modeling of metallic magnet atoms
- Fortran + MPI (Message Passing Interface)





#### 1 PF - 2008: Cray XT5; 150,000 Processors

- Superconductive materials
- C++/Fortran + MPI + vectorization





# 1 EF - ~2018: Cray \_\_\_\_; ~10,000,000 Processors

- TBD
- TBD: C/C++/Fortran + MPI + OpenMP/OpenACC/CUDA/OpenCL?

Or, perhaps something completely different?

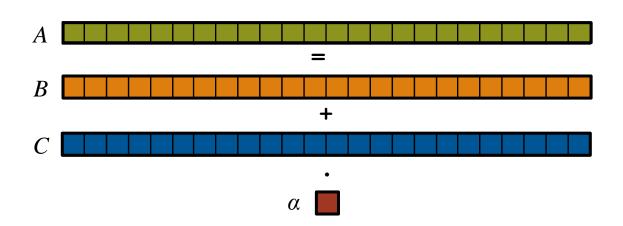


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**Given:** *m*-element vectors *A*, *B*, *C* 

**Compute:**  $\forall i \in 1..m, A_i = B_i + \alpha \cdot C_i$ 

#### In pictures:



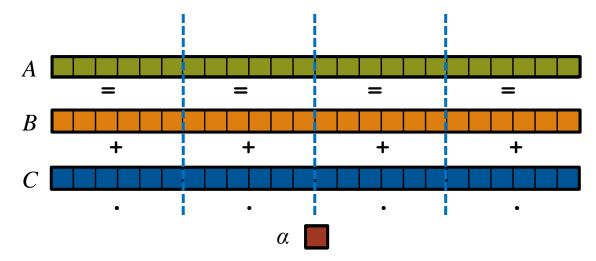


CRAY

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### In pictures, in parallel:

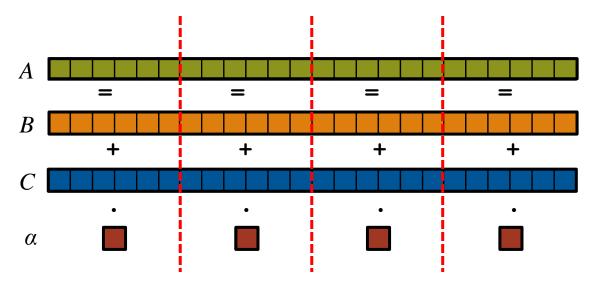




**Given:** *m*-element vectors *A*, *B*, *C* 

**Compute:**  $\forall i \in 1..m, A_i = B_i + \alpha \cdot C_i$ 

### In pictures, in parallel (distributed memory):



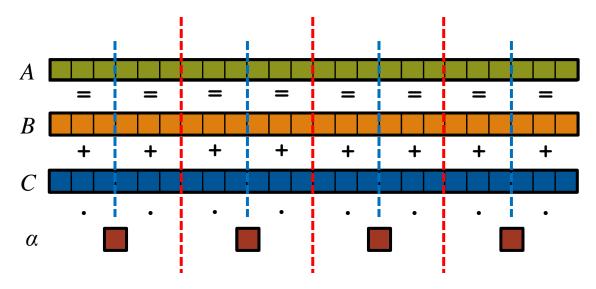


CRAY

**Given:** *m*-element vectors *A*, *B*, *C* 

**Compute:**  $\forall i \in 1..m, A_i = B_i + \alpha \cdot C_i$ 

In pictures, in parallel (distributed memory multicore):





#### STREAM Triad: MPI



```
#include <hpcc.h>
static int VectorSize;
static double *a, *b, *c;
```

```
int HPCC StarStream(HPCC Params *params) {
  int myRank, commSize;
  int rv, errCount;
 MPI Comm comm = MPI COMM WORLD;
 MPI Comm size ( comm, &commSize );
  MPI Comm rank( comm, &myRank );
  rv = HPCC Stream( params, 0 == myRank);
 MPI Reduce ( &rv, &errCount, 1, MPI INT, MPI SUM,
   0, comm);
  return errCount;
```

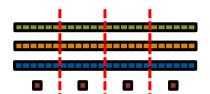
int HPCC Stream(HPCC Params \*params, int doIO) {

VectorSize = HPCC LocalVectorSize( params, 3,

a = HPCC XMALLOC( double, VectorSize );

b = HPCC XMALLOC( double, VectorSize );

c = HPCC XMALLOC( double, VectorSize );



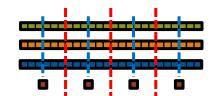
```
if (!a || !b || !c) {
  if (c) HPCC free(c);
  if (b) HPCC free(b);
  if (a) HPCC free(a);
  if (doIO) {
    fprintf( outFile, "Failed to allocate memory
  (%d).\n", VectorSize);
    fclose( outFile );
  return 1;
for (j=0; j<VectorSize; j++) {</pre>
 b[j] = 2.0;
  c[j] = 0.0;
scalar = 3.0;
for (j=0; j<VectorSize; j++)</pre>
  a[j] = b[j]+scalar*c[j];
HPCC free(c);
HPCC free(b);
HPCC free(a);
```



register int j; double scalar;

sizeof(double), 0);

#### STREAM Triad: MPI+OpenMP





#### MPI + OpenMP

```
#include <hpcc.h>
                                                       if (!a || !b || !c) {
#ifdef OPENMP
                                                         if (c) HPCC free(c);
#include <omp.h>
                                                         if (b) HPCC free(b);
#endif
                                                         if (a) HPCC free(a);
static int VectorSize;
                                                         if (doIO) {
                                                           fprintf( outFile, "Failed to allocate memory
static double *a, *b, *c;
                                                         (%d).\n", VectorSize);
int HPCC StarStream(HPCC Params *params) {
                                                           fclose( outFile );
  int myRank, commSize;
  int rv, errCount;
                                                         return 1;
 MPI Comm comm = MPI COMM WORLD;
 MPI Comm size( comm, &commSize );
                                                     #ifdef OPENMP
  MPI Comm rank( comm, &myRank );
                                                     #pragma omp parallel for
                                                     #endif
  rv = HPCC Stream( params, 0 == myRank);
                                                       for (j=0; j<VectorSize; j++) {</pre>
 MPI Reduce ( &rv, &errCount, 1, MPI INT, MPI SUM,
                                                         b[j] = 2.0;
   0, comm);
                                                         c[j] = 0.0;
  return errCount;
                                                        scalar = 3.0;
int HPCC Stream(HPCC Params *params, int doIO) {
                                                     #ifdef OPENMP
  register int j;
                                                     #pragma omp parallel for
  double scalar;
                                                      #endif
                                                       for (j=0; j<VectorSize; j++)</pre>
 VectorSize = HPCC LocalVectorSize( params, 3,
                                                         a[j] = b[j]+scalar*c[j];
   sizeof(double), 0);
                                                       HPCC free(c);
  a = HPCC XMALLOC( double, VectorSize );
                                                       HPCC free(b);
 b = HPCC XMALLOC( double, VectorSize );
                                                       HPCC free(a);
  c = HPCC XMALLOC( double, VectorSize );
```



### STREAM Triad: MPI+OpenMP vs. CUDA

#### MPI + OpenMP

```
#ifdef _OPENMP
#include <omp.h>
#endif

static int VectorSize;
static double *a, *b, *c;
int HPCC_StarStream(HPCC_Params *params) {
   int myRank, commSize;
   int rv, errCount;
   MPI_Comm comm = MPI_COMM_WORLD;

MPI_Comm_size( comm, &commSize );
   MPI_Comm_rank( comm, &myRank );
   rv = HPCC_Stream( params, 0 == myRank);
   MPI_Reduce( &rv, &errCount, 1, MPI_INT, MPI_SUM, 0, comm );
   return errCount;
```

#### CUDA

```
#define N 2000000
int main() {
  float *d_a, *d_b, *d_c;
  float scalar;

  cudaMalloc((void**)&d_a, sizeof(float)*N);
  cudaMalloc((void**)&d_b, sizeof(float)*N);
  cudaMalloc((void**)&d_c, sizeof(float)*N);
```

#### HPC suffers from too many distinct notations for expressing parallelism and locality

dim3 dimBlock(128)

```
VectorSize = HPCC LocalVectorSize( params, 3, sizeof(double), 0 );
 a = HPCC XMALLOC( double, VectorSize );
 b = HPCC XMALLOC( double, VectorSize );
 c = HPCC XMALLOC( double, VectorSize );
 if (!a || !b || !c) {
   if (c) HPCC free(c);
   if (b) HPCC free(b);
   if (a) HPCC free(a);
   if (doIO) {
     fprintf( outFile, "Failed to allocate memory (%d).\n", VectorSize );
     fclose( outFile );
   return 1:
#ifdef OPENMP
#pragma omp parallel for
#endif
 for (j=0; j<VectorSize; j++) {
   b[j] = 2.0;
   c[j] = 0.0;
 scalar = 3.0;
#ifdef OPENMP
#pragma omp parallel for
#endif
 for (j=0; j<VectorSize; j++)</pre>
   a[i] = b[i]+scalar*c[i];
 HPCC free(c);
 HPCC free(b);
 HPCC free (a);
 return 0:
```



### Why so many programming models?



#### HPC has traditionally given users...

- ...low-level, control-centric programming models
- ...ones that are closely tied to the underlying hardware
- ...ones that support only a single type of parallelism

Type of HW Parallelism	Programming Model	Unit of Parallelism
Inter-node	MPI	executable
Intra-node/multicore	OpenMP/pthreads	iteration/task
Instruction-level vectors/threads	pragmas	iteration
GPU/accelerator	CUDA/OpenCL/OpenACC	SIMD function/task

benefits: lots of control; decent generality; easy to implement downsides: lots of user-managed detail; brittle to changes



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# By Analogy: Let's Cross the United States!





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### Rewinding a few slides...

#### MPI + OpenMP

```
#ifdef OPENMP
#include <omp.h>
#endif

static int VectorSize;
static double *a, *b, *c;
int HPCC StarStream(HPCC Params *params) {
   int myRank, commSize;
   int rv, errCount;
   MPI_Comm comm = MPI_COMM_WORLD;

MPI_Comm_size( comm, &commSize );
   MPI_Comm_rank( comm, &commSize );
   rv = HPCC_Stream( params, 0 == myRank);
   MPI_Reduce( &rv, &errCount, 1, MPI_INT, MPI_SUM, 0, comm );
   return errCount;
```

```
CUDA
```

```
#define N 2000000
int main() {
  float *d_a, *d_b, *d_c;
  float scalar;

cudaMalloc((void**)&d_a, sizeof(float)*N);
  cudaMalloc((void**)&d_b, sizeof(float)*N);
  cudaMalloc((void**)&d_c, sizeof(float)*N);

dim3_dimBlock(128);
```

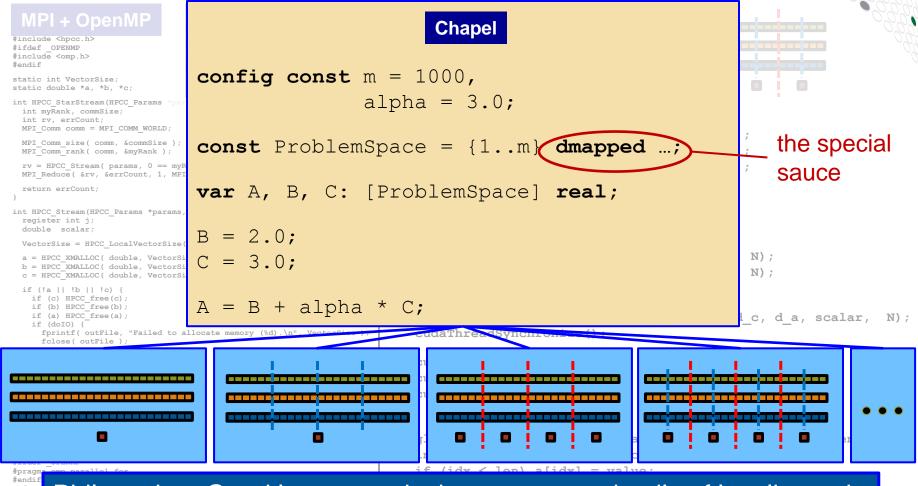
HPC suffers from too many distinct notations for expressing parallelism and locality

```
VectorSize = HPCC LocalVectorSize( params, 3, sizeof(double), 0 );
 a = HPCC XMALLOC( double, VectorSize );
 b = HPCC XMALLOC( double, VectorSize );
 c = HPCC XMALLOC( double, VectorSize );
 if (!a || !b || !c) {
   if (c) HPCC free(c);
   if (b) HPCC free(b);
   if (a) HPCC free(a);
   if (doIO) {
     fprintf( outFile, "Failed to allocate memory (%d).\n", VectorSize );
     fclose( outFile );
   return 1:
#ifdef OPENMP
#pragma omp parallel for
#endif
 for (j=0; j<VectorSize; j++) {
   b[j] = 2.0;
   c[j] = 0.0;
 scalar = 3.0;
#ifdef OPENMP
#pragma omp parallel for
#endif
 for (j=0; j<VectorSize; j++)</pre>
   a[i] = b[i]+scalar*c[i];
 HPCC free(c);
 HPCC free(b);
 HPCC free (a);
 return 0:
```



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### **STREAM Triad: Chapel**



<u>Philosophy:</u> Good language design can tease details of locality and parallelism away from an algorithm, permitting the compiler, runtime, applied scientist, and HPC expert to each focus on their strengths.



### **Outline**

CRAY

- ✓ Motivation
- Chapel Background and Themes
- Survey of Chapel Concepts
- Project Status and Next Steps



# **Motivating Chapel Themes**

- 1) General Parallel Programming
- 2) Global-View Abstractions
- 3) Multiresolution Design
- 4) Control over Locality/Affinity
- **5)** Reduce HPC ← Mainstream Language Gap



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# 1) General Parallel Programming



### With a unified set of concepts...

### ...express any parallelism desired in a user's program

- Styles: data-parallel, task-parallel, concurrency, nested, ...
- Levels: model, function, loop, statement, expression

### ...target any parallelism available in the hardware

• Types: machines, nodes, cores, instructions

Type of HW Parallelism	Programming Model	Unit of Parallelism
Inter-node	MPI	executable
Intra-node/multicore	OpenMP/pthreads	iteration/task
Instruction-level vectors/threads	pragmas	iteration
GPU/accelerator	CUDA/OpenCL/OpenACC	SIMD function/task



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# 1) General Parallel Programming



### With a unified set of concepts...

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- Styles: data-parallel, task-parallel, concurrency, nested, ...
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### ...target any parallelism available in the hardware

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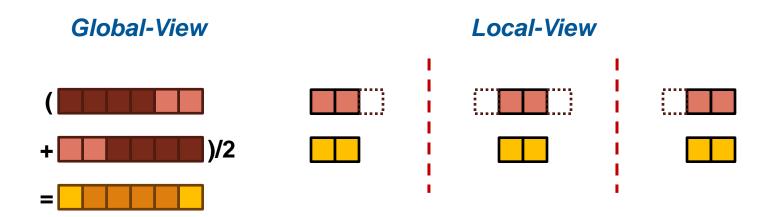
Type of HW Parallelism	Programming Model	Unit of Parallelism
Inter-node	Chapel	executable/task
Intra-node/multicore	Chapel	iteration/task
Instruction-level vectors/threads	Chapel	iteration
GPU/accelerator	Chapel	SIMD function/task



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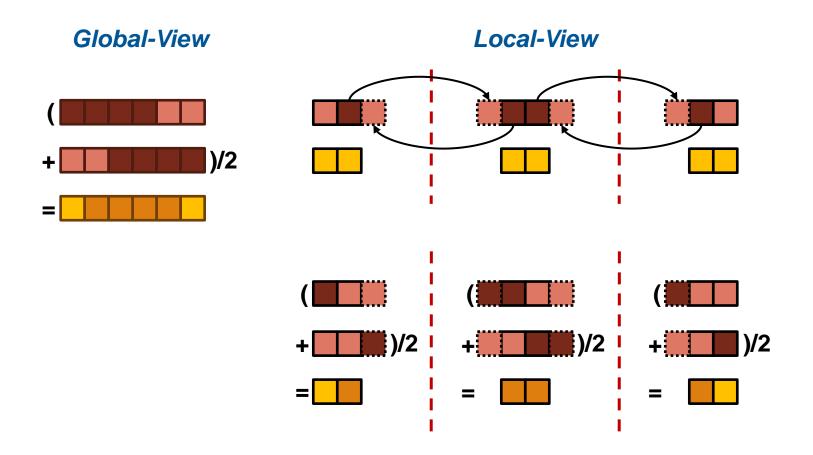
In pictures: "Apply a 3-Point Stencil to a vector"







### In pictures: "Apply a 3-Point Stencil to a vector"





### In code: "Apply a 3-Point Stencil to a vector"

#### Global-View

```
proc main() {
    var n = 1000;
    var A, B: [1..n] real;

    forall i in 2..n-1 do
        B[i] = (A[i-1] + A[i+1])/2;
}
```

#### Local-View (SPMD)

```
proc main() {
  var n = 1000;
  var p = numProcs(),
      me = myProc(),
      myN = n/p
  var A, B: [0..myN+1] real;
  if (me < p-1) {
    send(me+1, A[myN]);
    recv (me+1, A[myN+1]);
  if (me > 0) {
    send (me-1, A[1]);
    recv (me-1, A[0]);
  forall i in 1..myN do
    B[i] = (A[i-1] + A[i+1])/2;
```

Bug: Refers to uninitialized values at ends of A



MPUTE

### In code: "Apply a 3-Point Stencil to a vector"

#### Global-View

```
proc main() {
   var n = 1000;
   var A, B: [1..n] real;

  forall i in 2..n-1 do
   B[i] = (A[i-1] + A[i+1])/2;
}
```

Communication becomes geometrically more complex for higher-dimensional arrays

#### Local-View (SPMD)

```
proc main()
                   Assumes p divides n
  var n = 1000;
  \mathbf{yar} p = \mathbf{numProcs}(),
      me = myProc(),
      myN = n/p,
      mvLo = 1,
      myHi = myN;
  var A, B: [0..myN+1] real;
  if (me < p-1) {
    send(me+1, A[myN]);
    recv (me+1, A[myN+1]);
  } else
  / myHi = myN-1;
  if (me > 0) {
    send (me-1, A[1]);
    recv (me-1, A[0]);
  } else
    myLo = 2;
  forall i in myLo..myHi do
    B[i] = (A[i-1] + A[i+1])/2;
```



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### 2) Global-View Programming: A Final Note



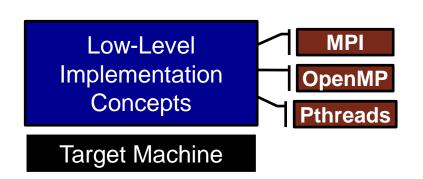
 A language may support both global- and local-view programming — in particular, Chapel does

```
proc main() {
   coforall loc in Locales do
      on loc do
        MySPMDProgram(loc.id, Locales.numElements);
}
proc MySPMDProgram(myImageID, numImages) {
   ...
}
```



# 3) Multiresolution Design: Motivation





HPF High-Level Abstractions

Target Machine

"Why is everything so tedious/difficult?"

"Why don't my programs port trivially?"

"Why don't I have more control?"



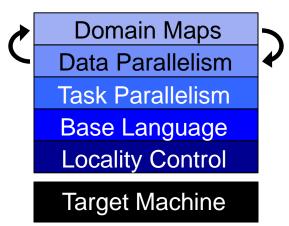
# 3) Multiresolution Design



### Multiresolution Design: Support multiple tiers of features

- higher levels for programmability, productivity
- lower levels for greater degrees of control

Chapel language concepts



- build the higher-level concepts in terms of the lower
- permit the user to intermix layers arbitrarily

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# 4) Control over Locality/Affinity



#### **Consider:**

- Scalable architectures package memory near processors
- Remote accesses take longer than local accesses

#### Therefore:

- Placement of data relative to tasks affects scalability
- Give programmers control of data and task placement

#### Note:

 Over time, we expect locality to matter more and more within the compute node as well



### **Chapel and PGAS**



- Chapel is PGAS, but unlike most, it's not restricted to SPMD
  - ⇒ never think about "the other copies of the program"
  - ⇒ "global name/address space" comes from lexical scoping
    - as in traditional languages, each declaration yields one variable
    - variables are stored on the locale where the task declaring it is executing



Locales (think: "compute nodes")



# 5) Reduce HPC ↔ Mainstream Language Gap



#### **Consider:**

- Students graduate with training in Java, Matlab, Python, etc.
- Yet HPC programming is dominated by Fortran, C/C++, MPI

### We'd like to narrow this gulf with Chapel:

- to leverage advances in modern language design
- to better utilize the skills of the entry-level workforce...
- ...while not alienating the traditional HPC programmer
  - e.g., support object-oriented programming, but make it optional

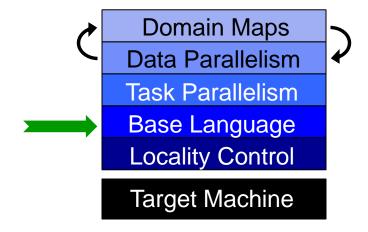


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### **Outline**

CRAY

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Project Status and Next Steps



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# **Static Type Inference**



```
const pi = 3.14,
           // pi is a real
    coord = 1.2 + 3.4i, // coord is a complex...
    coord2 = pi*coord, // ...as is coord2
    verbose = false;  // verbose is boolean
return x + y; // and an inferred return type
                 // sum is a real
var sum = addem(1, pi),
  fullname = addem(name, "ford"); // fullname is a string
writeln((sum, fullname));
```

#### (4.14, bradford)



### Range Types and Algebra

```
const r = 1..10;
printVals(r);
printVals(r # 3);
printVals(r by 2);
printVals(r by -2);
printVals(r by 2 # 3);
printVals(r # 3 by 2);
printVals(0.. #n);
proc printVals(r) {
  for i in r do
    write(r, " ");
  writeln();
```

```
1 2 3 4 5 6 7 8 9 10
1 2 3
1 3 5 7 9
10 8 6 4 2
1 3 5
1 3
0 1 2 3 4 ... n-1
```



#### **Iterators**

```
iter fibonacci(n) {
  var current = 0,
    next = 1;
  for 1..n {
    yield current;
    current += next;
    current <=> next;
}
```

```
for f in fibonacci(7) do
  writeln(f);

0
1
2
3
5
8
```

```
for ij in tiledRMO({1..m, 1..n}, 2) do
  write(ij);
(1,1) (1,2) (2,1) (2,2)
```

```
(1,1) (1,2) (2,1) (2,2)
(1,3) (1,4) (2,3) (2,4)
(1,5) (1,6) (2,5) (2,6)
...
(3,1) (3,2) (4,1) (4,2)
```



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# **Zippered Iteration**



```
for (i,f) in zip(0..#n, fibonacci(n)) do
  writeln("fib #", i, " is ", f);
```

```
fib #0 is 0
fib #1 is 1
fib #2 is 1
fib #3 is 2
fib #4 is 3
fib #5 is 5
fib #6 is 8
...
```



### Other Base Language Features

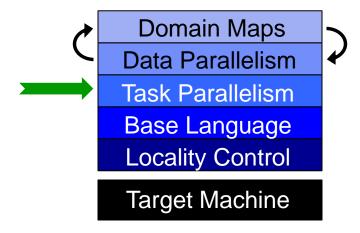


- tuple types and values
- rank-independent programming features
- interoperability features
- compile-time features for meta-programming
  - e.g., compile-time functions to compute types, parameters
- OOP (value- and reference-based)
- argument intents, default values, match-by-name
- overloading, where clauses
- modules (for namespace management)
- •



### **Outline**

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Project Status and Next Steps



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# **Defining our Terms**

Task:





# **Defining our Terms**



**Task:** a unit of computation that can/should execute in parallel with other tasks

**Task Parallelism:** 

(in contrast with):

**Data Parallelism:** 



# **Defining our Terms**



**Task:** a unit of computation that can/should execute in parallel with other tasks

**Task Parallelism:** a style of parallel programming in which parallelism is driven by programmer-specified tasks

(in contrast with):

**Data Parallelism:** a style of parallel programming in which parallelism is driven by computations over collections of data elements or their indices



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# Task Parallelism: Begin Statements



```
// create a fire-and-forget task for a statement
begin writeln("hello world");
writeln("goodbye");
```

### **Possible outputs:**

hello world goodbye goodbye
hello world



 $\begin{pmatrix} 10 \\ 0 \end{pmatrix}$ 

# **Task Parallelism: Cobegin Statements**



```
// create a task per child statement
cobegin {
  producer(1);
  producer(2);
  consumer(1);
} // implicit join of the three tasks here
```



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### Task Parallelism: Coforall Loops



```
// create a task per iteration
coforall t in 0..#numTasks {
  writeln("Hello from task ", t, " of ", numTasks);
} // implicit join of the numTasks tasks here
writeln("All tasks done");
```

### Sample output:

```
Hello from task 2 of 4
Hello from task 0 of 4
Hello from task 3 of 4
Hello from task 1 of 4
All tasks done
```



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# Task Parallelism: Data-Driven Synchronization

- CRAY
- 1) atomic variables: support atomic operations (as in C++)
  - e.g., compare-and-swap; atomic sum, mult, etc.

2) single-assignment variables: reads block until assigned

- 3) synchronization variables: store full/empty state
  - by default, reads/writes block until the state is full/empty



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### **Bounded Buffer Producer/Consumer Example**

```
begin producer();
consumer();
// 'sync' types store full/empty state along with value
var buff$: [0..#buffersize] sync real;
proc producer() {
  var i = 0;
  for ... {
    i = (i+1) % buffersize;
   buff$[i] = ...; // writes block until empty, leave full
proc consumer() {
  var i = 0;
  while ... {
    i= (i+1) % buffersize;
    ...buff$[i]...; // reads block until full, leave empty
```



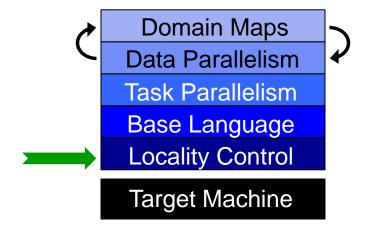
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### **Outline**

CRAY

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Project Status and Next Steps



10 5

### **Chapel and PGAS**



- Chapel is PGAS, but unlike most, it's not restricted to SPMD
  - ⇒ never think about "the other copies of the program"
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    - as in traditional languages, each declaration yields one variable
    - variables are stored on the locale where the task declaring it is executing



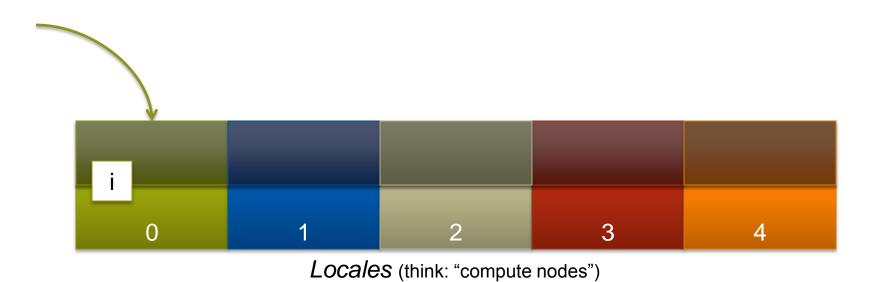
Locales (think: "compute nodes")



(10)

# **Chapel: Scoping and Locality**

var i: int;

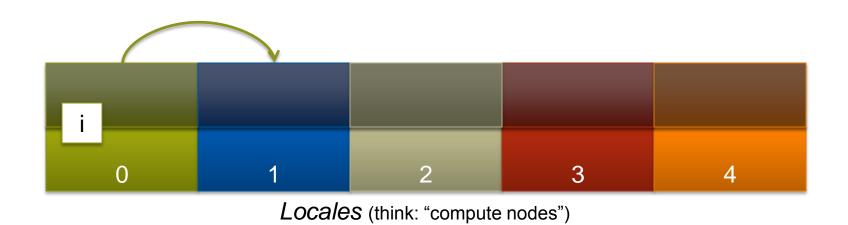




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```
Chapel: Scoping and Locality
```

```
var i: int;
on Locales[1] {
```

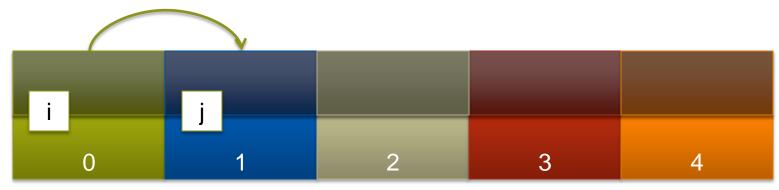




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# **Chapel: Scoping and Locality**

```
var i: int;
on Locales[1] {
  var j: int;
```



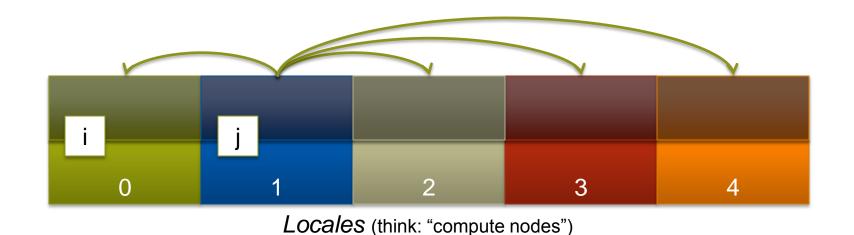
Locales (think: "compute nodes")



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### **Chapel: Scoping and Locality**

```
var i: int;
on Locales[1] {
  var j: int;
  coforall loc in Locales {
    on loc {
```





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### **Chapel: Scoping and Locality**

```
var i: int;
on Locales[1] {
  var j: int;
  coforall loc in Locales {
     on loc {
       var k: int;
       // within this scope, i, j, and k can be referenced;
       // the implementation manages the communication for i and j
                                     k
                                                 k
                                                              k
                      Locales (think: "compute nodes")
```



 $\begin{pmatrix} 11 \\ 1 \end{pmatrix}$ 





### How public a variable is depends only on scoping

- who can see it?
- who actually bothers to refer to it non-locally?

```
var i: int;
on Locales[1] {
  var j: int;
  coforall loc in Locales {
    on loc {
     var k = i + j;
    }
}
```



Locales (think: "compute nodes")



### The Locale Type



#### **Definition:**

- Abstract unit of target architecture
- Supports reasoning about locality
  - defines "here vs. there" / "local vs. remote"
- Capable of running tasks and storing variables
  - i.e., has processors and memory

Typically: A compute node (multicore processor or SMP)



 $\binom{11}{3}$ 

### **Getting started with locales**



Specify # of locales when running Chapel programs

```
% a.out --numLocales=8
```

Chapel provides built-in locale variables

User's main() begins executing on locale #0



11/4

### **Locale Operations**



Locale methods support queries about the target system:

```
proc locale.physicalMemory(...) { ... }
proc locale.numCores { ... }
proc locale.id { ... }
proc locale.name { ... }
```

On-clauses support placement of computations:

```
writeln("on locale 0");

on Locales[1] do
  writeln("now on locale 1");

writeln("on locale 0 again");
```

```
begin on A[i,j] do
   bigComputation(A);

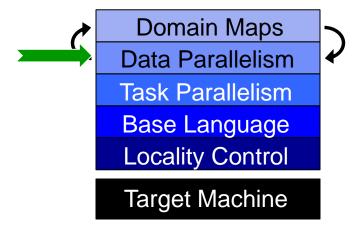
begin on node.left do
   search(node.left);
```



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#### **Outline**

- ✓ Motivation
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Project Status and Next Steps



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#### **Domains**

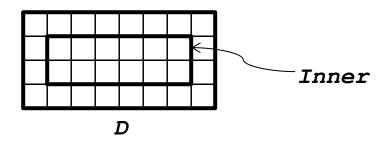


#### Domain:

- A first-class index set
- The fundamental Chapel concept for data parallelism

```
config const m = 4, n = 8;

const D = {1..m, 1..n};
const Inner = {2..m-1, 2..n-1};
```





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#### **Domains**



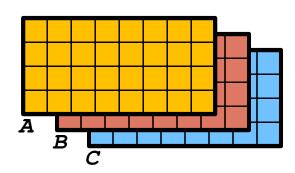
#### **Domain:**

- A first-class index set
- The fundamental Chapel concept for data parallelism
- Useful for declaring arrays and computing with them

```
config const m = 4, n = 8;

const D = {1..m, 1..n};
const Inner = {2..m-1, 2..n-1};

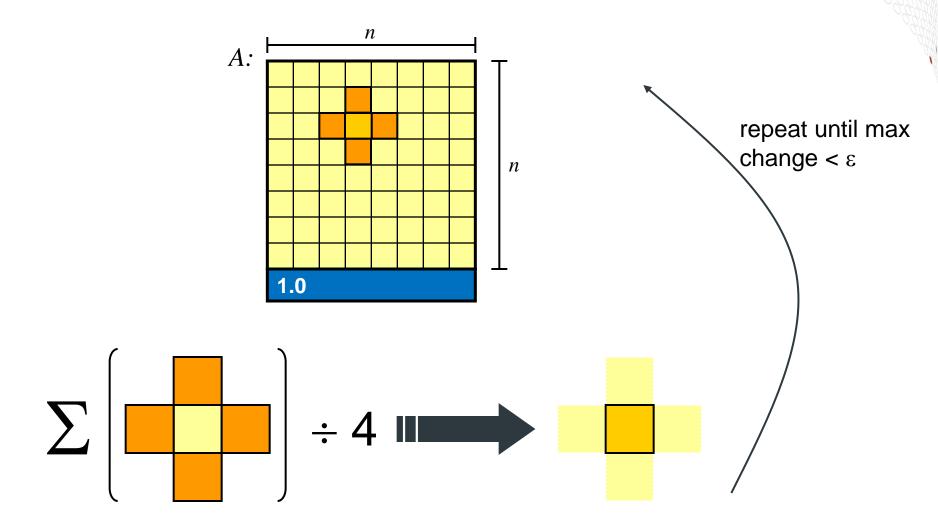
var A, B, C: [D] real;
```





 $\binom{11}{8}$ 

# Data Parallelism by Example: Jacobi Iteration







# **Jacobi Iteration in Chapel**

```
config const n = 6,
             epsilon = 1.0e-5;
const BigD = \{0..n+1, 0..n+1\},
         D = BiqD[1..n, 1..n],
   LastRow = D.exterior(1,0);
var A, Temp : [BiqD] real;
A[LastRow] = 1.0;
do {
  forall (i, i) in D do
    Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;
  const delta = max reduce abs(A[D] - Temp[D]);
 A[D] = Temp[D];
} while (delta > epsilon);
writeln(A);
```





```
config const n = 6,
               epsilon = 1.0e-5;
const BigD = \{0..n+1, 0..n+1\},
          D = BiqD[1..n, 1..n],
var A, Temp : [BiqD] real;
A [ La | Declare program parameters
     const ⇒ can't change values after initialization
  fo
      config ⇒ can be set on executable command-line
               prompt> jacobi --n=10000 --epsilon=0.0001
      note that no types are given; they're inferred from initializers
  wh
               n \Rightarrow default integer (64 bits)
               epsilon ⇒ default real floating-point (64 bits)
```

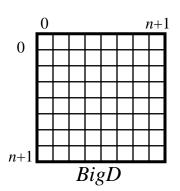


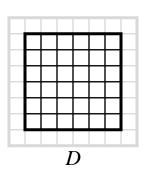


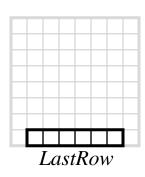
#### **Declare domains (first class index sets)**

**{lo..hi, lo2..hi2}** ⇒ 2D rectangular domain, with 2-tuple indices

**Dom1[Dom2]** ⇒ computes the intersection of two domains







**.exterior()** ⇒ one of several built-in domain generators

COMPUTE



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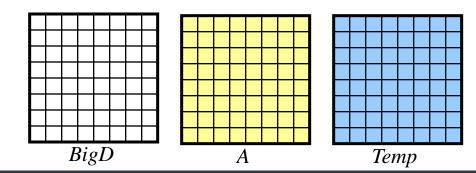


# **Jacobi Iteration in Chapel**

#### **Declare arrays**

**var**  $\Rightarrow$  can be modified throughout its lifetime : **[Dom] T**  $\Rightarrow$  array of size *Dom* with elements of type *T* (**no initializer**)  $\Rightarrow$  values initialized to default value (0.0 for reals)

[i,j+1]) / 4;







```
config const n = 6,
const BiqD = \{0..n+1, 0..n+1\},
         D = BiqD[1..n, 1..n],
   LastRow = D.exterior(1,0);
var A, Temp : [BiqD] real;
A[LastRow] = 1.0;
```

#### **Set Explicit Boundary Condition**

**Arr[Dom]** ⇒ refer to array slice ("forall i in Dom do ...Arr[i]...")

COMPUTE

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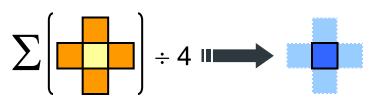


# **Jacobi Iteration in Chapel**

config const n = 6,

#### Compute 5-point stencil

forall ind in Dom ⇒ parallel forall expression over Dom's indices, binding them to ind (here, since Dom is 2D, we can de-tuple the indices)



```
do {
    forall (i,j) in D do
        Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;

    const delta = max reduce abs(A[D] - Temp[D]);
    A[D] = Temp[D];
} while (delta > epsilon);
```





#### **Compute maximum change**

*op* reduce ⇒ collapse aggregate expression to scalar using *op* 

**Promotion:** abs() and – are scalar operators; providing array operands results in parallel evaluation equivalent to:

```
forall (a,t) in zip(A,Temp) do abs(a - t)
```

```
do {
    forall (i,j) in D do
        Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;

    const delta = max reduce abs(A[D] - Temp[D]);
    A[D] = Temp[D];
} while (delta > epsilon);
writeln(A);
```





# **Jacobi Iteration in Chapel**

#### Copy data back & Repeat until done

uses slicing and whole array assignment standard *do...while* loop construct

```
do {
    forall (i,j) in D do
        Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;

    const delta = max reduce abs(A[D] - Temp[D]);
    A[D] = Temp[D];
} while (delta > epsilon);
writeln(A);
```





```
config const n = 6,
const BiqD = \{0..n+1, 0..n+1\},
         D = BiqD[1..n, 1..n],
   LastRow = D.exterior(1,0);
var A, Temp : [BiqD] real;
                                    Write array to console
  forall (i, j) in D do
    Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j]
                                                    A[i,j+1]) / 4;
  const delta = max reduce abs (A
} while (delta > eps
writeln(A);
```





By default, domains and their arrays are mapped to a single locale.

Any data parallelism over such domains/ arrays will be executed by the cores on that locale. Thus, this is a shared-memory parallel program.

```
Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;

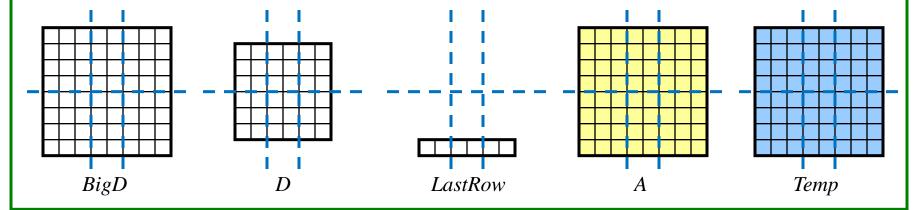
const delta = max reduce abs(A[D] - Temp[D]);
A[D] = Temp[D];
} while (delta > epsilon);
writeln(A);
```





# **Jacobi Iteration in Chapel**

With this simple change, we specify a mapping from the domains and arrays to locales Domain maps describe the mapping of domain indices and array elements to *locales* specifies how array data is distributed across locales specifies how iterations over domains/arrays are mapped to locales







# **Jacobi Iteration in Chapel**

```
config const n = 6,
             epsilon = 1.0e-5;
const BigD = \{0..n+1, 0..n+1\} dmapped Block(\{1..n, 1..n\}),
         D = BigD[1..n, 1..n],
   LastRow = D.exterior (1,0);
var A, Temp : [BiqD] real;
A[LastRow] = 1.0;
do {
  forall (i, i) in D do
    Temp[i,j] = (A[i-1,j] + A[i+1,j] + A[i,j-1] + A[i,j+1]) / 4;
  const delta = max reduce abs(A[D] - Temp[D]);
  A[D] = Temp[D];
} while (delta > epsilon);
writeln(A);
use BlockDist;
```



### **Notes on Forall Loops**



```
forall (a, i) in zip(A, 1..n) do
a = i / 10.0; /
```

Like for loops, forall-loops may be zippered, and corresponding iterations will match up



### **STREAM Triad: Chapel**

```
Chapel
#include <hpcc.h>
#ifdef OPENMP
#include <omp.h>
#endif
static int VectorSize;
                             config const m = 1000,
static double *a, *b, *c;
int HPCC StarStream (HPCC Params *)
 int myRank, commSize;
                                                       alpha = 3.0;
 int rv, errCount;
 MPI Comm comm = MPI COMM WORLD;
 MPI Comm size ( comm, &commSize );
 MPI Comm rank ( comm, &myRank );
                                                                                                                         the special
                             const ProblemSpace = {1..m} dmapped
 rv = HPCC Stream( params, 0 == myF
 MPI Reduce ( &rv, &errCount, 1, MP:
                                                                                                                         sauce
 return errCount:
                             var A, B, C: [ProblemSpace] real;
int HPCC Stream (HPCC Params *params
 register int j;
 double scalar:
 VectorSize = HPCC LocalVectorSize(
                                = 2.0;
                                                                                                                     N);
 a = HPCC XMALLOC ( double, VectorSi
 b = HPCC XMALLOC ( double, VectorSi
                             C = 3.0;
                                                                                                                     N);
 c = HPCC XMALLOC( double, VectorSi
 if (!a || !b || !c) {
  if (c) HPCC free(c);
   if (b) HPCC free(b);
   if (a) HPCC_free(a);
                             A = B + alpha * C;
                                                                                                                     c, da, scalar, N);
   if (doIO) {
    fprintf( outFile, "Failed to
    fclose(outFile);
------
```

<u>Philosophy:</u> Good language design can tease details of locality and parallelism away from an algorithm, permitting the compiler, runtime, applied scientist, and HPC expert to each focus on their strengths.



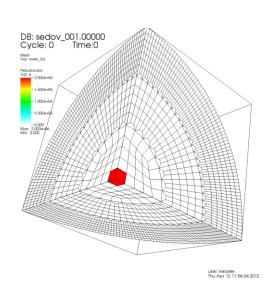
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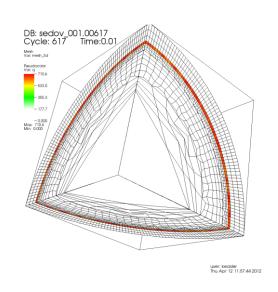
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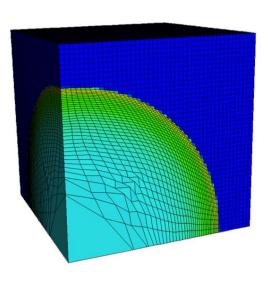
# **LULESH: a DOE Proxy Application**



Goal: Solve one octant of the spherical Sedov problem (blast wave) using Lagrangian hydrodynamics for a single material







pictures courtesy of Rob Neely, Bert Still, Jeff Keasler, LLNL





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### 1288 lines of source code

plus 266 lines of comments

487 blank lines

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### (the corresponding C+MPI+OpenMP version is nearly 4x bigger)

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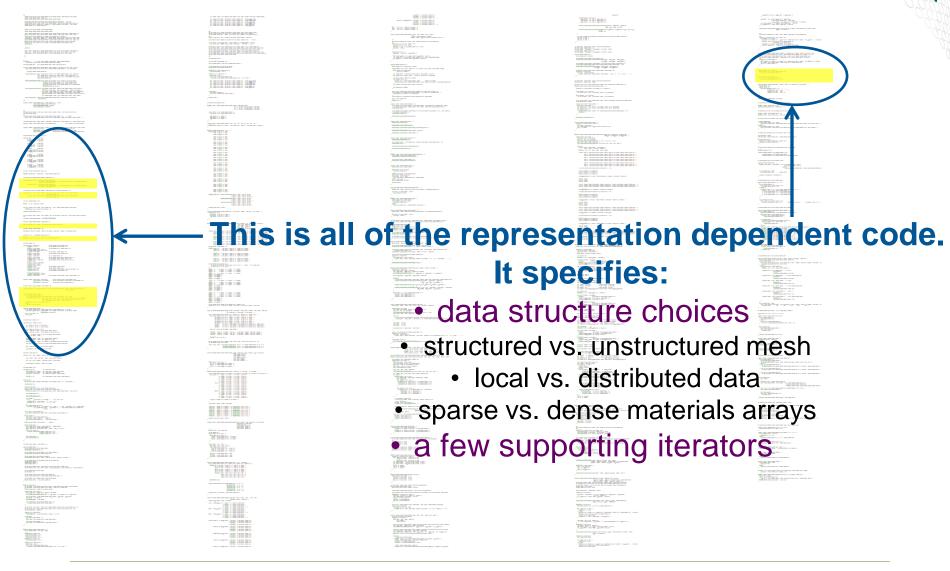
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This can be found in Chapel v1.9 in examples/benchmarks/lulesh/\*.chpl

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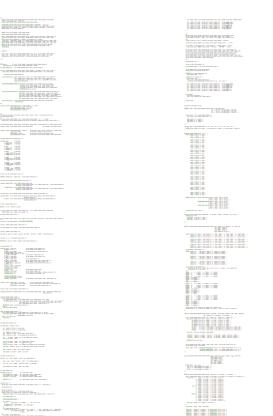
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Here is some sample representation-independent code

IntegrateStressForElems()

LULESH spec, section 1.5.1.1 (2.)

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### Representation-Independent Physics

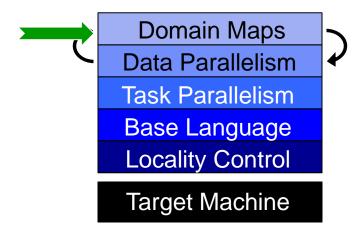


```
proc IntegrateStressForElems(sigxx, sigyy, sigzz, determ) {
 forall k in Elems {
                                                                parallel loop over elements
  var b_x, b_y, b_z: 8*real;
  var x_local, y_local, z_local: 8*real;
                                                                   collect nodes neighboring this
  localizeNeighborNodes(k, x, x_local, y, y_local, z, z_local); <
                                                                   element; localize node fields
  var fx_local, fy_local, fz_local: 8*real;
  local {
   /* Volume calculation involves extra work for numerical consistency. */
   CalcElemShapeFunctionDerivatives(x local, y local, z local,
                                    b_x, b_y, b_z, determ[k]);
   CalcElemNodeNormals(b_x, b_y, b_z, x_local, y_local, z_local);
   SumElemStressesToNodeForces(b_x, b_y, b_z, sigxx[k], sigyy[k], sigzz[k],
                                  fx_local, fy_local, fz_local);
                                                                     update node forces from
                                                                     element stresses
  for (noi, t) in elemToNodesTuple(k) {
   fx[noi].add(fx_local[t]);
   fy[noi].add(fy_local[t]);
                                      Because of domain maps, this code is independent of:
   fz[noi].add(fz_local[t]);
                                      structured vs. unstructured mesh
                                      shared vs. distributed data
                                      sparse vs. dense representation
```

CHAPEL

#### **Outline**

- ✓ Motivation
- ✓ Chapel Background and Themes
- Survey of Chapel Concepts



Project Status and Next Steps

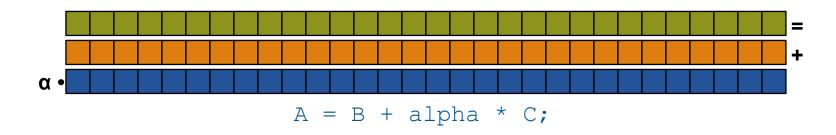


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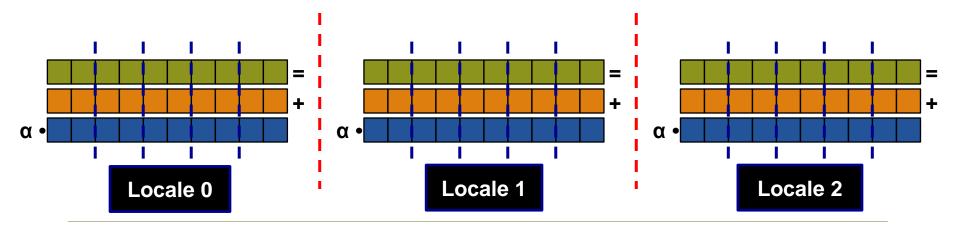
### **Domain Maps**



Domain maps are "recipes" that instruct the compiler how to map the global view of a computation...



### ...to the target locales' memory and processors:

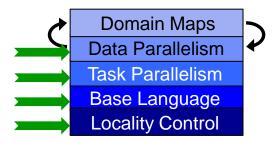




### **Chapel's Domain Map Philosophy**



- 1. Chapel provides a library of standard domain maps
  - to support common array implementations effortlessly
- 2. Expert users can write their own domain maps in Chapel
  - to cope with any shortcomings in our standard library

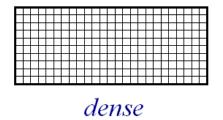


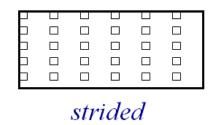
- 3. Chapel's standard domain maps are written using the same end-user framework
  - to avoid a performance cliff between "built-in" and user-defined cases

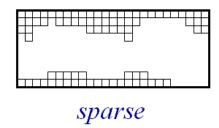


### **Chapel Domain Types**

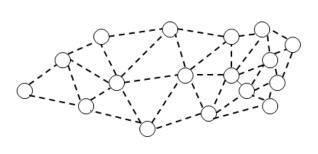












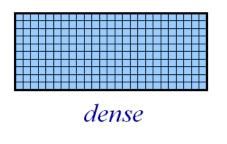
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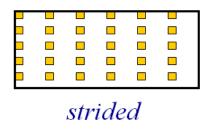
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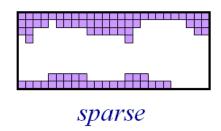


# **Chapel Array Types**

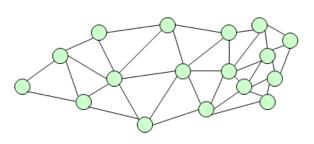












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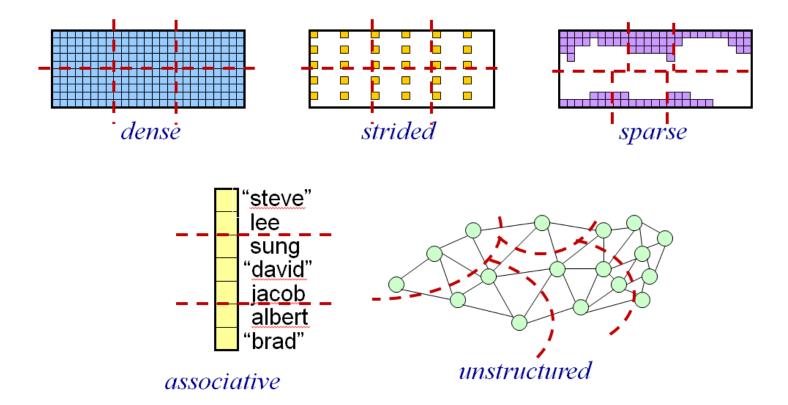


COMPUTE

associative

## **All Domain Types Support Domain Maps**







## **Domain Maps Summary**



- Data locality requires mapping arrays to memory well
  - distributions between distinct memories
  - layouts within a single memory
- Most languages define a single data layout & distribution
  - where the distribution is often the degenerate "everything's local"
- Domain maps...
  - ...move such policies into user-space...
  - ...exposing them to the end-user through high-level declarations

```
const Elems = {0..#numElems} dmapped Block(...)
```



## **Two Other Thematically Similar Features**



- 1) parallel iterators: Define parallel loop policies
- 2) locale models: Define target architectures

#### Like domain maps, these are...

- ...written in Chapel by expert users using lower-level features
  - e.g., task parallelism, on-clauses, base language features, ...
- ...available to the end-user via higher-level abstractions
  - e.g., forall loops, on-clauses, lexically scoped PGAS memory, ...



## **Multiresolution Summary**



Chapel's multiresolution philosophy allows users to write... ...custom array implementations via domain maps

...custom parallel iterators via leader-follower iterators

...custom architectural models via hierarchical locales

The result is a language that decouples crucial policies for managing data locality out of the language's definition and into an expert user's hand...

...while making them available to end-users through highlevel abstractions



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#### For More Information on...



#### ...domain maps

<u>User-Defined Distributions and Layouts in Chapel: Philosophy and Framework</u> [slides], Chamberlain, Deitz, Iten, Choi; HotPar'10, June 2010.

<u>Authoring User-Defined Domain Maps in Chapel</u> [slides], Chamberlain, Choi, Deitz, Iten, Litvinov; Cug 2011, May 2011.

#### ...leader-follower iterators

<u>User-Defined Parallel Zippered Iterators in Chapel</u> [slides], Chamberlain, Choi, Deitz, Navarro; PGAS 2011, October 2011.

#### ...hierarchical locales

Hierarchical Locales: Exposing Node-Level Locality in Chapel, Choi; 2<sup>nd</sup> KIISE-KOCSEA SIG HPC Workshop talk, November 2013.

Status: all of these concepts are in-use in every Chapel program today (pointers to code/docs in the release available by request)



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## **Summary**



# Higher-level programming models can help insulate algorithms from parallel implementation details

- yet, without necessarily abdicating control
- Chapel does this via its multiresolution design
  - here, we saw it principally in domain maps
    - leader-follower iterators and locale models are other examples
  - these avoid locking crucial policy decisions into the language

## We believe Chapel can greatly improve productivity

- ...for current and emerging HPC architectures
- ...for emerging mainstream needs for parallelism and locality



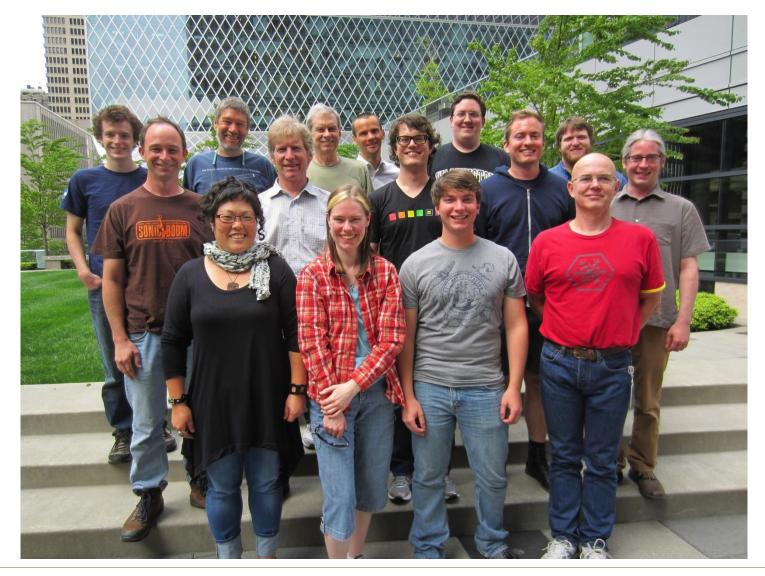
#### **Outline**

CRAY

- ✓ Motivation
- ✓ Chapel Background and Themes
- ✓ Survey of Chapel Concepts
- Project Status and Next Steps



# **The Cray Chapel Team (Summer 2014)**







### Chapel...

## ...is a collaborative effort — join us!



















Proudly Operated by Battelle Since 1965













## A Year in the Life of Chapel



- Two major releases per year (April / October)
  - latest release: version 1.11, April 2<sup>nd</sup>, 2015
  - ~a month later: detailed release notes
    - version 1.11 release notes: <a href="http://chapel.cray.com/download.html#releaseNotes">http://chapel.cray.com/download.html#releaseNotes</a>
- CHIUW: Chapel Implementers and Users Workshop (May-June)
  - workshop focusing on community efforts, code camps
  - this year will be held in Portland, June 13-14
- **SC** (Nov)
  - the primary conference for the HPC industry
  - we give tutorials, BoFs, talks, etc. to show off year's work
- Talks, tutorials, research visits, blogs, ... (year-round)



(15

## **Implementation Status -- Version 1.11** (Apr 2015)



#### **Overall Status:**

- User-facing Features: generally in good shape
  - some require additional attention (e.g., strings, memory mgmt)
- Multiresolution Features: in use today
  - their interfaces are likely to continue evolving over time
- Error Messages: not always as helpful as one would like
  - correct code works well, incorrect code can be puzzling
- Performance: hit-or-miss depending on the idioms used
  - Chapel designed to ultimately support competitive performance
  - to-date, we've focused primarily on correctness and local perf.

### This is a great time to:

- Try out the language and compiler
- Use Chapel for non-performance-critical projects
- Give us feedback to improve Chapel
- Use Chapel for parallel programming education



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## **Chapel and Education**



#### When teaching parallel programming, I like to cover:

- data parallelism
- task parallelism
- concurrency
- synchronization
- locality/affinity
- deadlock, livelock, and other pitfalls
- performance tuning
- ...

## I don't think there's been a good language out there...

- for teaching all of these things
- for teaching some of these things well at all
- until now: We believe Chapel can play a crucial role here
   (see <a href="http://chapel.cray.com/education.html">http://chapel.cray.com/education.html</a> for more information and <a href="http://cs.washington.edu/education/courses/csep524/13wi/">http://cs.washington.edu/education/courses/csep524/13wi/</a> for my use of Chapel in class)



## Chapel: the next five years



- Harden prototype to production-grade
  - add/improve lacking features
  - optimize performance
  - improve interoperability
- Target more complex/modern compute node types
  - e.g., Intel Phi, CPU+GPU, AMD APU, ...
- Continue to grow the user and developer communities
  - including nontraditional circles: desktop parallelism, "big data"
  - transition Chapel from Cray-managed to community-governed



## **Summary**



# Higher-level programming models can help insulate algorithms from parallel implementation details

- yet, without necessarily abdicating control
- Chapel does this via its multiresolution design
  - here, we saw it principally in domain maps
    - parallel iterators and locale models are other examples
  - these avoid locking crucial policy decisions into the language

## We believe Chapel can greatly improve productivity

- ...for current and emerging HPC architectures
- ...for emerging mainstream needs for parallelism and locality



#### For More Information: Online Resources



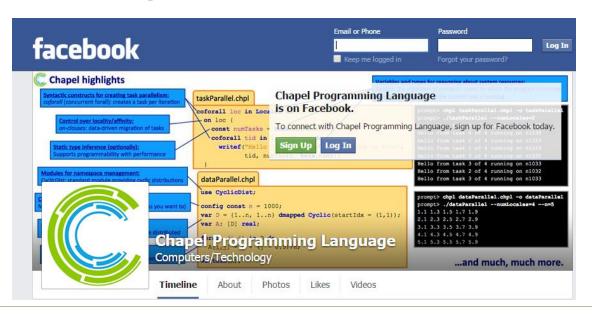
#### Chapel project page: <a href="http://chapel.cray.com">http://chapel.cray.com</a>

overview, papers, presentations, language spec, ...

### Chapel GitHub page: <a href="https://github.com/chapel-lang">https://github.com/chapel-lang</a>

download 1.11.0 release, browse source repository

## Chapel Facebook page: <a href="https://www.facebook.com/ChapelLanguage">https://www.facebook.com/ChapelLanguage</a>





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## For More Information: Community Resources



## Chapel SourceForge page: <a href="https://sourceforge.net/projects/chapel/">https://sourceforge.net/projects/chapel/</a>

join community mailing lists; alternative release download site

### **Mailing Aliases:**

- chapel\_info@cray.com: contact the team at Cray
- chapel-announce@lists.sourceforge.net: list for announcements only
- chapel-users@lists.sourceforge.net: user-oriented discussion list
- chapel-developers@lists.sourceforge.net: developer discussion
- chapel-education@lists.sourceforge.net: educator discussion
- chapel-bugs@lists.sourceforge.net: public bug forum



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# For More Information: Suggested Reading



#### **Overview Papers:**

- <u>A Brief Overview of Chapel</u>, Chamberlain (pre-print of a chapter for A Brief Overview of Parallel Programming Models, edited by Pavan Balaji, to be published by MIT Press in 2014).
  - a detailed overview of Chapel's history, motivating themes, features
- <u>The State of the Chapel Union</u> [slides], Chamberlain, Choi, Dumler, Hildebrandt, Iten, Litvinov, Titus. CUG 2013, May 2013.
  - a higher-level overview of the project, summarizing the HPCS period



## For More Information: Lighter Reading



#### **Blog Articles:**

- <u>Chapel: Productive Parallel Programming</u>, Chamberlain, <u>Cray Blog</u>, May 2013.
  - a short-and-sweet introduction to Chapel
- Why Chapel? (part 1, part 2, part 3), Chamberlain, Cray Blog, June-August 2014.
  - a current series of articles answering common questions about why we are pursuing Chapel in spite of the inherent challenges
- [Ten] Myths About Scalable Programming Languages (index available here), Chamberlain, IEEE TCSC Blog, April-November 2012.
  - a series of technical opinion pieces designed to combat standard arguments against the development of high-level parallel languages



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