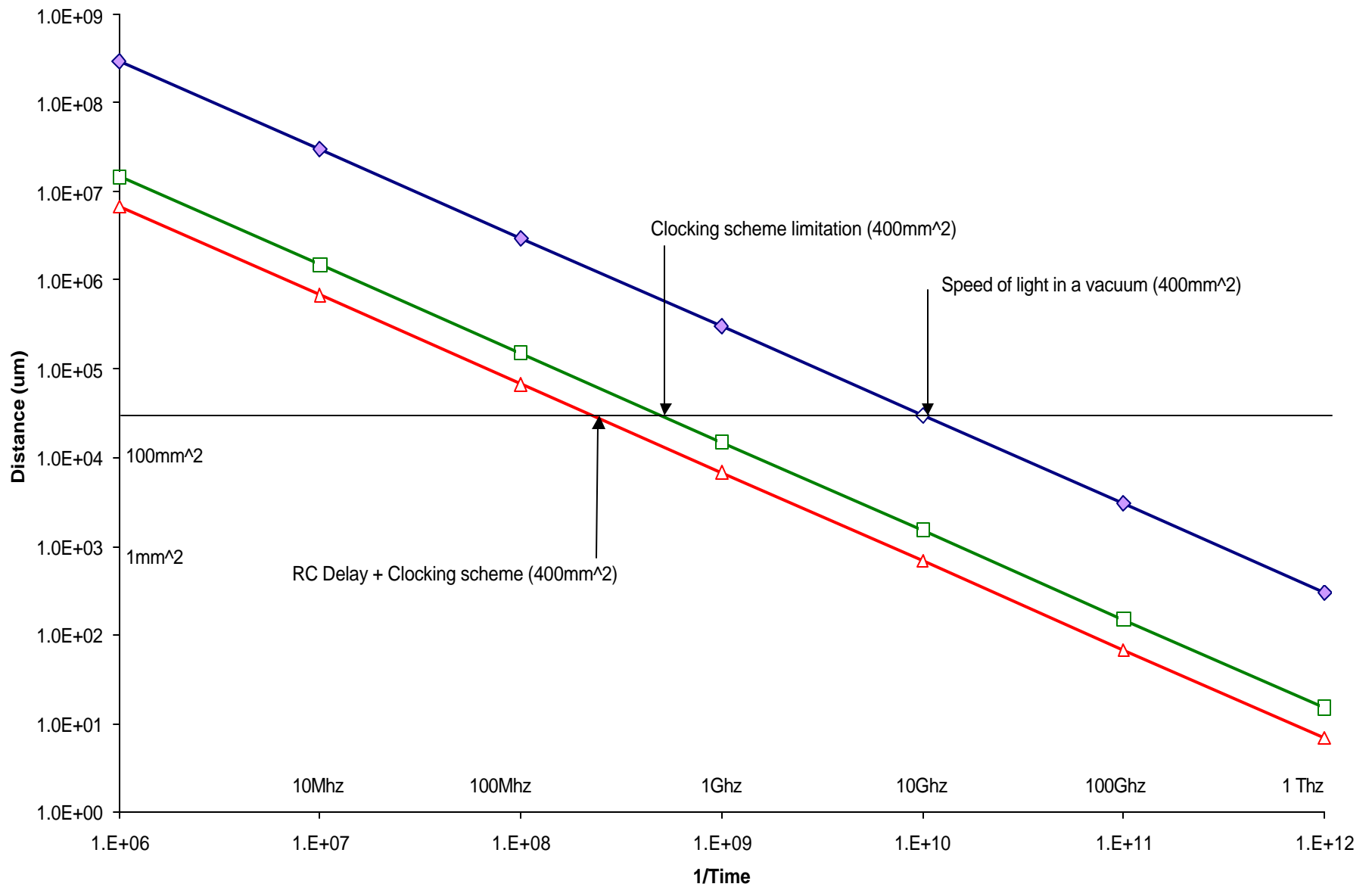


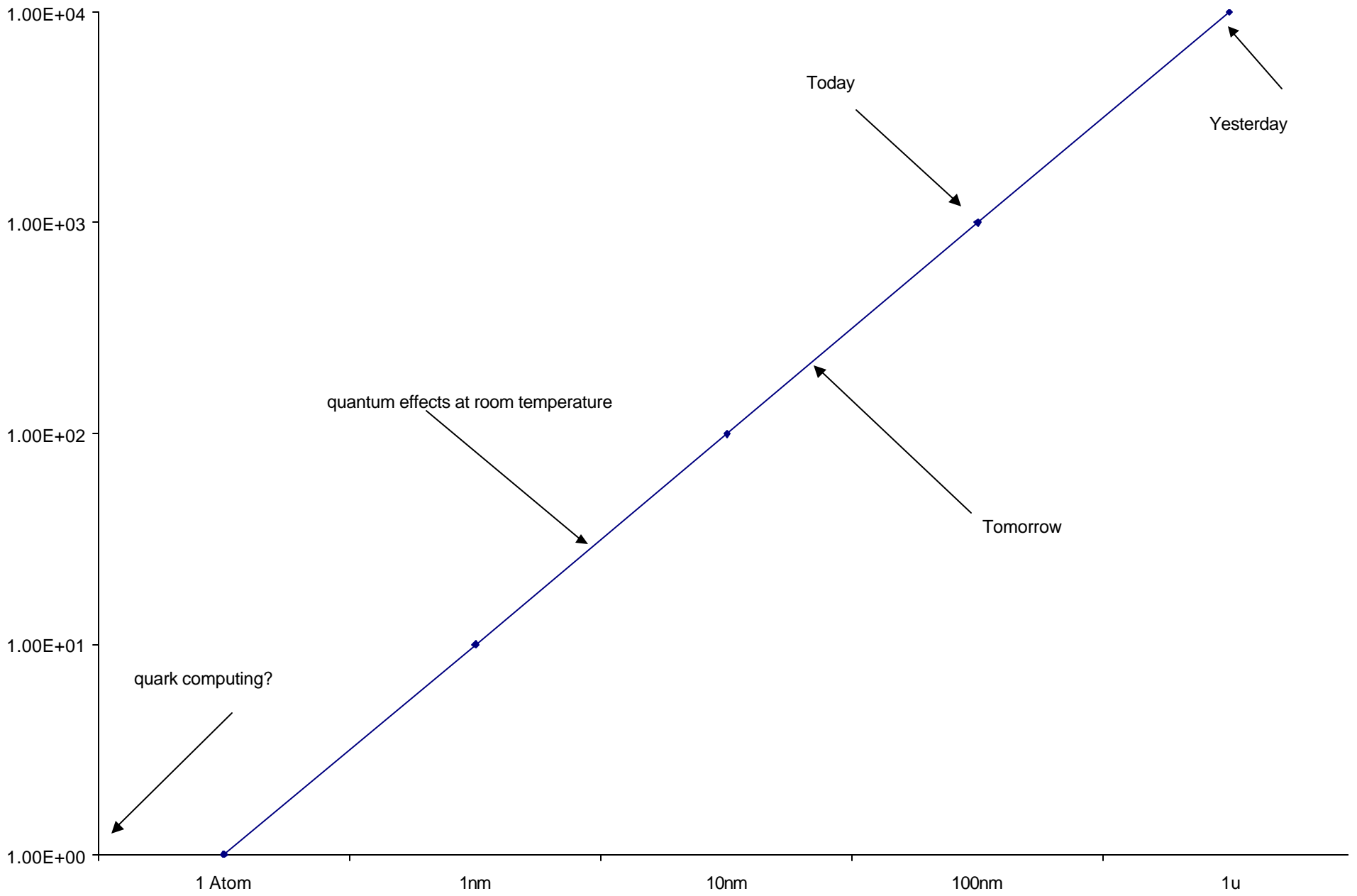
# What will the future be like?

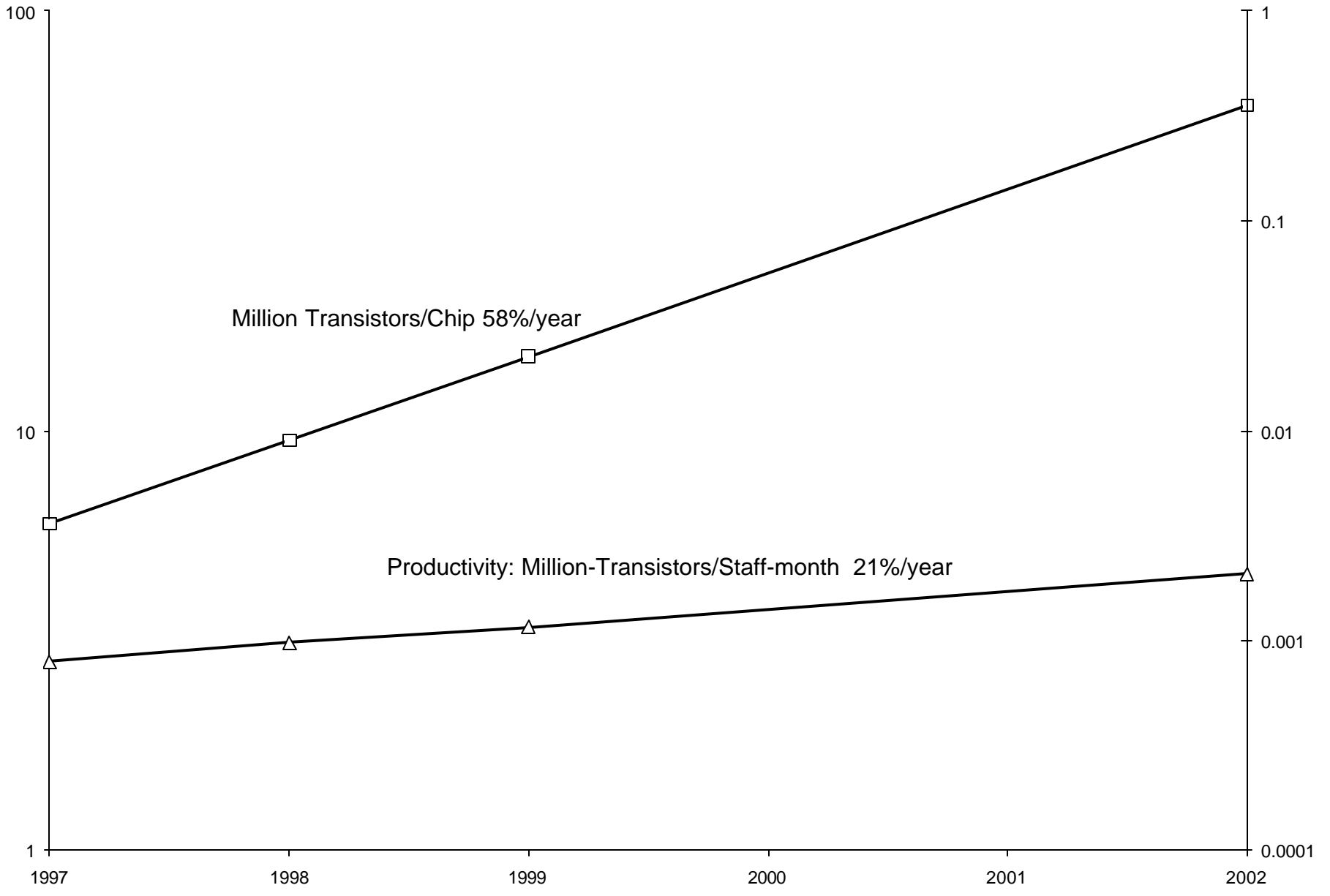
- Why not make smaller less power intensive chips?
  - Ubiquitous?
  - Different Application space?
- Software perspective
  - Timely output to market
  - Correctness
  - Conventions
  - Workflow
  - Usability / Features
  - Little work as possible

# What will the future be like

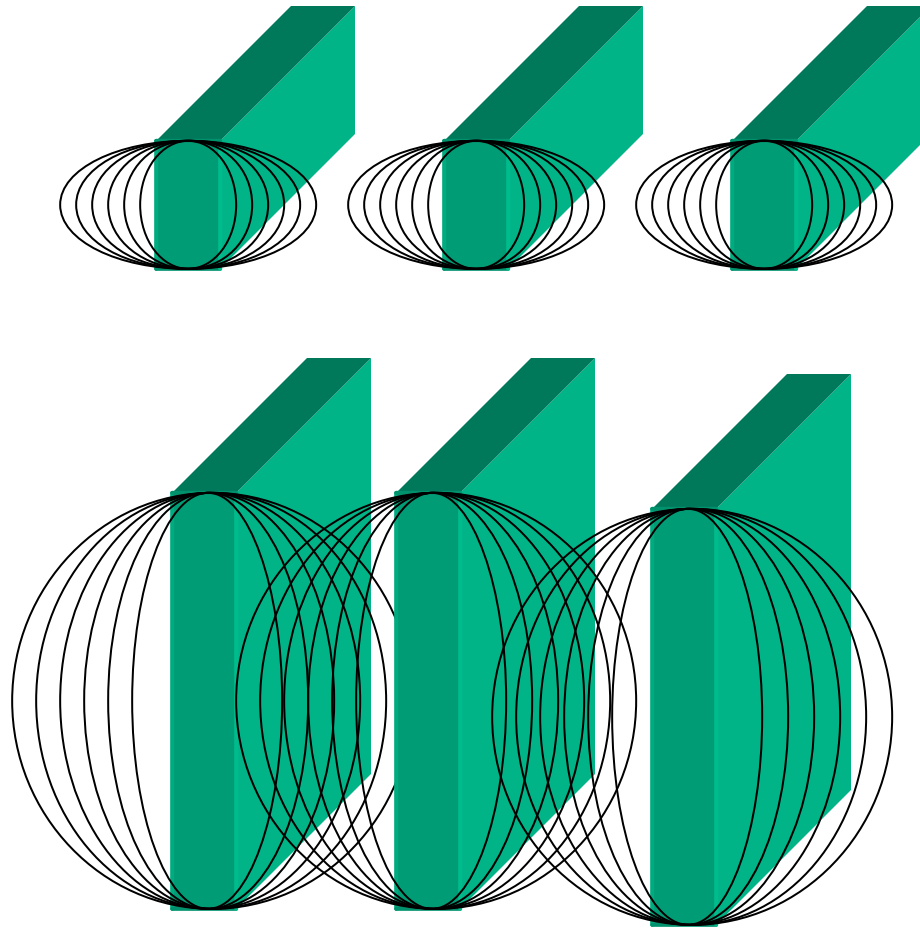
- More parallel computers
- Better packaging
  - Flip chip
  - 3D stacking of xtrs
- Non-silicon?







# Why are wires slow?



Is there a *hard* limit?

- But does  $h$  make a limit?