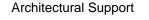
CSE 451: Operating Systems Spring 2005

Module 23 Course Review

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Privileged instructions

- what are they, and who gets to execute them?
- how does CPU know whether to execute them?
- why do they need to be privileged?
- what do they manipulate?
- Events

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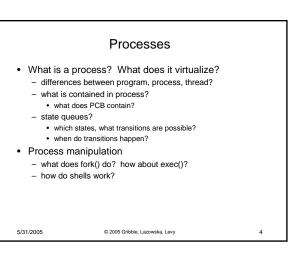
- exceptions: what generates them? trap vs. fault?

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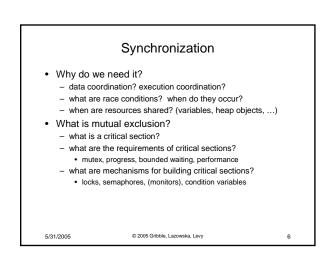
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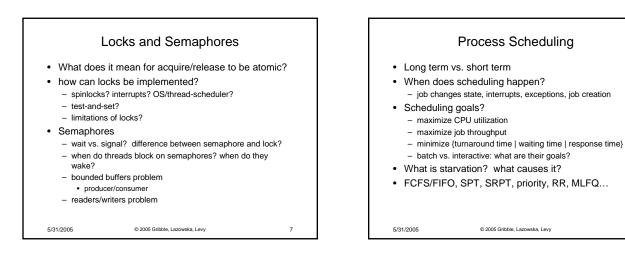
– interrupt: what generates them?

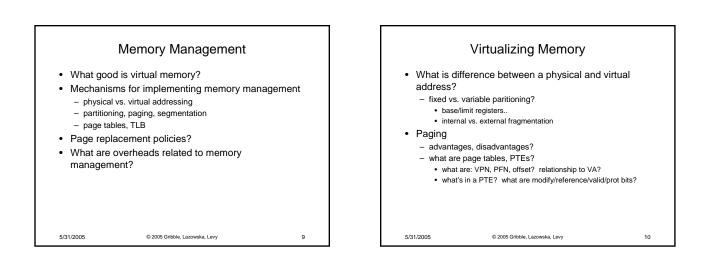
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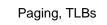


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- How to reduce overhead of page table?
 - how do multi-level page tables work?
 - what problem does TLB solve?
 - why do they work?
 - how are they managed?
 software vs. hardware managed?
 - sonware vs. hardware r
- Page faults
 - what is one? how is it used to implement demand paging?
 what is complete sequence of steps for translating a virtual address to a PA?

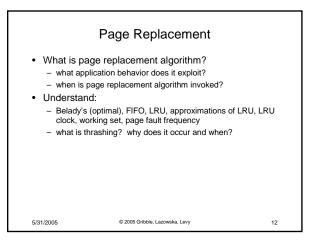
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- all the way from TLB access to paging in from disk
- MM tricks

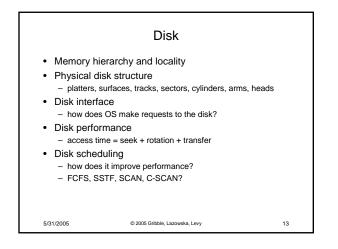
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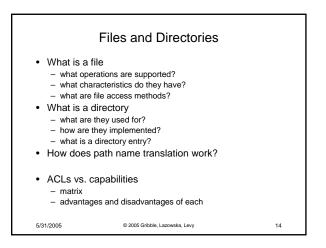
- shared memory? Mapped files? copy-on-write?

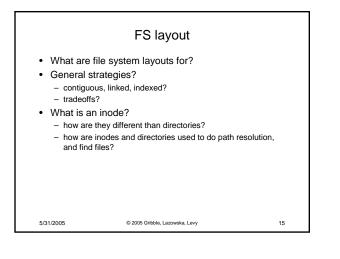
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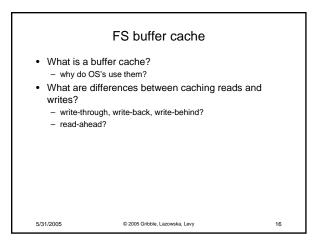


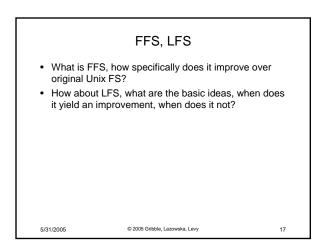
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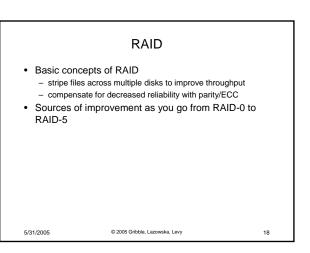


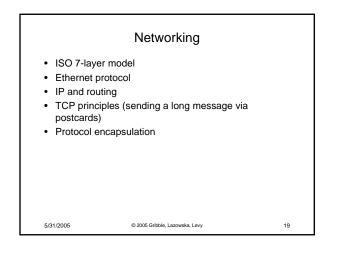


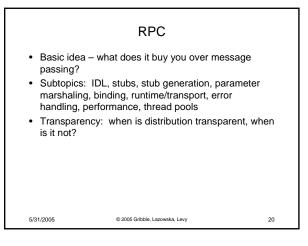


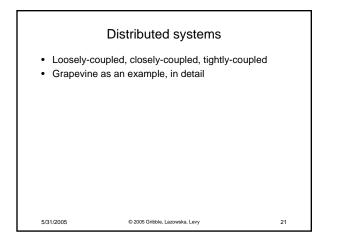


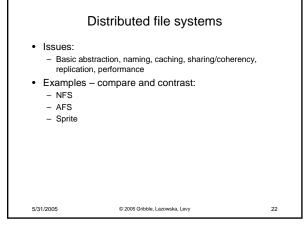














- General concepts of buffer overflow exploits
- Details how, specifically, would you program one, for the x86 architecture?

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