









Size AC 1000 60 60	Power-W	+/sec	Mem KB	р.:			
AC 1000 60 60	124 500		THUR THE	Price	Pr/Perf	Adj Price	Adj P/Perf
0 60	10,000	1,900	48	\$1M	1	\$5.0M	1
0	10.000	500,000 330,000	64 4	\$1M \$16,000	263	\$4,1M \$66.071	318
0 58	60,000	166M	4 32 768	\$10,000 \$4M	21 842	\$8.5M	51 604
C 1	150	240,000	256	\$3,000	42,105	\$4,081	154,673
0 2	500	50M	16,384	\$74,000	3.5M	\$8,156	16.1M
00 2	500	400M	16,384	\$4,400	47,8M	\$4,400	239.0M
60 Model 50	HP-9000 Mode	al 750					
60 Model 50, I	HP-9000 Mode	el 750					
6 6	C 1 0 2 00 2 0 Model 50, 5 in cubic feet	2 1 150 2 500 10 2 500 0 Model 50, HP-9000 Model in cubic feet	C    1    150    240,000      Q    500    50M    90      NO    2    500    400M      O    Model 50, HP-9000    Model 750    in cubic feet	C 1 150 240,000 256 C 2 500 50M 16,384 NO 2 500 400M 16,384 0 Model 50, HP-9000 Model 750 in cubic feet	C    1    150    240,000    256    \$3,000      D    2    500    50M    16,384    \$74,000      NO    2    500    400M    16,384    \$4,400      0    Model 50, HP-9000 Model 750    in cubic feet    \$4,400	C    1    150    240,000    256    \$3,000    42,105      D    2    500    50M    16,384    \$74,000    3.5M      NO    2    500    400M    16,384    \$4,400    47,8M      0    Model 50, HP-9000 Model 750    in cubic feet    500 <td>C  1  150  240,000  256  \$3,000  42,105  \$4,081    D  2  500  50M  16,384  \$74,000  3.5M  \$8,156    00  2  500  400M  16,384  \$4,400  47,8M  \$4,400    0  Model 50, HP-9000 Model 750      </td>	C  1  150  240,000  256  \$3,000  42,105  \$4,081    D  2  500  50M  16,384  \$74,000  3.5M  \$8,156    00  2  500  400M  16,384  \$4,400  47,8M  \$4,400    0  Model 50, HP-9000 Model 750

## Architectural Terminology

abstraction	DRAM	
assembler	implementation	
binary number	instruction	
bit	instruction set architecture	
cache	integrated circuit	
central processing unit	memory	
chip	operating system	
compiler	processor	
computer family	semiconductor	
control	supercomputer	
data path	transistor	
defect	VLSI	
die	yield	
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