CSE352 Spring 2013 Homework Assignment #1
Due in class Wednesday April 17th 2013

Q1) Formulate the truth-table and draw the CMOS schematic of a gate that implements the following 3-input function:
\[ Y = \neg((A + B) \cdot C) \]

Q2) Formulate the truth-table and draw the CMOS schematic of a circuit that implements the following 3-input function (hint: use an inverter):
\[ Y = A \cdot B + C \]

*BONUS:* What gate does this CMOS gate layout represent?