Please read through the entire examination first! We designed this exam so that it can be completed in 50 minutes and, hopefully, this estimate will prove to be reasonable.

There are 5 problems for a total of 95 points. The point value of each problem is indicated in the table below. Write your answer neatly in the spaces provided. If you need more space, you can write on the back of the sheet where the question is posed, but please make sure that you indicate clearly the problem to which the comments apply. If you have difficulty with part of a problem, move on to the next one. They are independent of each other.

The exam is CLOSED book and CLOSED notes (no summary sheets, no calculators, no mobile phones, no laptops). Please do not ask or provide anything to anyone else in the class during the exam. Make sure to ask clarification questions early so that both you and the others may benefit as much as possible from the answers.

Good Luck!

Name (as it appears on your ID): ____________________________

Student Number: ____________________________

UWNet ID: ____________________________

<table>
<thead>
<tr>
<th>Problem</th>
<th>Max Score</th>
<th>Score</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
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<tr>
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<td>20</td>
<td></td>
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<tr>
<td>5</td>
<td>30</td>
<td></td>
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<tr>
<td>TOTAL</td>
<td>95</td>
<td></td>
</tr>
</tbody>
</table>
1. Integers and Floats (25 points total)

A. (21 points) Given the following declarations:

```c
int x1 = ...; // x1 > 0
int x2 = ...; // x2 < 0
float f = ...;
double d1 = ...;
double d2 = ...;
```

Assume neither d1, d2 nor f is NaN.

For each of the following, indicate if it is TRUE for all possible values of the given variables (note that x1 and x2 have specific ranges). **If not, select FALSE and give a BRIEF one sentence justification for your answer** – BE SPECIFIC. You do not need to give a justification for true answers.

<p>| | | | | |</p>
<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>d1 == (double)(float) d1</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>2.</td>
<td>x2 == (int)(double) x2</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>3.</td>
<td>x1 == (int)(float) x1</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>4.</td>
<td>d1 == -(d1)</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>5.</td>
<td>x1 + x2 will never overflow</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>6.</td>
<td>(d1 + d2) - d1 == d2</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
<tr>
<td>7.</td>
<td>f == (float)(double) f</td>
<td>TRUE</td>
<td>FALSE</td>
<td></td>
</tr>
</tbody>
</table>
B. (4 points) What is the largest positive number we can represent with a 10-bit signed two’s complement integer?

Bit pattern in binary:

Value in decimal:
2. C to Assembly (10 points)

Given the following C function:

```c
long happy(long *x, long y, long z) {
    if (y > z)
        return z + y;
    else
        return *x;
}
```

Write x86-64 bit assembly code for this function here. Comments are not required but could help for partial credit. We are not judging you on the efficiency of your code, just the correctness. It is fine to leave off the size suffixes if you prefer to (e.g. b, w, l, q).

`happy:`
3. **C to Assembly (10 points)**

Given the following C function:

```c
long silly(long *z, long index) {
    z = z + 2;
    return z[index] - 7;
}
```

Write **x86-64** bit assembly code for this function here. Comments are not required but could help for partial credit. We are not judging you on the efficiency of your code, just the correctness. It is fine to leave off the size suffixes if you prefer to (e.g. b, w, l, q).

**silly:**
4. Assembly to C (20 points)

Given the C code for the function `sunny()`, determine which x86-64 code snippet corresponds to a correct implementation of `sunny()`.

```c
long sunny(long *z, long counter){
    long temp = *z;
    while (counter > 1) {
        temp = temp * 8;
        counter--;
    }
    return temp;
}
```

Circle all of the x86-64 implementations below that correctly implement `sunny()` (there could be more than one). For implementations that are not correct give at least one reason why it is not correct. (You do not need to give reasons why the correct ones are correct.)

If Incorrect, give Reason:

a) ```
movq (%rdi), %rax
jmp .L10

.L11:
    leaq 8(%rax), %rax
subq $1, %rsi
jg  .L11
rep ret
```

Circle One:  
Correct  Incorrect

b) ```
movq (%rdi), %rax
jmp .L10

.L11:
    salq $3, %rax
subq $1, %rsi
cmpq $1, %rsi
jg  .L11
rep ret
```

Circle One:  
Correct  Incorrect

If Incorrect, give Reason:
c)  

```assembly
movq (%rdi), %rax  
.L11:  
cmpq $1, %rsi  
jle .L10  
leaq (,%rax,8), %rax  
subq $1, %rsi  
jmp .L11  
.L10:  
rep ret
```

If Incorrect, give Reason:

---

d)  

```assembly
movq %rdi, %rax  
jmp .L10  
.L11:  
salq $3, %rax  
subq $1, %rsi  
.L10:  
cmpq $1, %rsi  
jg .L11  
rep ret
```

If Incorrect, give Reason:

---

e)  

```assembly
leaq (%rdi), %rax  
jmp .L10  
.L11:  
salq $3, %rax  
subq $1, %rsi  
.L10:  
cmpq $1, %rsi  
jg .L11  
rep ret
```

If Incorrect, give Reason:
5. Stack Discipline (30 points)

Examine the following recursive function:

```c
long magic(long x, long *y) {
    long temp;
    if (x < 2) {
        return *y;
    } else {
        temp = *y + 1;
        return x + magic(x-3, &temp);
    }
}
```

Here is the x86_64 assembly for the same function:

```
4005f6 <magic>:
4005f6:  cmp    $0x1,%rdi
4005fa:  jg     0x400600 <magic+10>
4005fc:  mov    (%rsi),%rax
4005ff:  retq
400600:  push   %rbx
400601:  sub    $0x10,%rsp
400605:  mov    %rdi,%rbx
400608:  mov    (%rsi),%rax
40060b:  add    $0x1,%rax
40060f:  mov    %rax,0x8(%rsp)
400614:  lea    -0x3(%rdi),%rdi
400618:  lea    0x8(%rsp),%rsi
40061d:  callq  0x4005f6 <magic>
400622:  add    %rbx,%rax
400625:  add    $0x10,%rsp
400629:  pop    %rbx
40062a:  retq
```

Suppose we call `magic` from `main()`, with registers `%rsi = 0x7ff…fbaa` and `%rdi = 6`. The value stored at address `0x7ff…fbaa` is the long value 2. We set a breakpoint at “return *y” (i.e. we are just about to return from `magic()` without making another recursive call). We have executed the `mov` instruction at `4005fc` but have not yet executed the `retq`.

**Fill in the register values on the next page and draw what the stack will look like when the program hits that breakpoint.** Give both a description of the item stored at that location and the value stored at that location. If a location on the stack is not used, write “unused” in the Description for that address and put “-----” for its Value. You may list the Values in hex or decimal. Unless preceded by `0x` we will assume decimal. It is fine to use `f…f` for sequences of `f`’s as shown above for `%rsi`. Add more rows to the table as needed. Also, fill in the box on the next page to include the value this call to `magic` will finally return to `main`. 

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<table>
<thead>
<tr>
<th>Register</th>
<th>Original Value</th>
<th>Value at Breakpoint</th>
</tr>
</thead>
<tbody>
<tr>
<td>rsp</td>
<td>0x7ff...ffad0</td>
<td></td>
</tr>
<tr>
<td>rdi</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>rsi</td>
<td>0x7ff...ffbaa</td>
<td></td>
</tr>
<tr>
<td>rbx</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>rax</td>
<td>8</td>
<td></td>
</tr>
</tbody>
</table>

Don’t forget: What value is **finally** returned to `main` by this call?

<table>
<thead>
<tr>
<th>Memory address on stack</th>
<th>Name / description of item</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x7fffffffffffffffad0</td>
<td>Return address back to main</td>
<td>0x400827</td>
</tr>
<tr>
<td>0x7fffffffffffffffac8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffac0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffab8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffab0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffba8</td>
<td></td>
<td></td>
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<tr>
<td>0x7fffffffffffffffba0</td>
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<td></td>
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<td>0x7fffffffffffffffba98</td>
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<td>0x7fffffffffffffffba90</td>
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<td>0x7fffffffffffffffba88</td>
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<td>0x7fffffffffffffffba80</td>
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<td>0x7fffffffffffffffba70</td>
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<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffba68</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0x7fffffffffffffffba60</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
REFERENCES

Powers of 2:

| $2^0$ = 1 | $2^{-1} = .5$ |
| $2^1$ = 2 | $2^{-2} = .25$ |
| $2^2$ = 4 | $2^{-3} = .125$ |
| $2^3$ = 8 | $2^{-4} = .0625$ |
| $2^9$ = 16 | $2^{-5} = .03125$ |
| $2^{10}$ = 32 | $2^{-6} = .015625$ |
| $2^{11}$ = 64 | $2^{-7} = .0078125$ |
| $2^{12}$ = 128 | $2^{-8} = .00390625$ |
| $2^{13}$ = 256 | $2^{-9} = .001953125$ |
| $2^{14}$ = 512 | $2^{-10} = .0009765625$ |
| $2^{15}$ = 1024 | $2^{-11} = .00048828125$ |

Assembly Code Instructions:

- **push**: push a value onto the stack and decrement the stack pointer
- **pop**: pop a value from the stack and increment the stack pointer
- **call**: jump to a procedure after first pushing a return address onto the stack
- **ret**: pop return address from stack and jump there
- **mov**: move a value between registers and memory
- **lea**: compute effective address and store in a register
- **add**: add src (1st operand) to dst (2nd) with result stored in dst (2nd)
- **sub**: subtract src (1st operand) from dst (2nd) with result stored in dst (2nd)
- **and**: bit-wise AND of src and dst with result stored in dst
- **or**: bit-wise OR of src and dst with result stored in dst
- **sar**: shift data in the dst to the right (arithmetic shift) by the number of bits specified in 1st operand
- **sal**: shift data in the dst to the left (arithmetic shift) by the number of bits specified in 1st operand
- **jmp**: jump to address
- **jg**: conditional jump to address if greater than
- **jle**: conditional jump to address if less than or equal
- **jne**: conditional jump to address if zero flag is not set
- **cmp**: subtract src (1st operand) from dst (2nd) and set flags
- **test**: bit-wise AND src and dst and set flags
Register map for x86-64:

Note: all registers are caller-saved except those explicitly marked as callee-saved, namely, rbx, rbp, r12, r13, r14, and r15. rsp is a special register.

<table>
<thead>
<tr>
<th>Register</th>
<th>Description</th>
<th>Register</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>%rax</td>
<td>Return value</td>
<td>%r8</td>
<td>Argument #5</td>
</tr>
<tr>
<td>%rbx</td>
<td>Callee saved</td>
<td>%r9</td>
<td>Argument #6</td>
</tr>
<tr>
<td>%rcx</td>
<td>Argument #4</td>
<td>%r10</td>
<td>Caller saved</td>
</tr>
<tr>
<td>%rdx</td>
<td>Argument #3</td>
<td>%r11</td>
<td>Caller Saved</td>
</tr>
<tr>
<td>%rsi</td>
<td>Argument #2</td>
<td>%r12</td>
<td>Callee saved</td>
</tr>
<tr>
<td>%rdi</td>
<td>Argument #1</td>
<td>%r13</td>
<td>Callee saved</td>
</tr>
<tr>
<td>%rsp</td>
<td>Stack pointer</td>
<td>%r14</td>
<td>Callee saved</td>
</tr>
<tr>
<td>%rbp</td>
<td>Callee saved</td>
<td>%r15</td>
<td>Callee saved</td>
</tr>
</tbody>
</table>