Q1. The fact that two computers implement the same Instruction Set Architecture means that
A. they are both made by the same manufacturer
B. they run at the same clock rate
C. they can execute a common set of instructions
D. they use the same set of peripheral devices
E. they have the same size cache memory

Q2. The fetch / execute cycle includes which of the following steps?
A. instruction fetch and instruction decode
B. data fetch and result return (aka data write)
C. instruction execution
D. Some, but not all, of A, B, and C
E. All of A, B, and C

Q3. The processor in a computer can:
A. read program instructions from memory
B. write data values to memory
C. read data values from memory
D. Some, but not all, of A, B, and C
E. All of A, B, and C

Q4. Peripheral devices are often connected to a computer using what piece of hardware?
A. daughterboard
B. CRT
C. ALU (Arithmetic / Logical Unit)
D. program counter
E. photolithography